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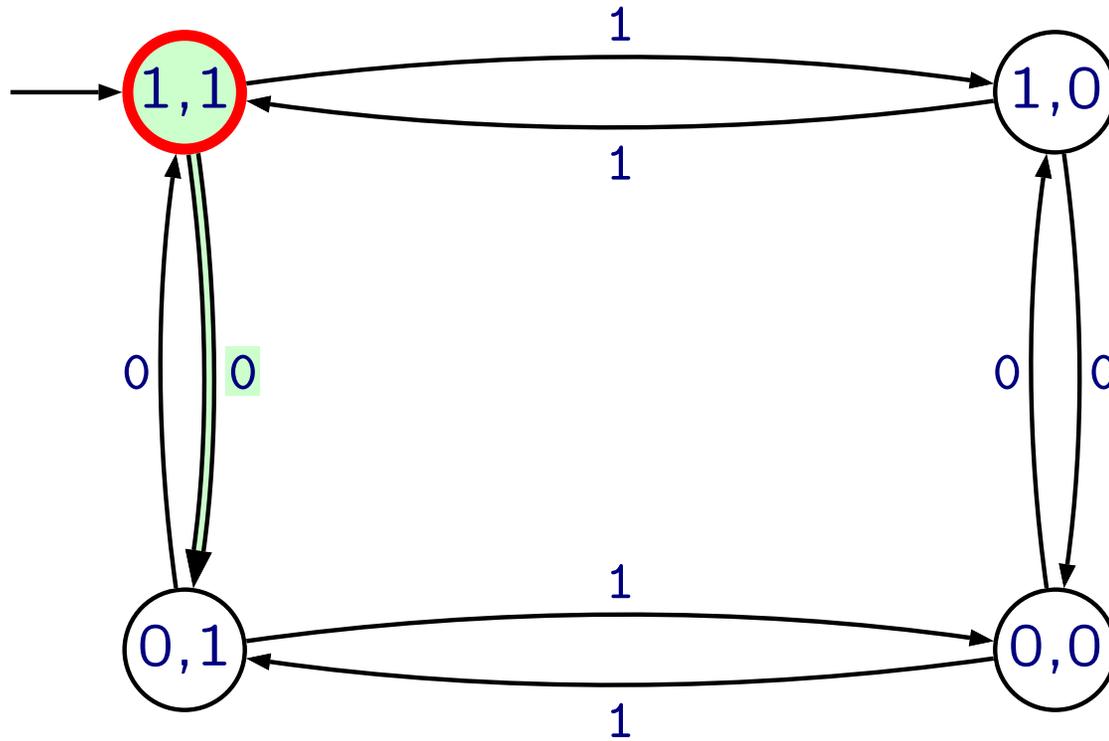
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state transition graph



parity test



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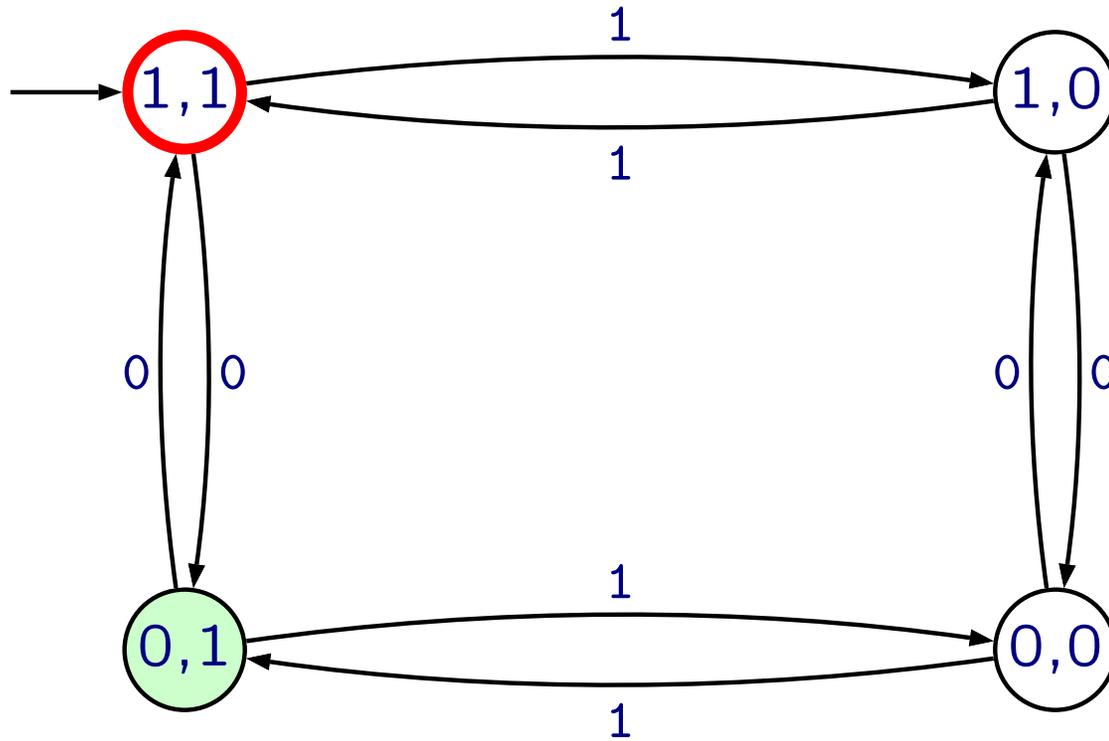
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state transition graph



parity test



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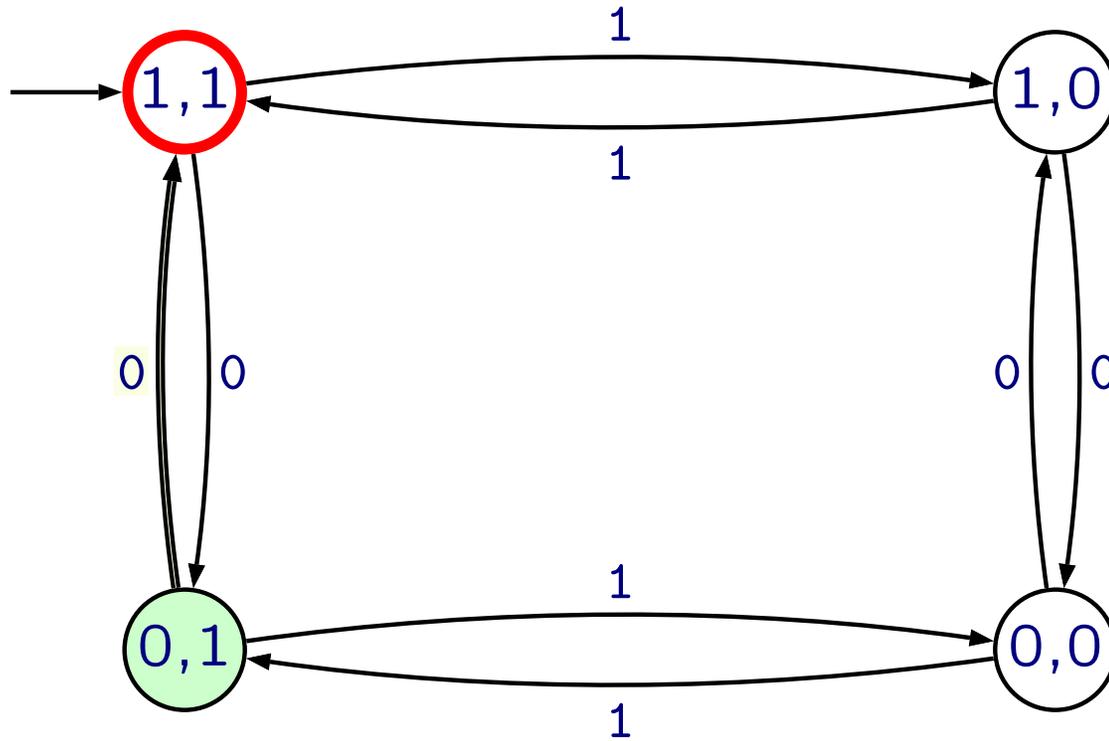
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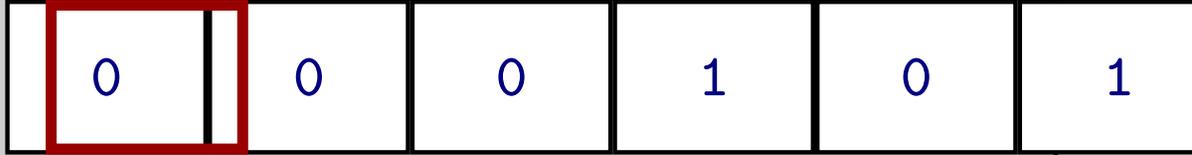


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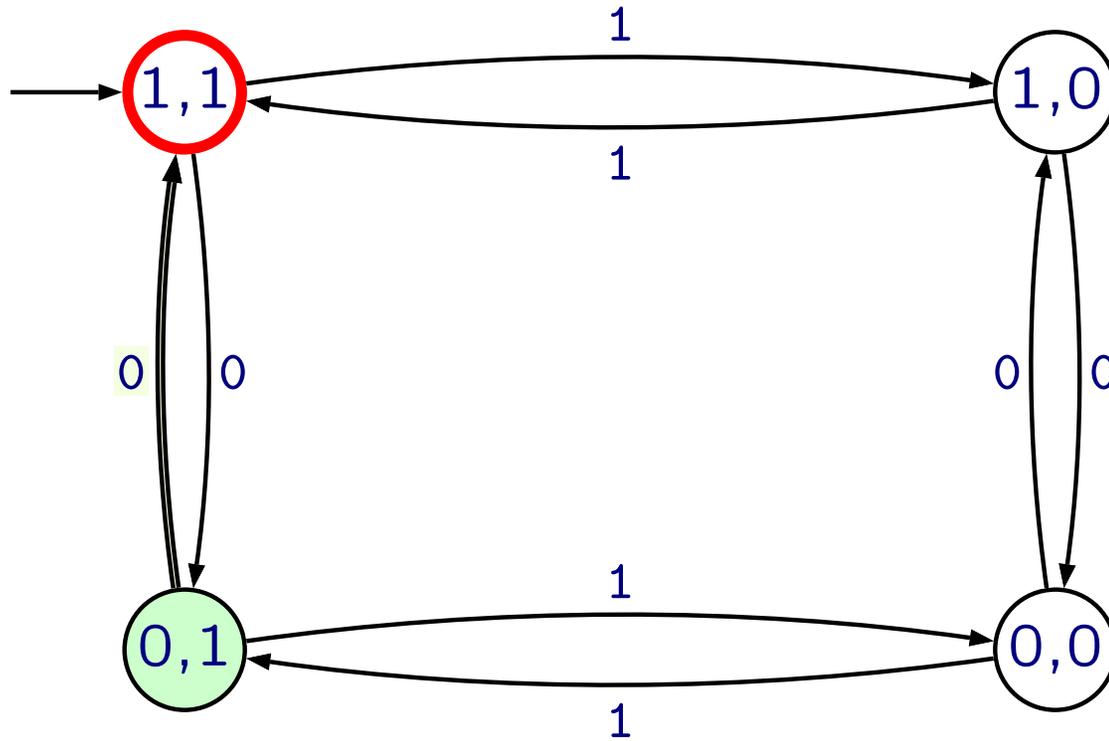


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state transition graph



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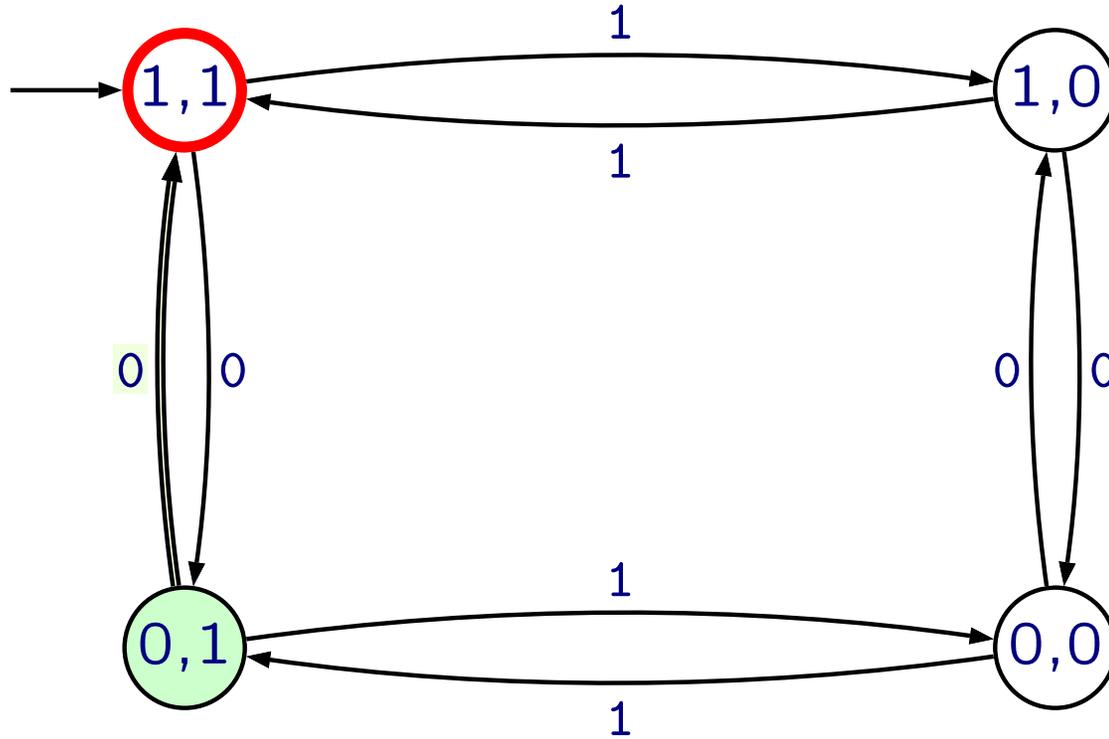


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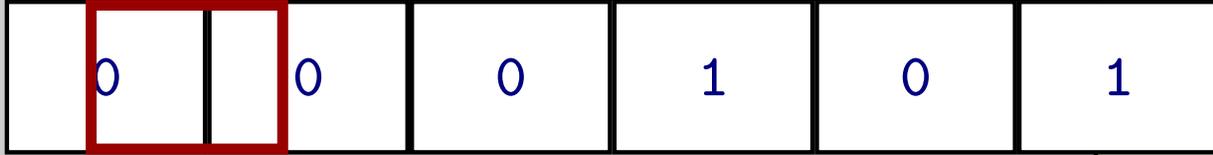


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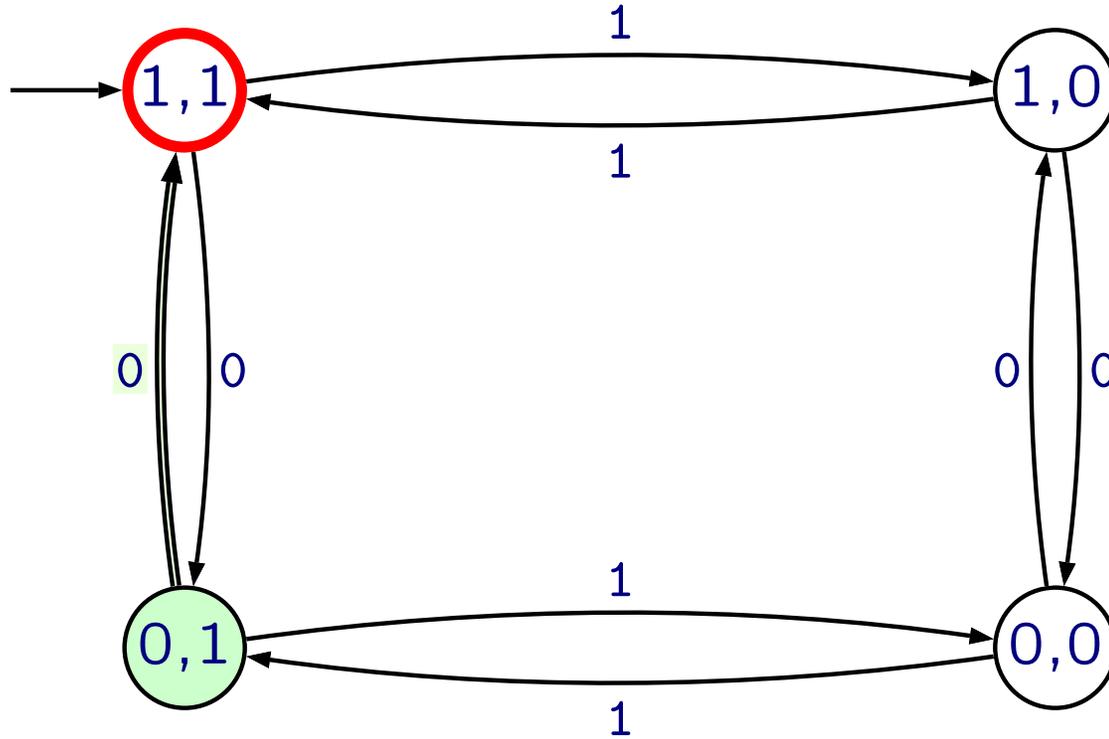


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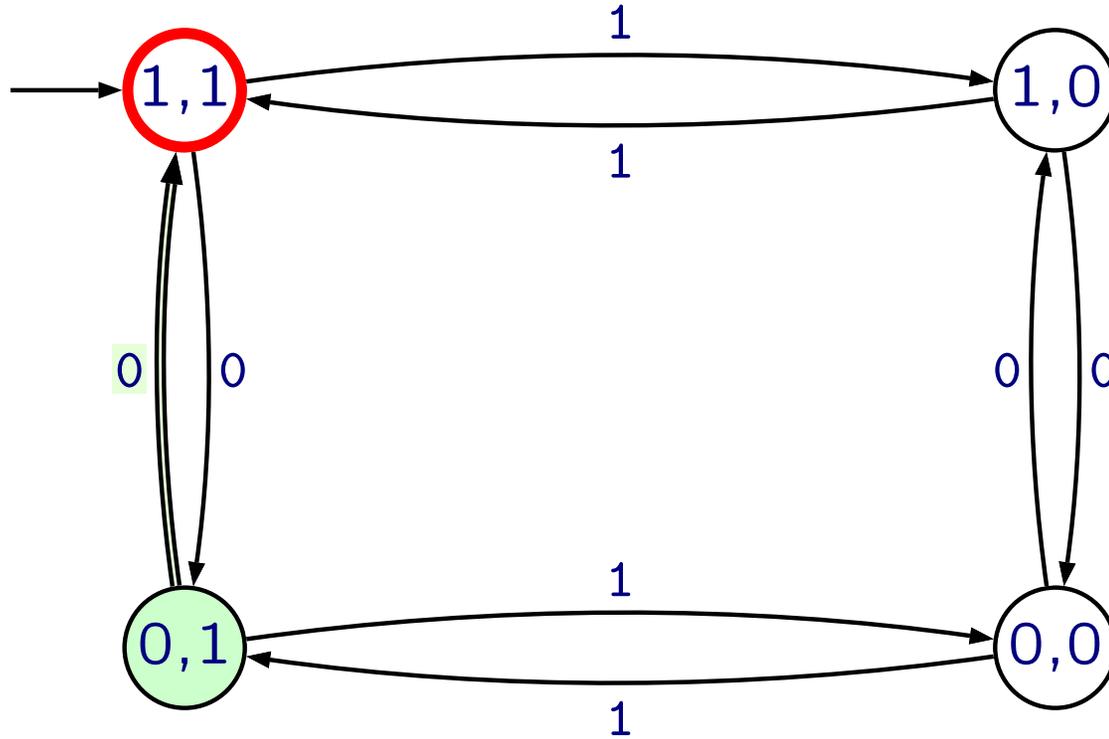


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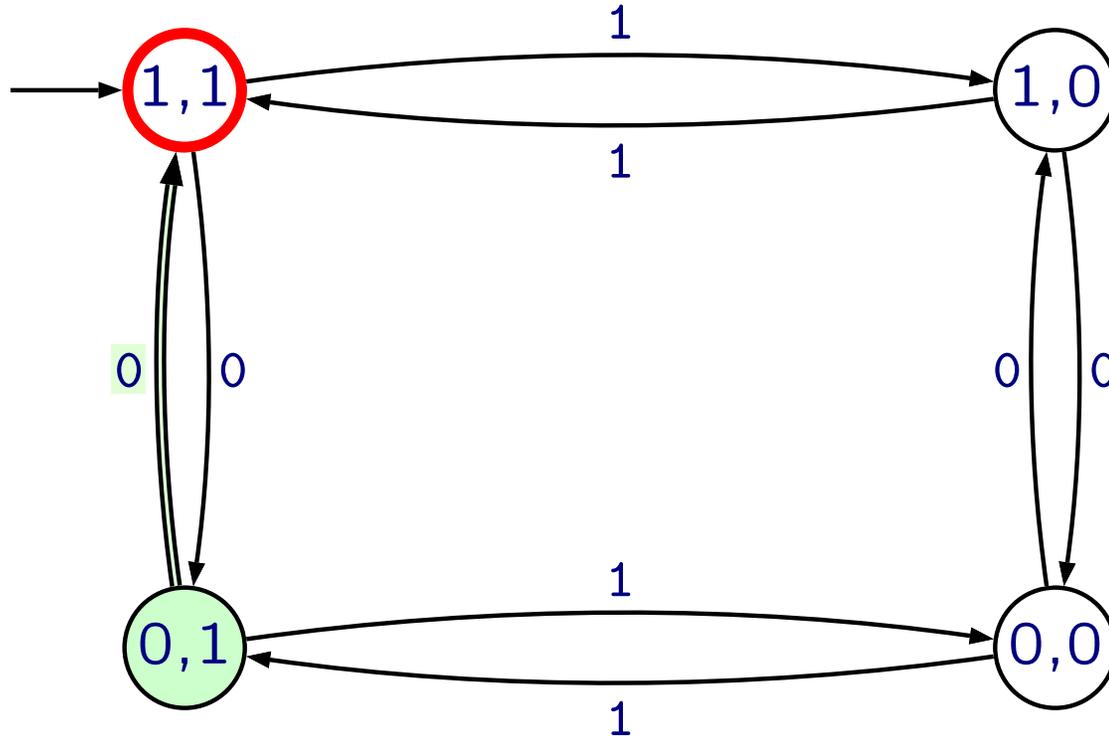


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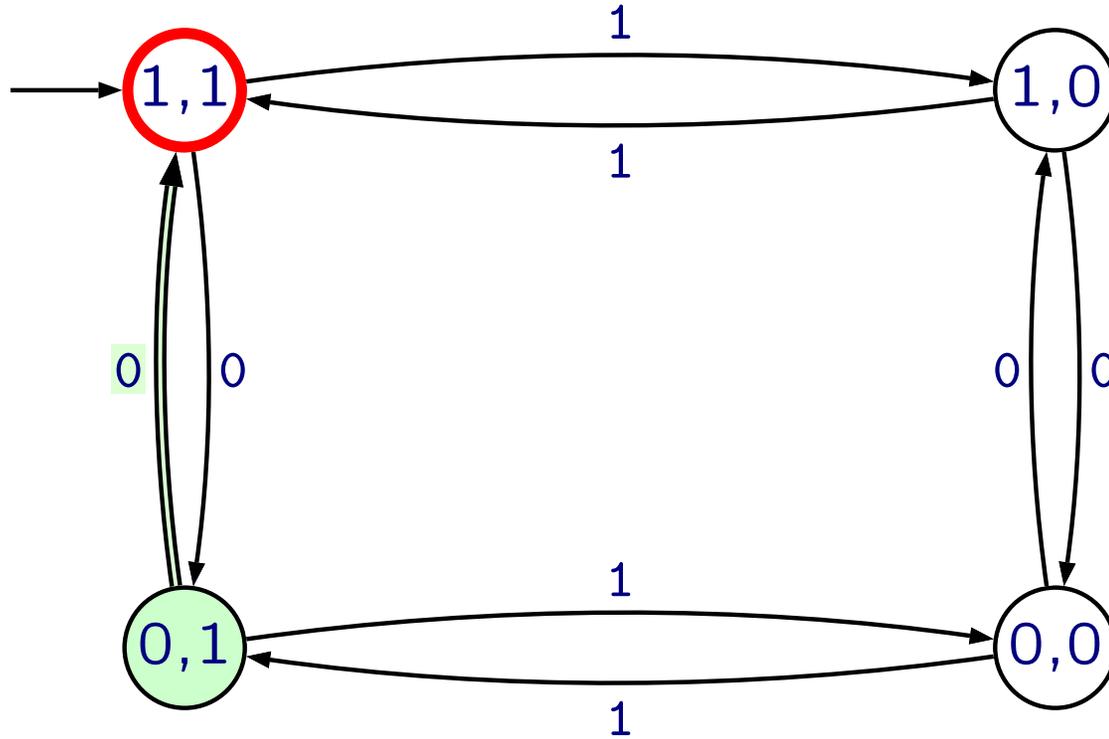


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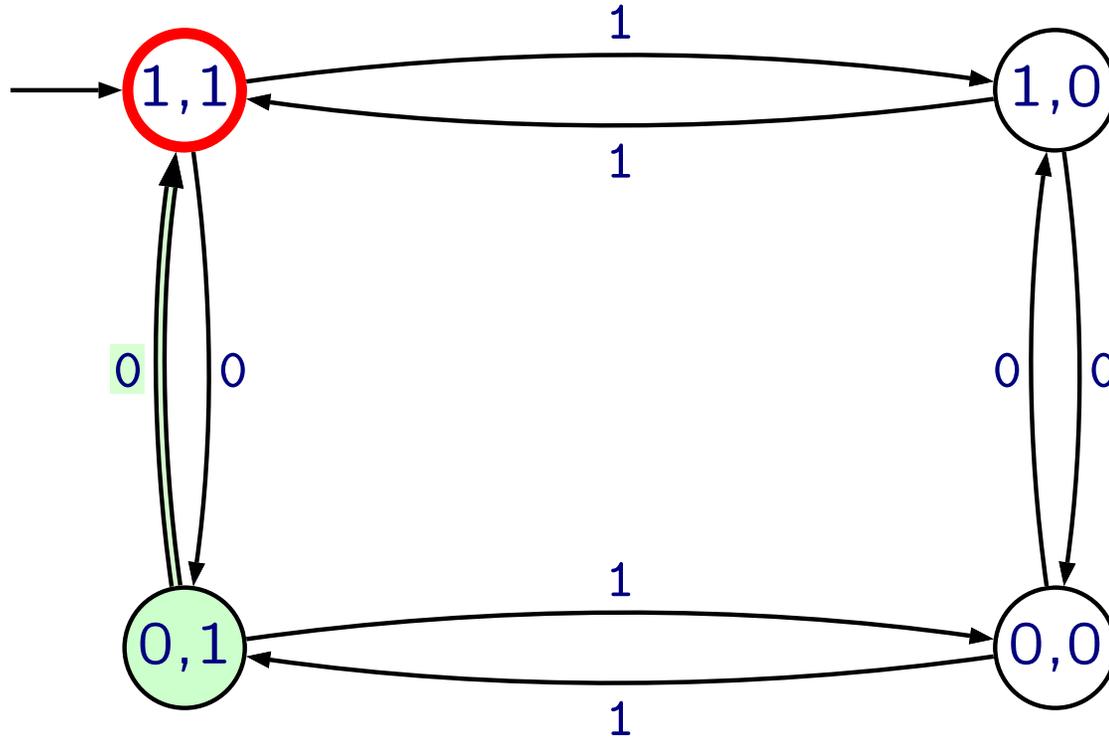


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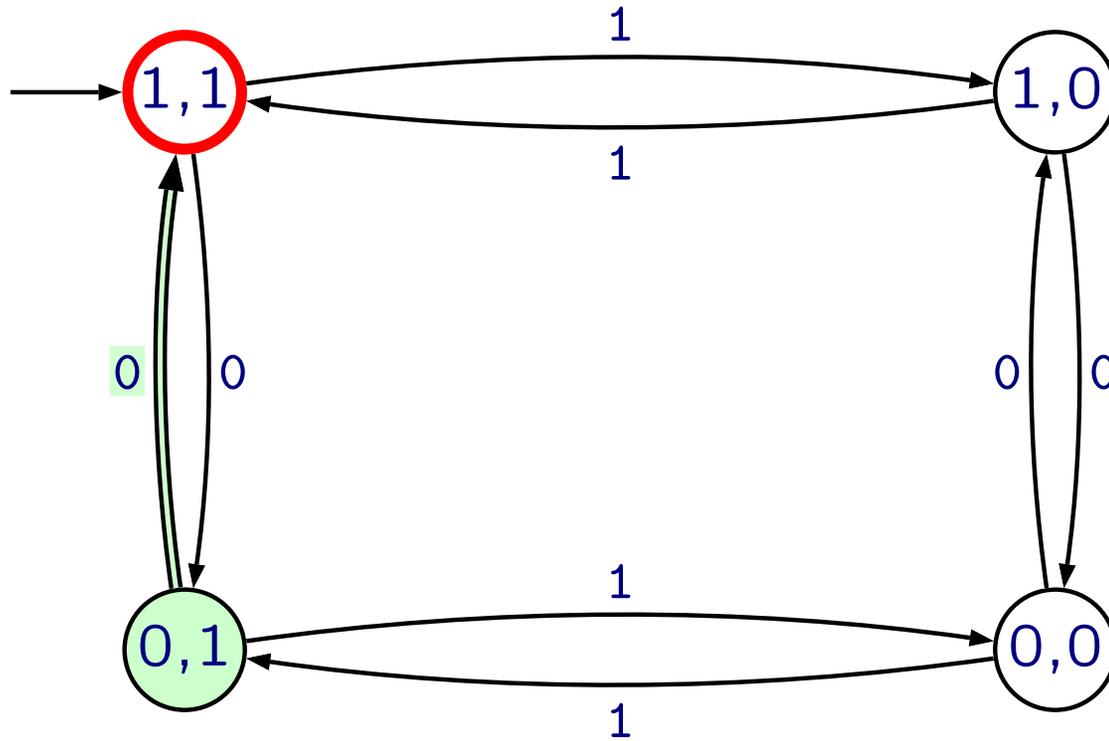


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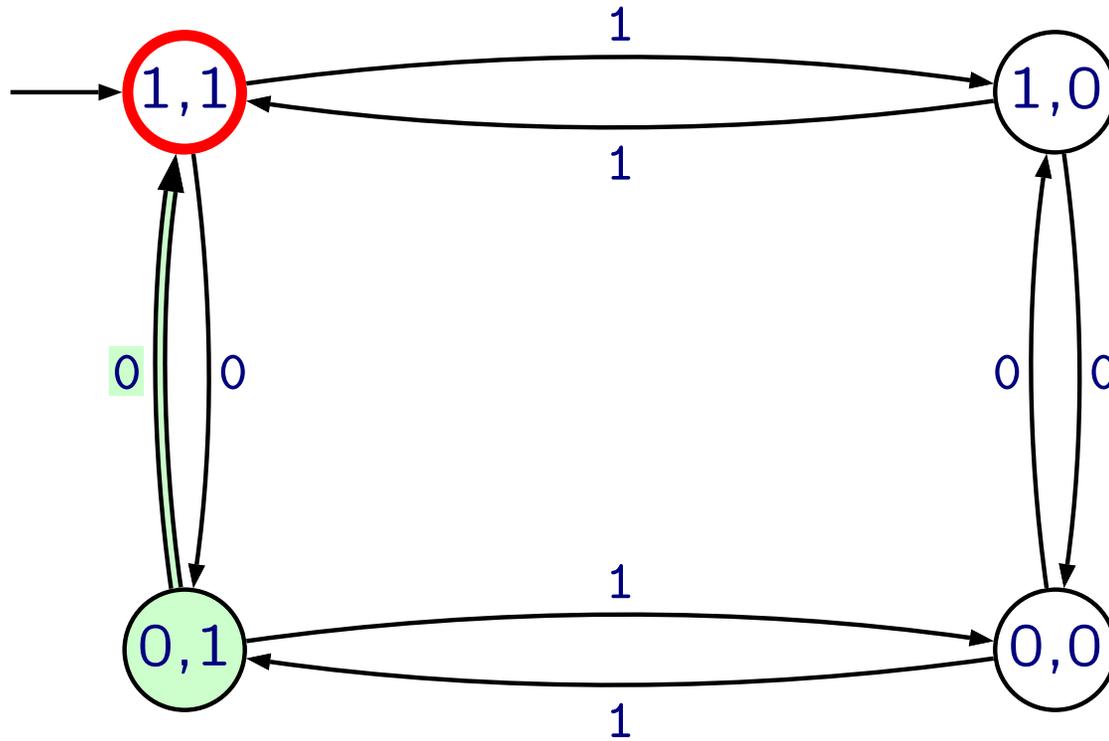
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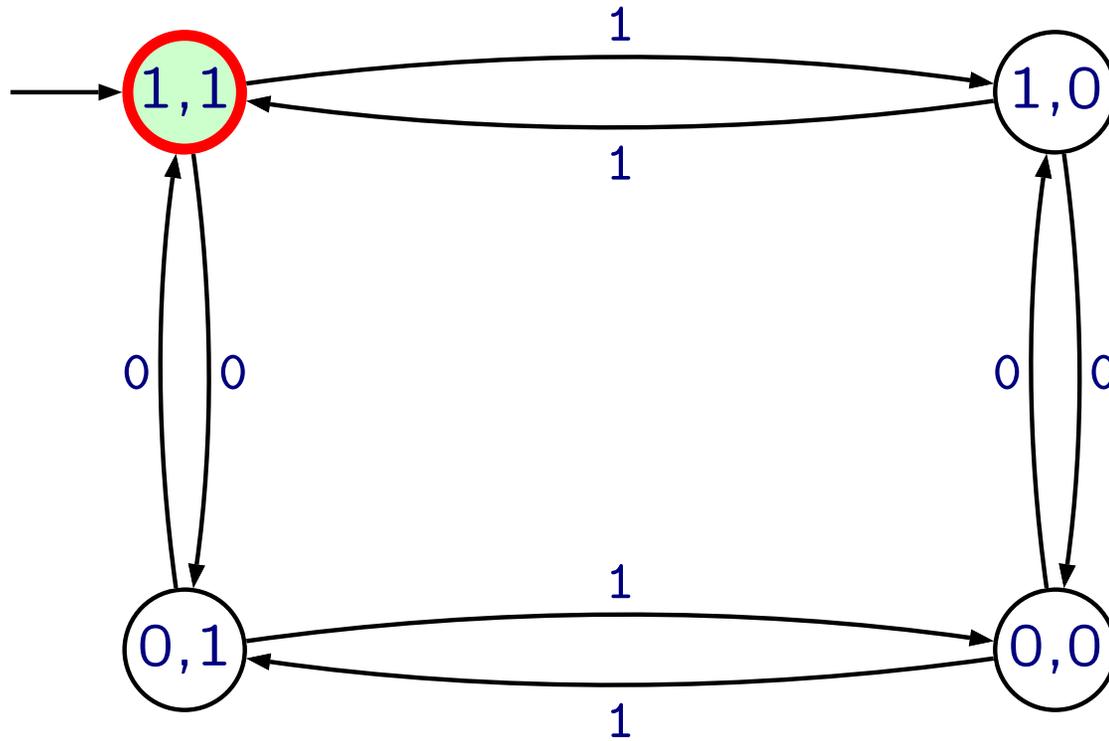
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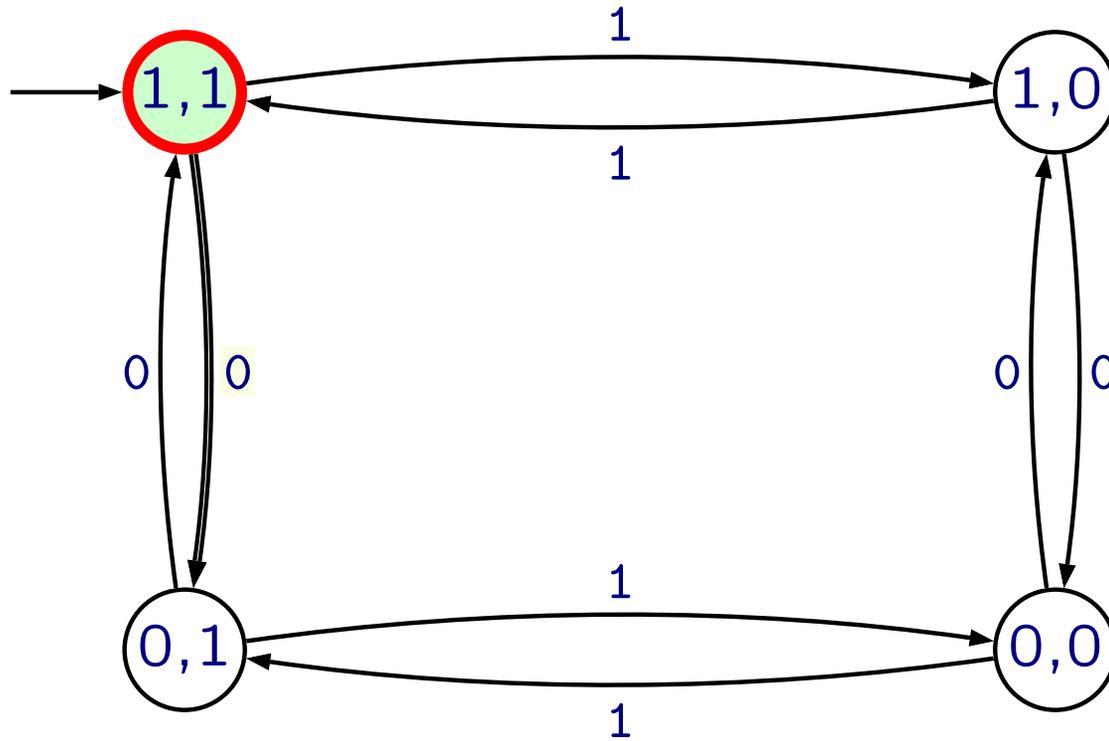


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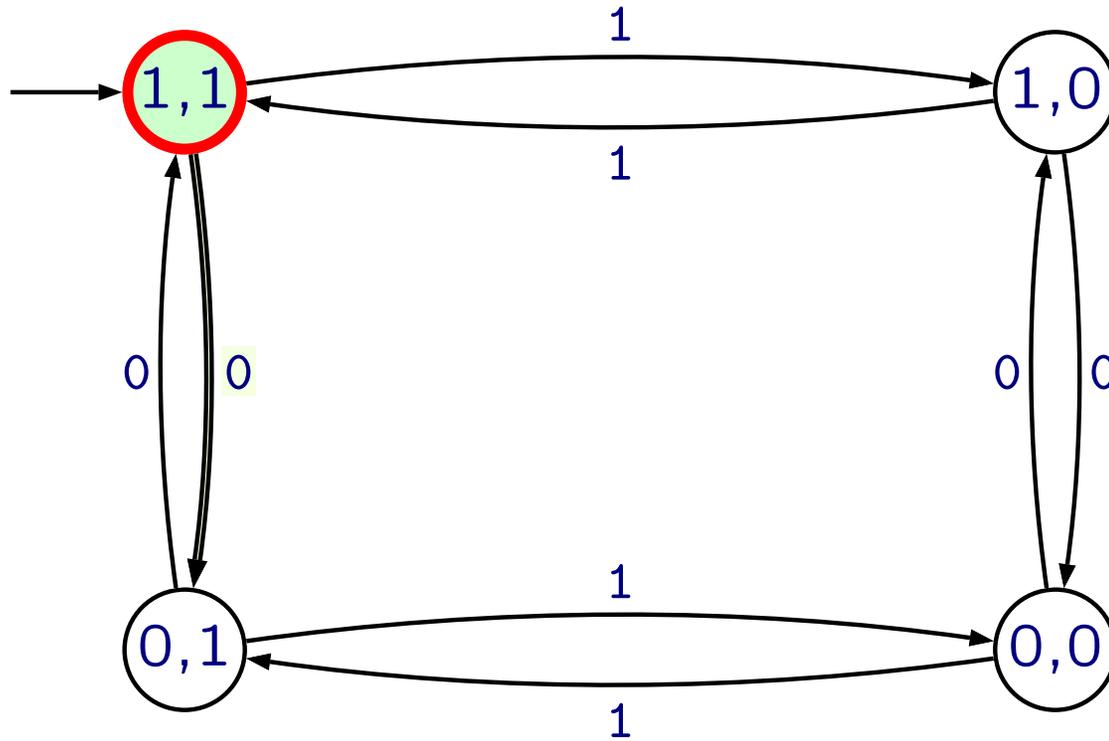


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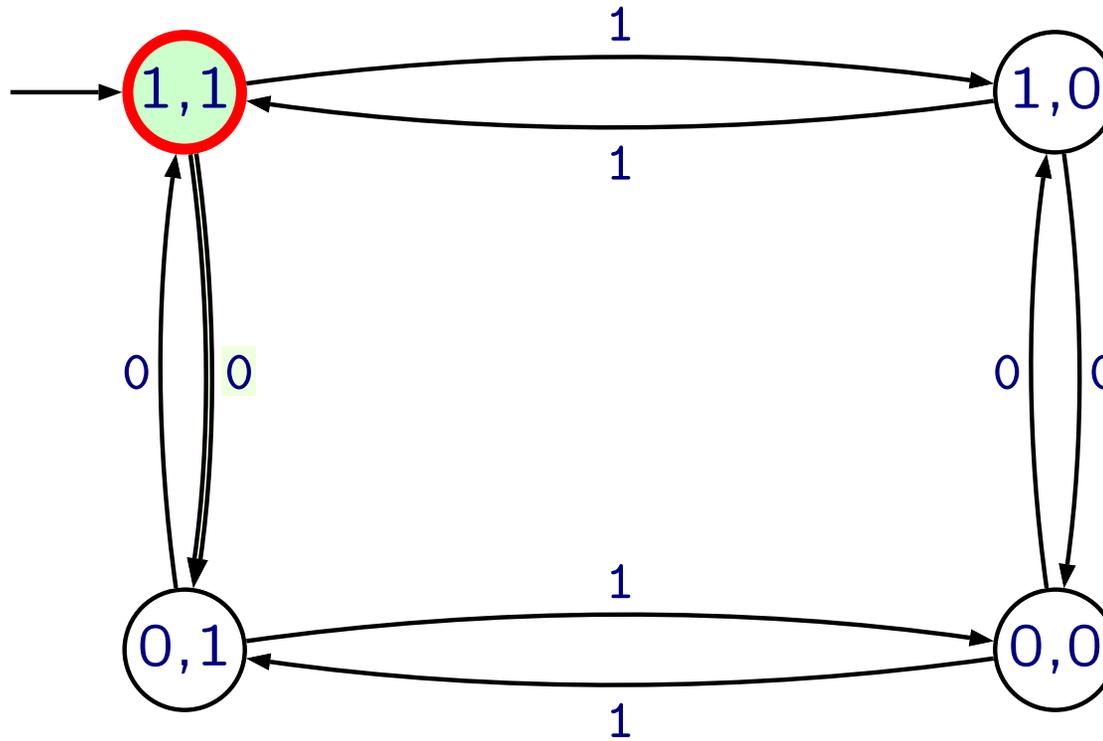


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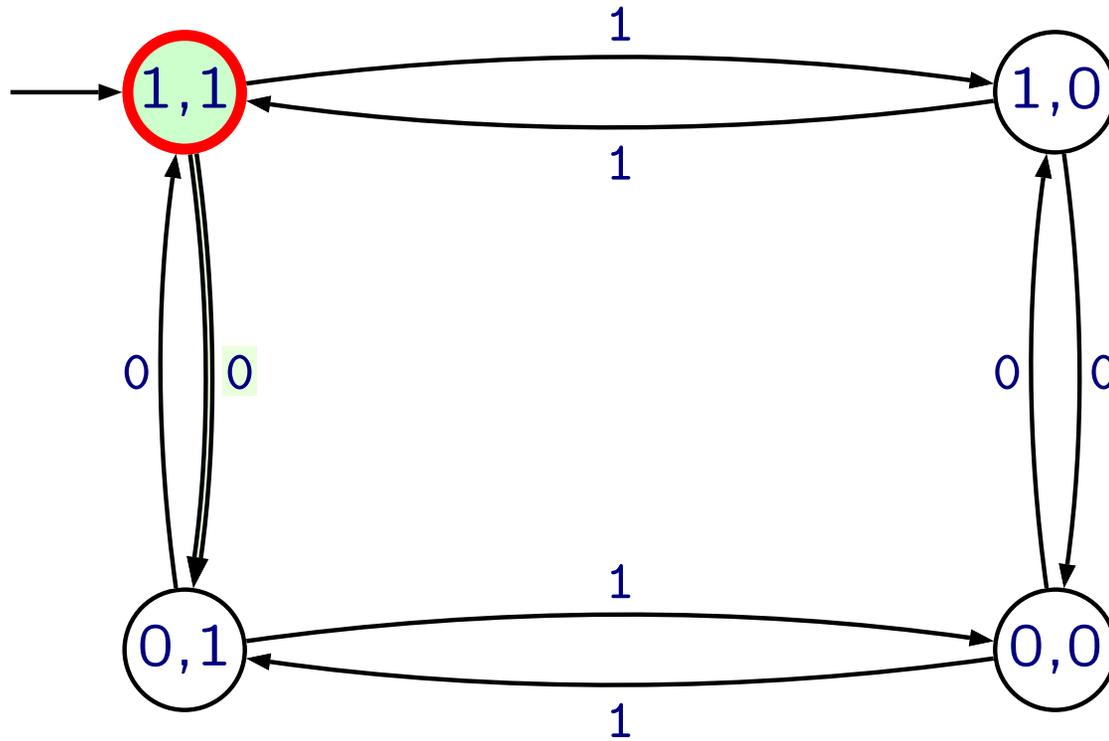


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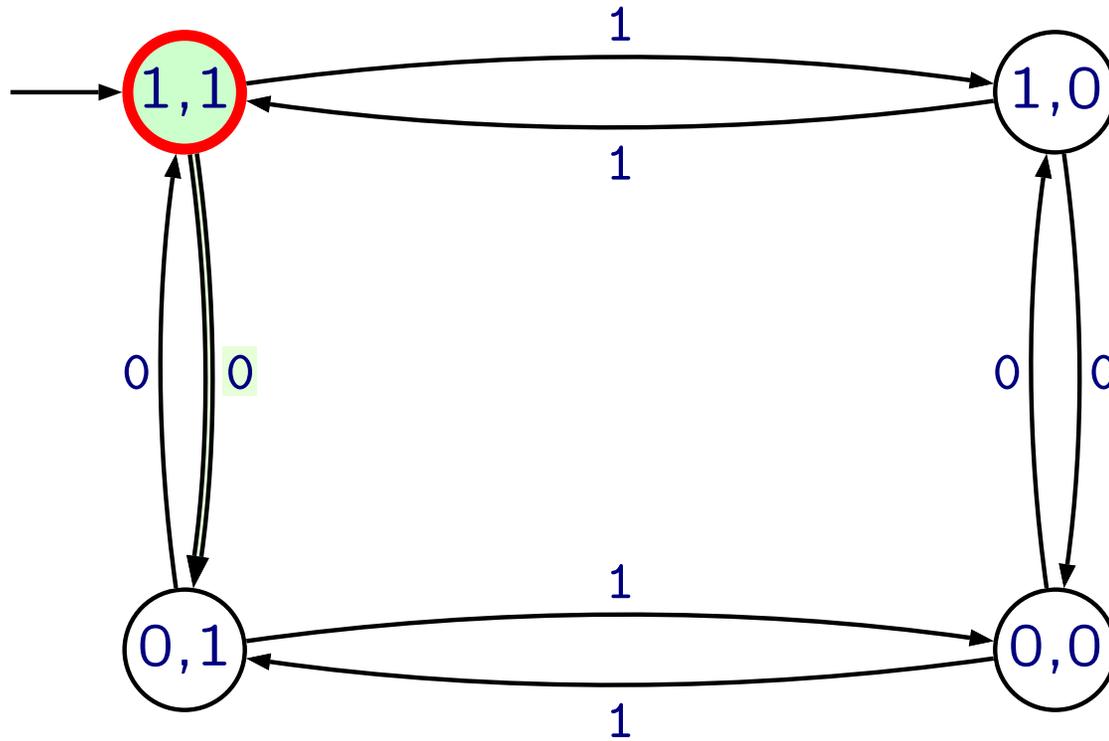


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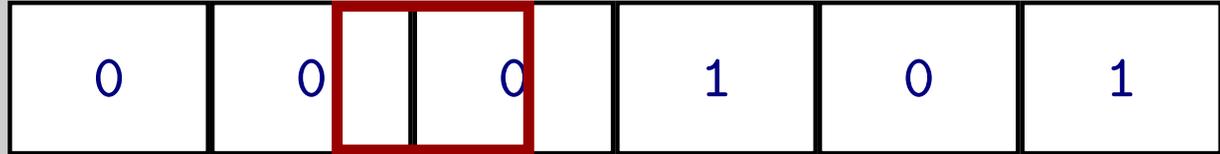


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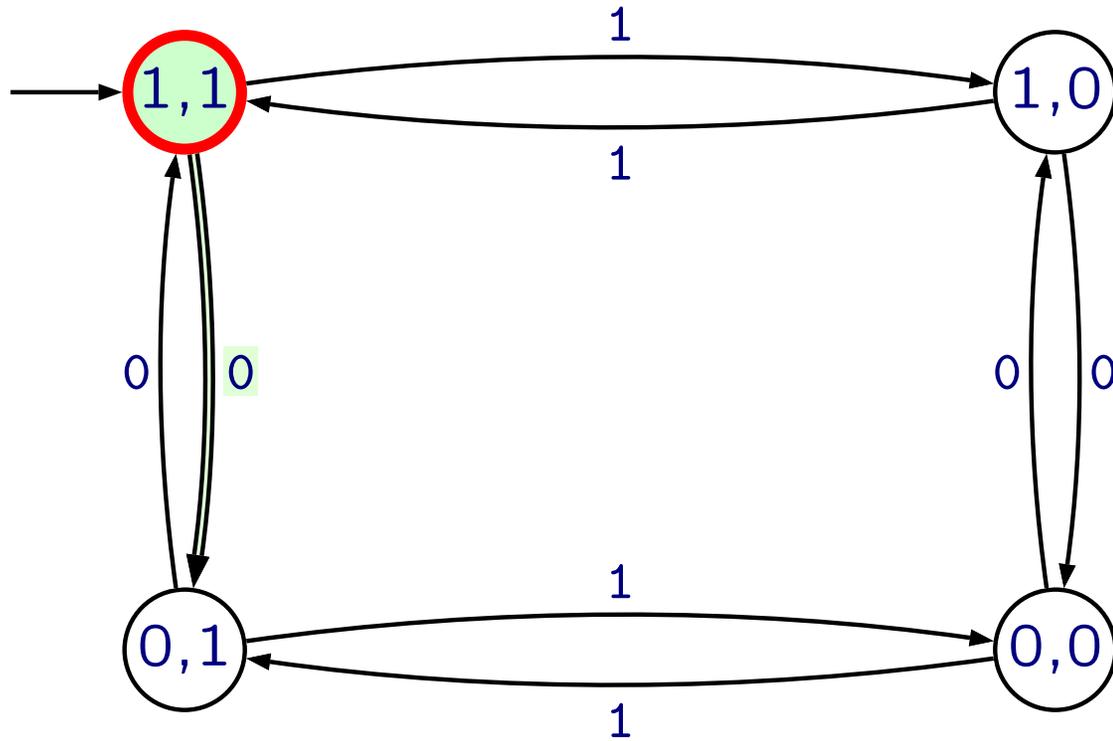


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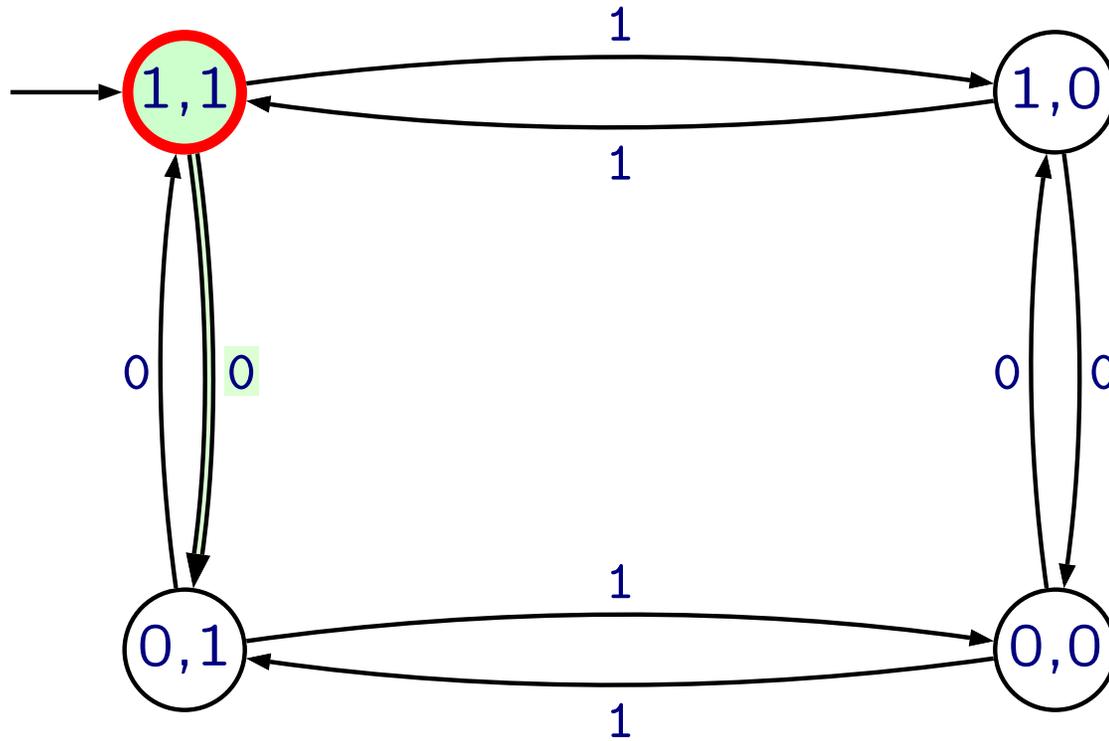


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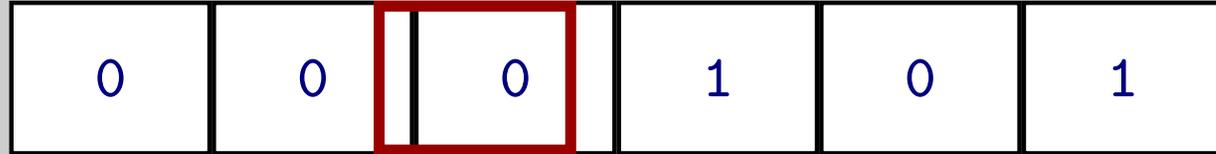


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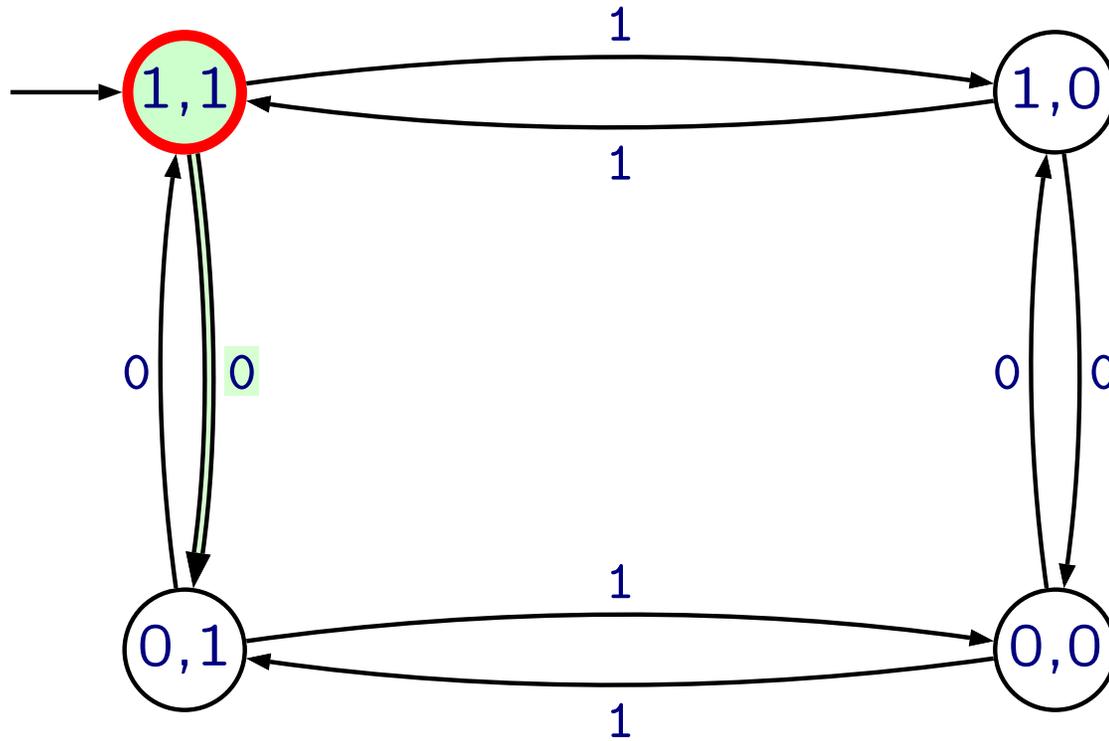


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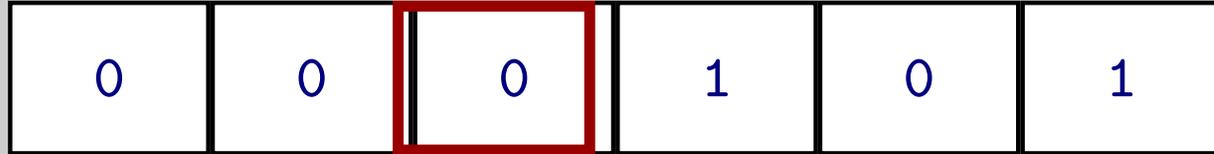


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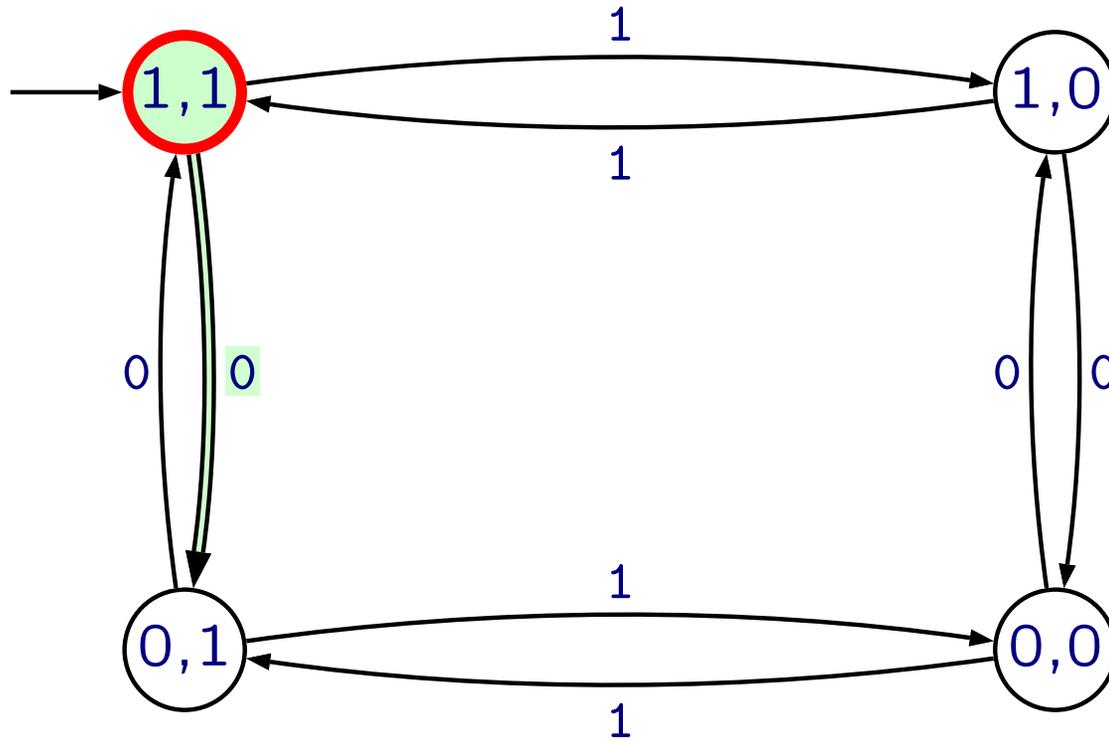


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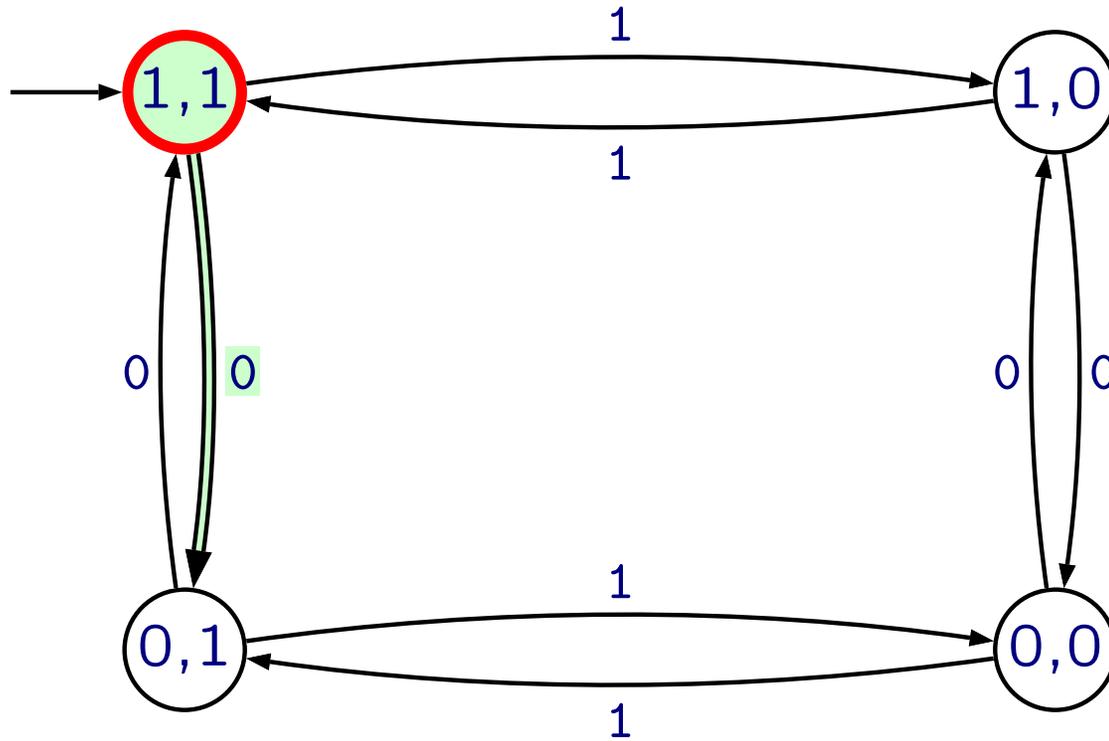
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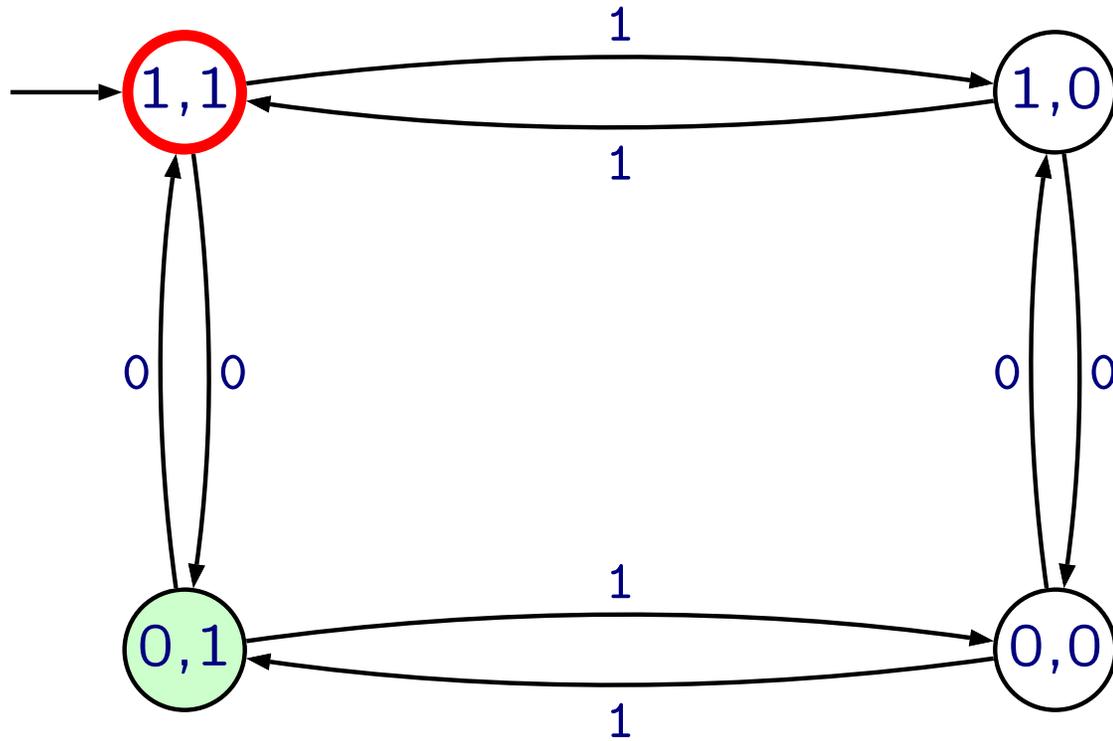
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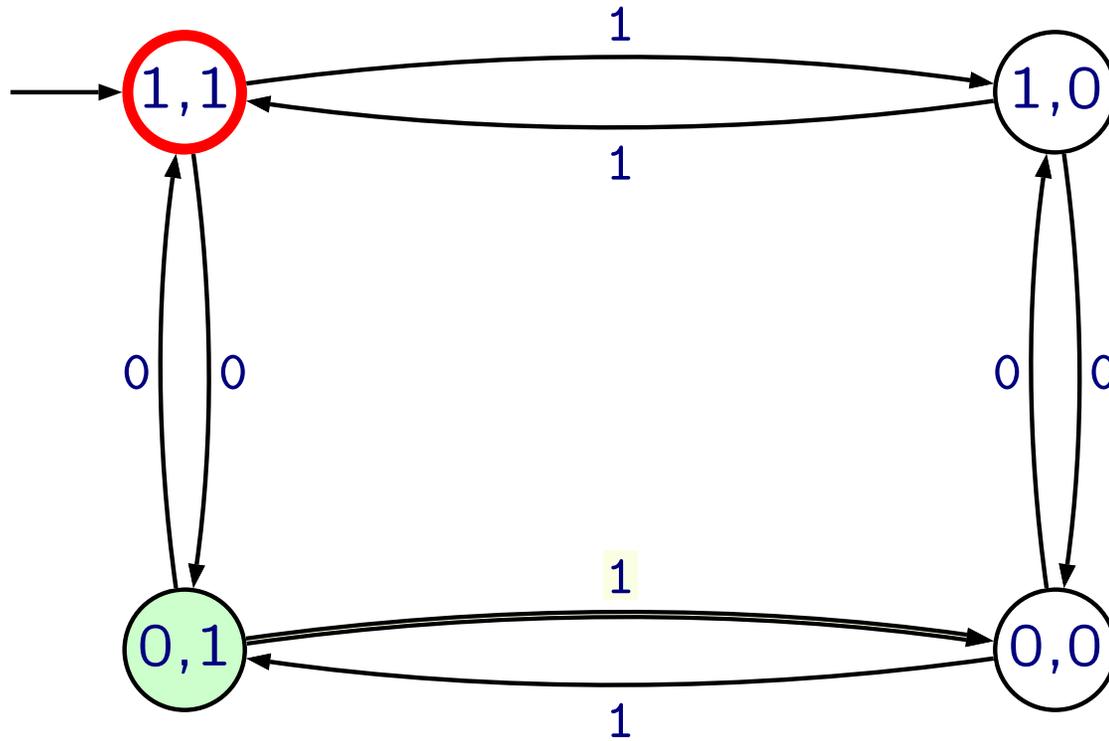
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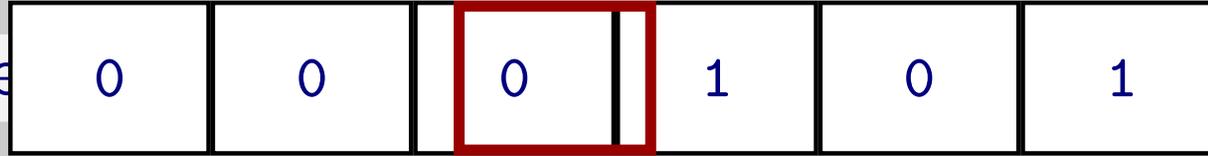


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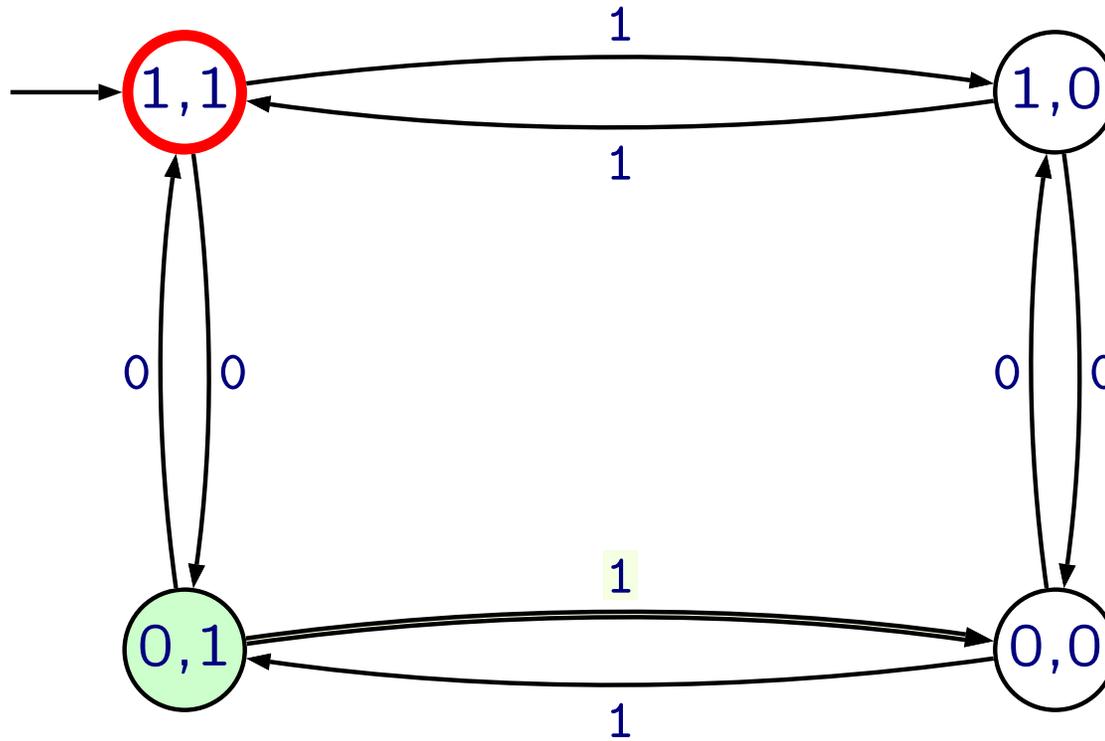


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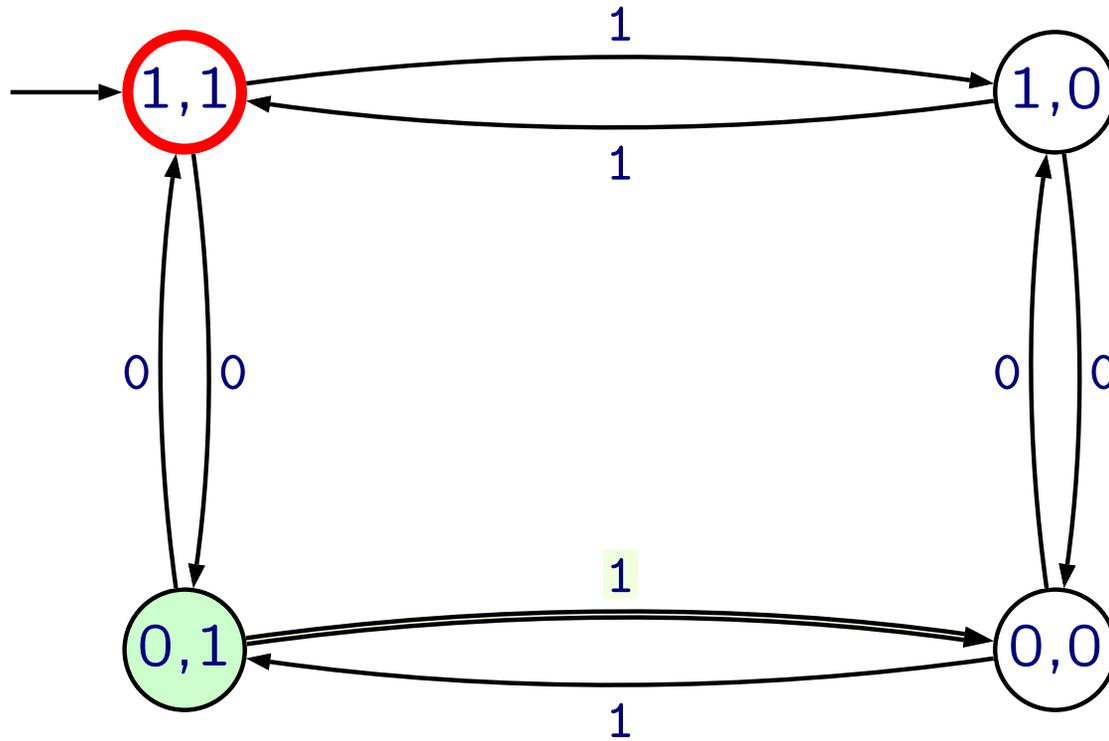
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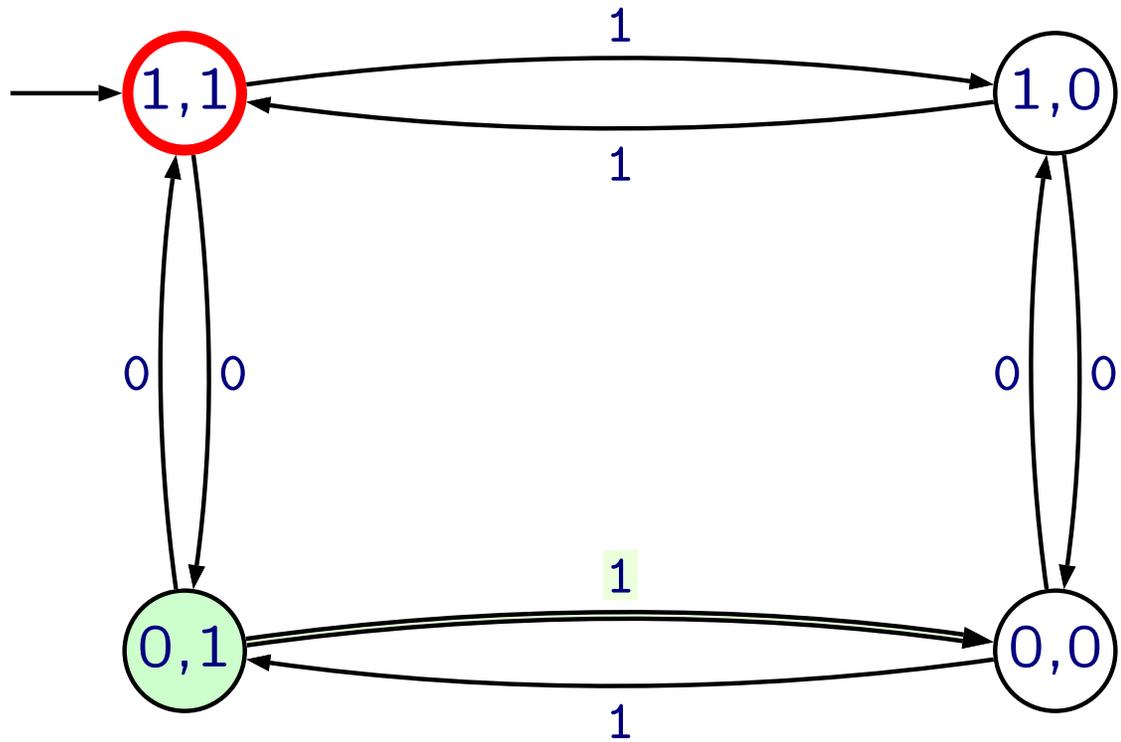
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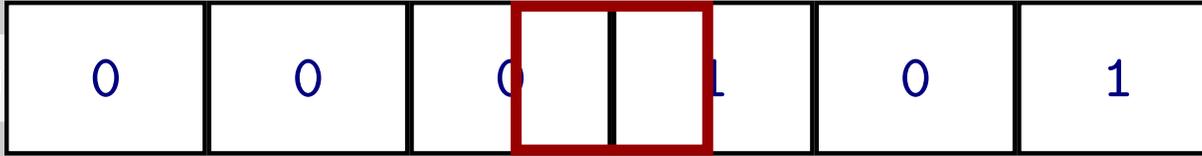


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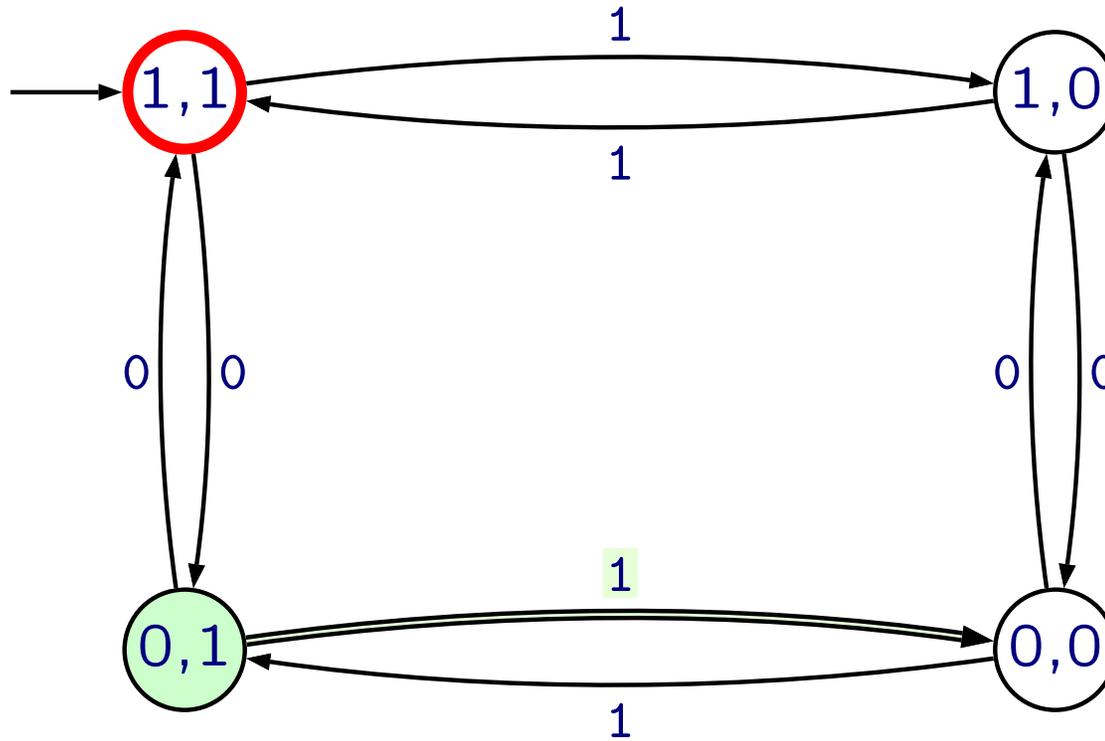


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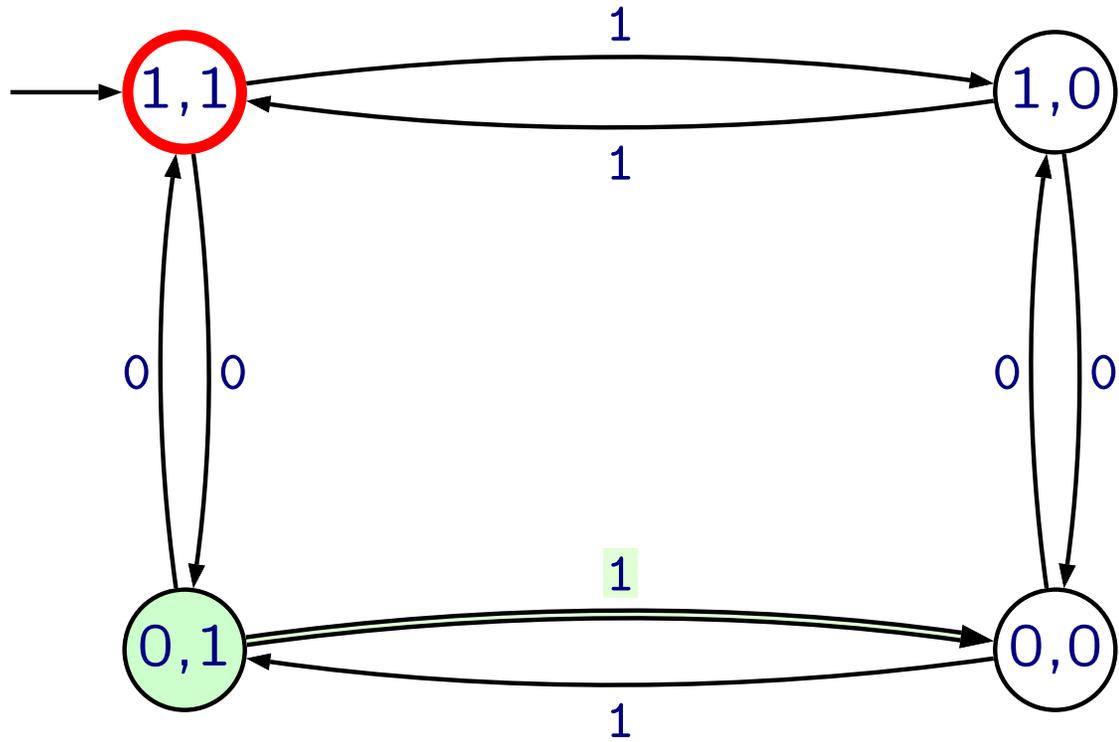
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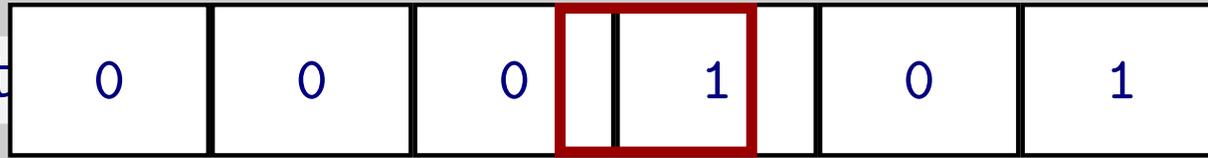


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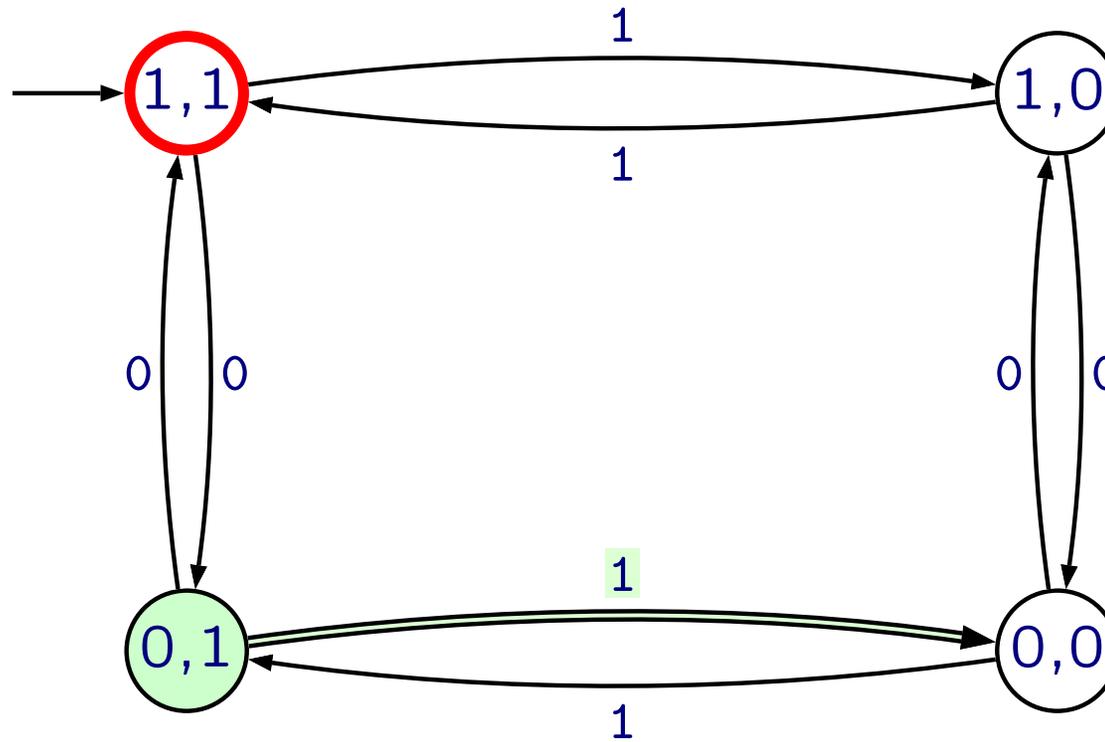


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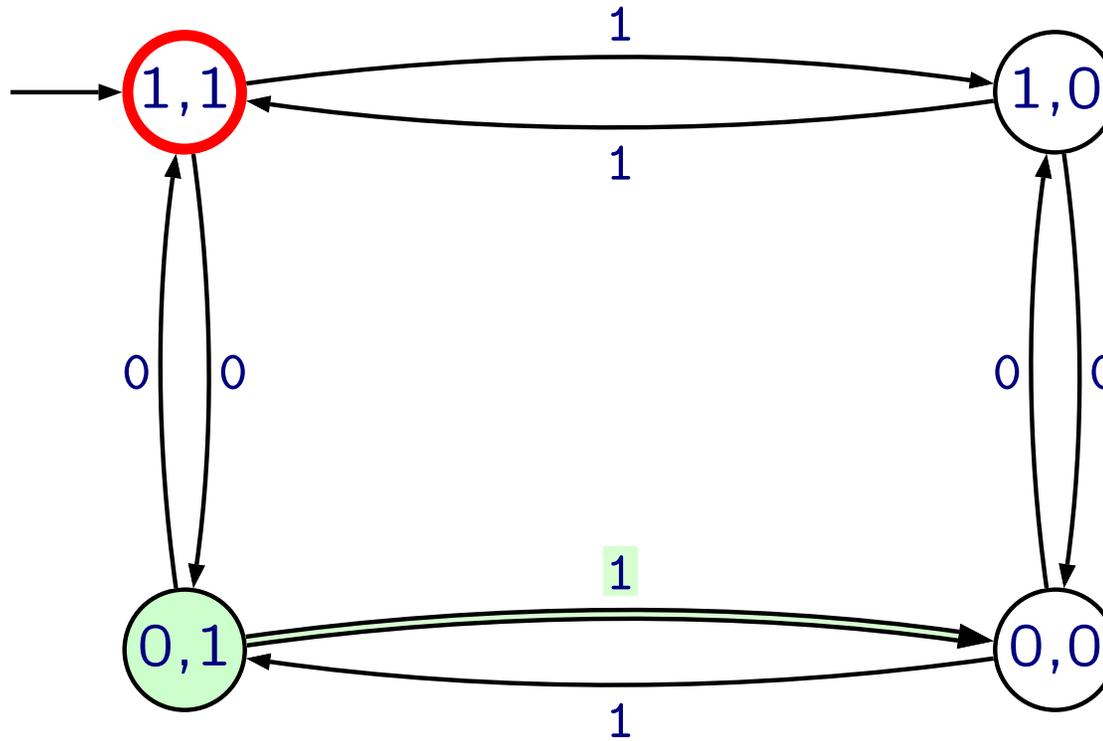


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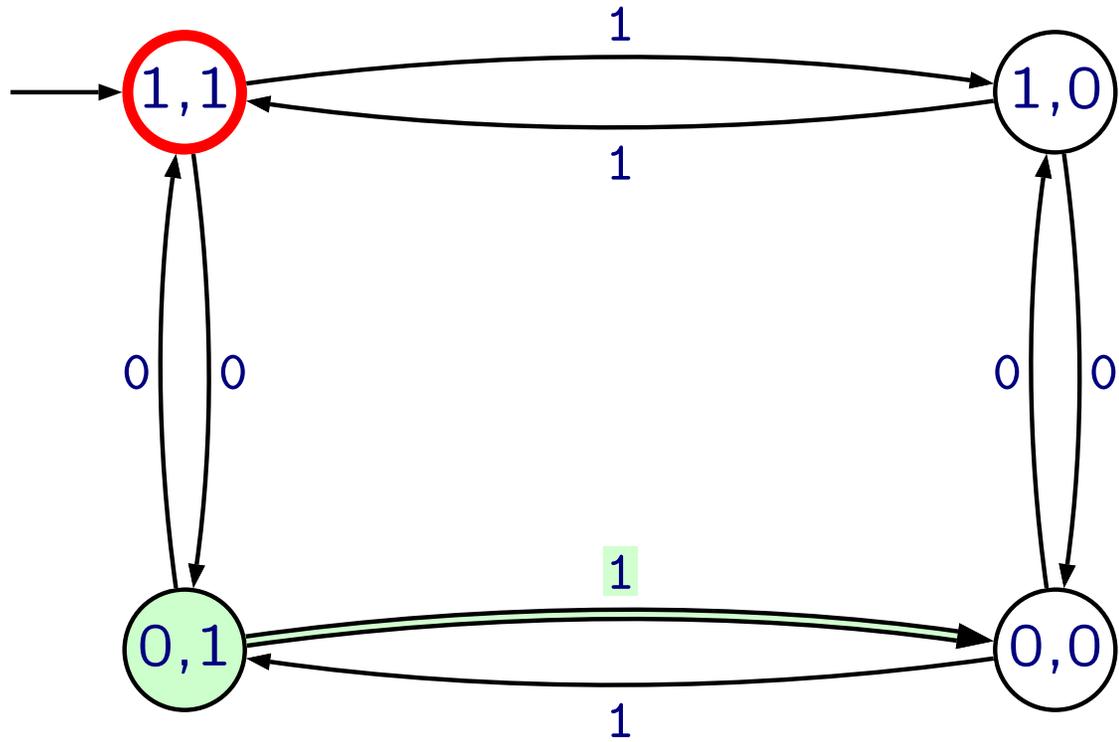
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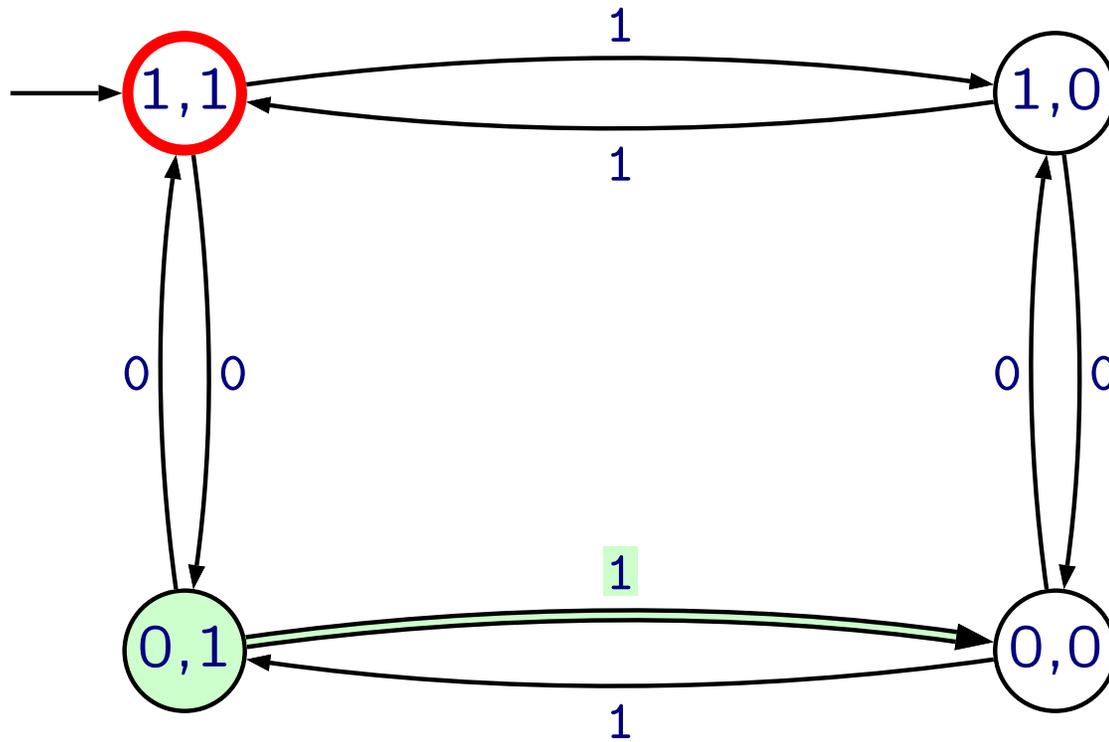


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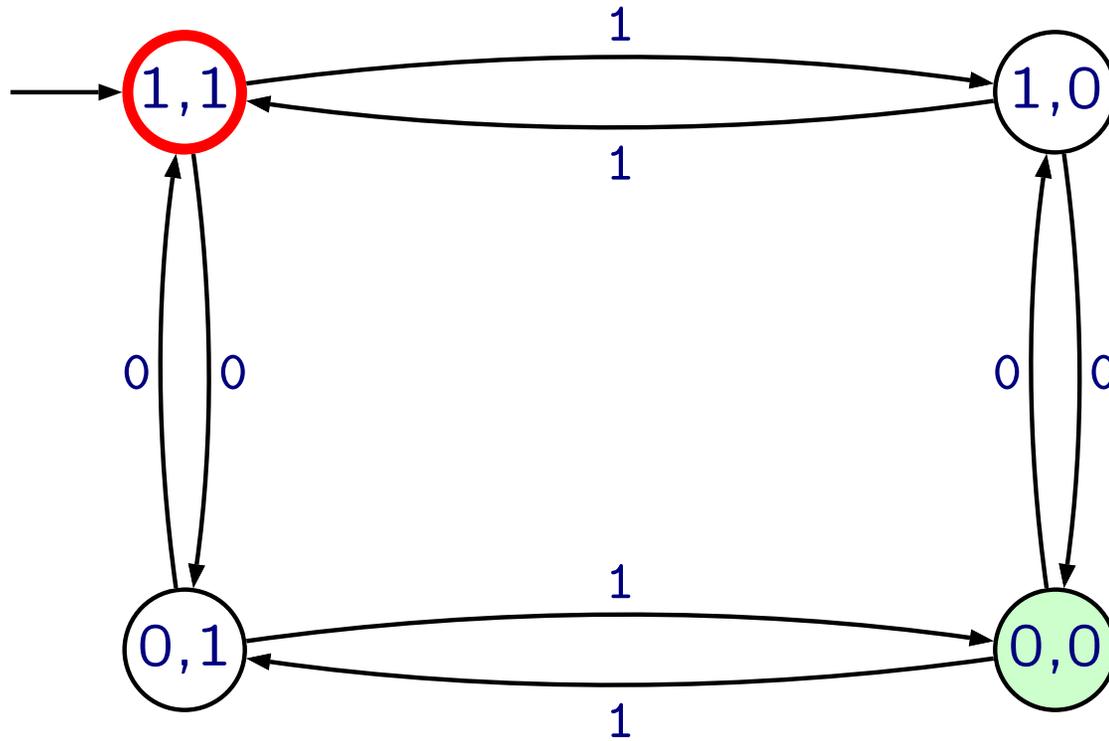


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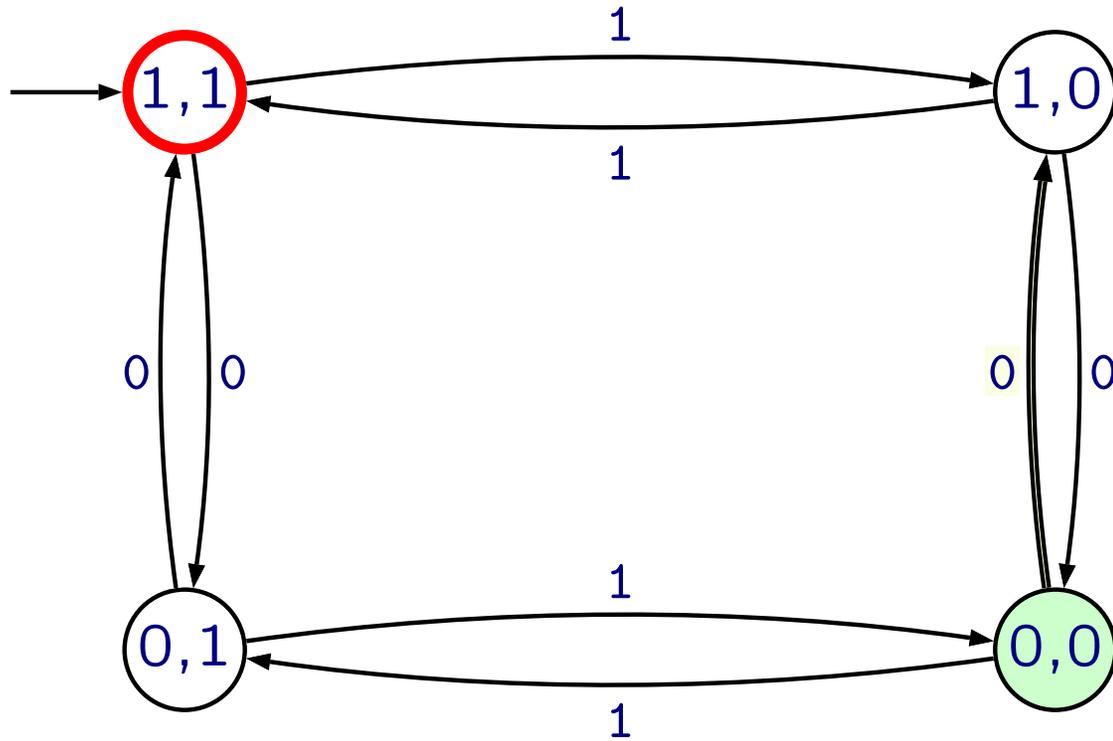
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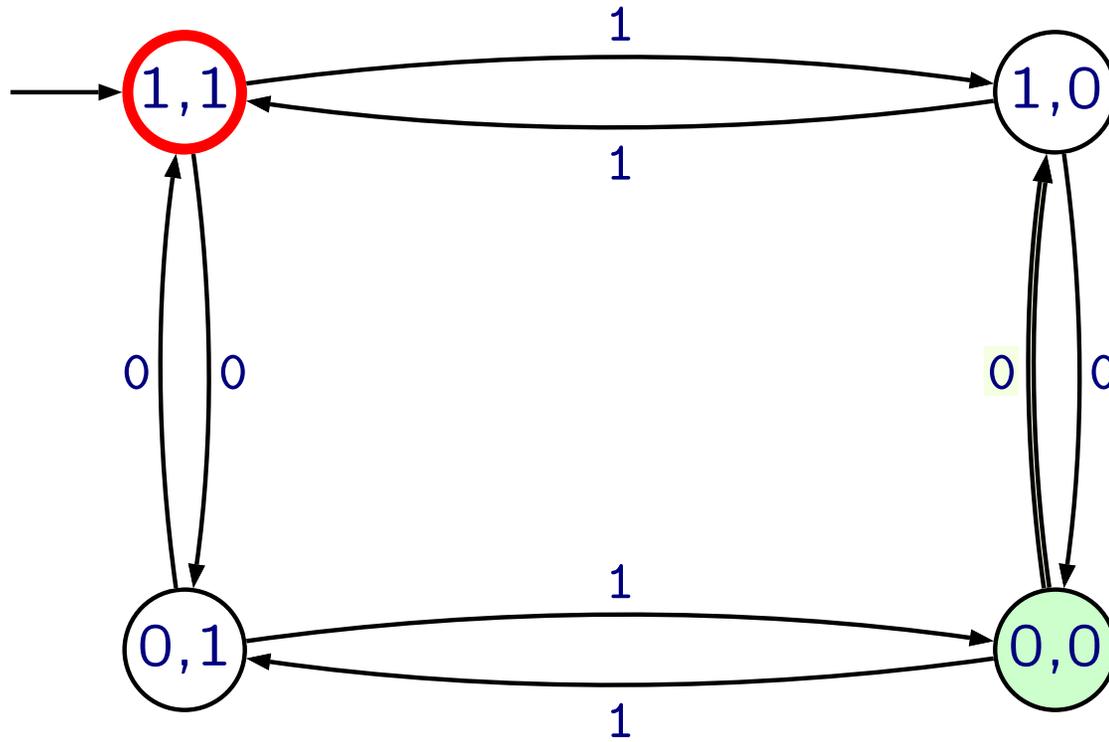


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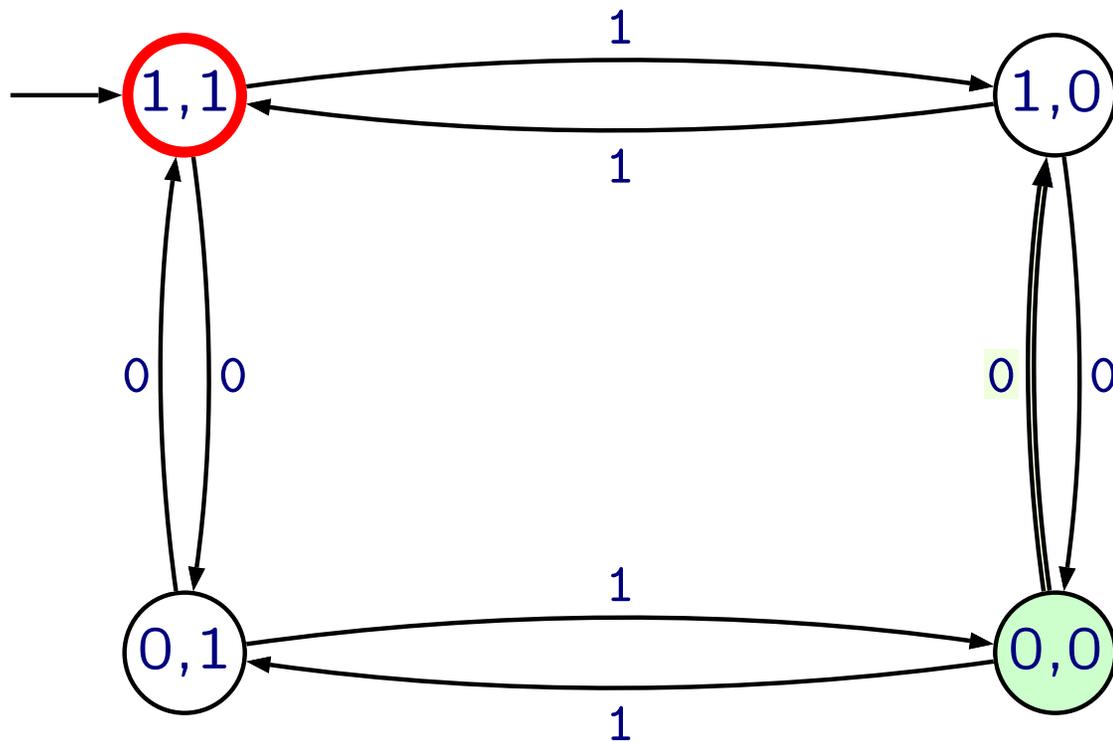
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state transition graph



parity test



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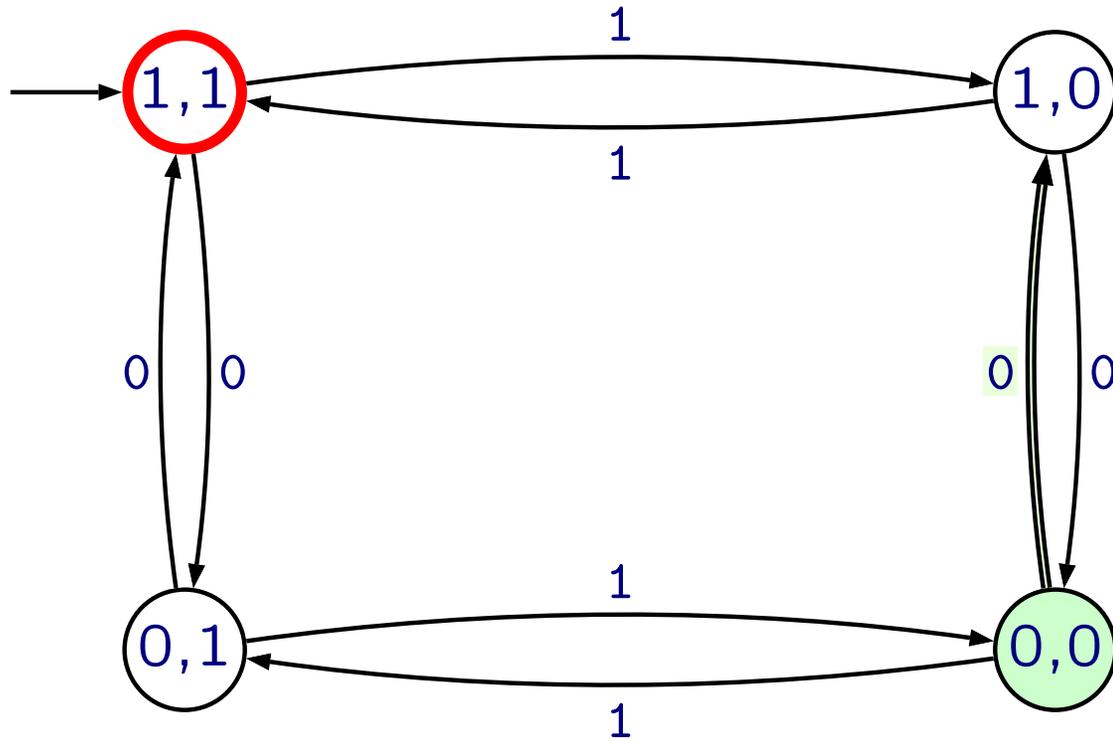
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state transition graph



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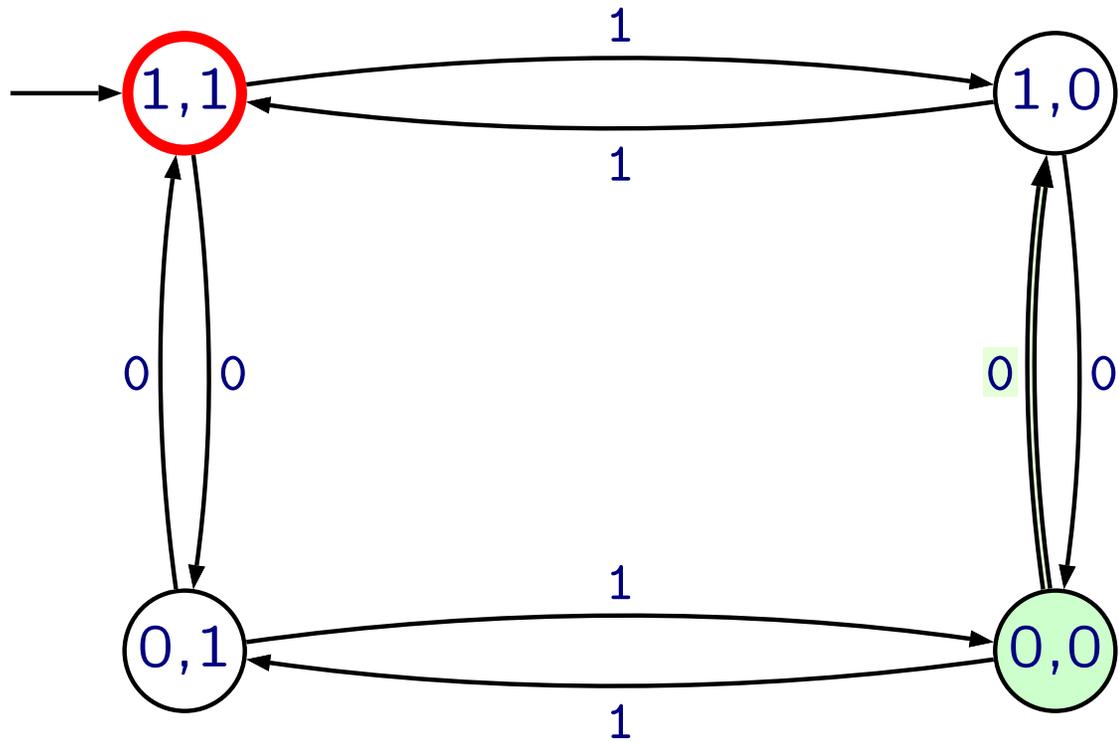
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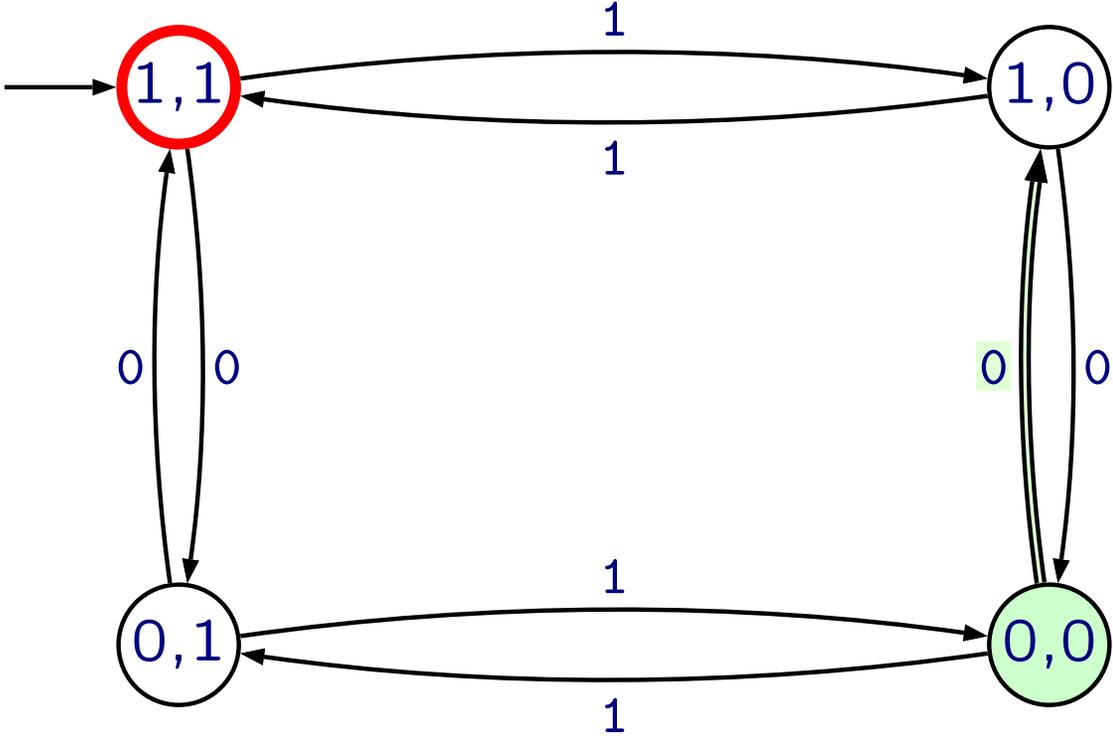
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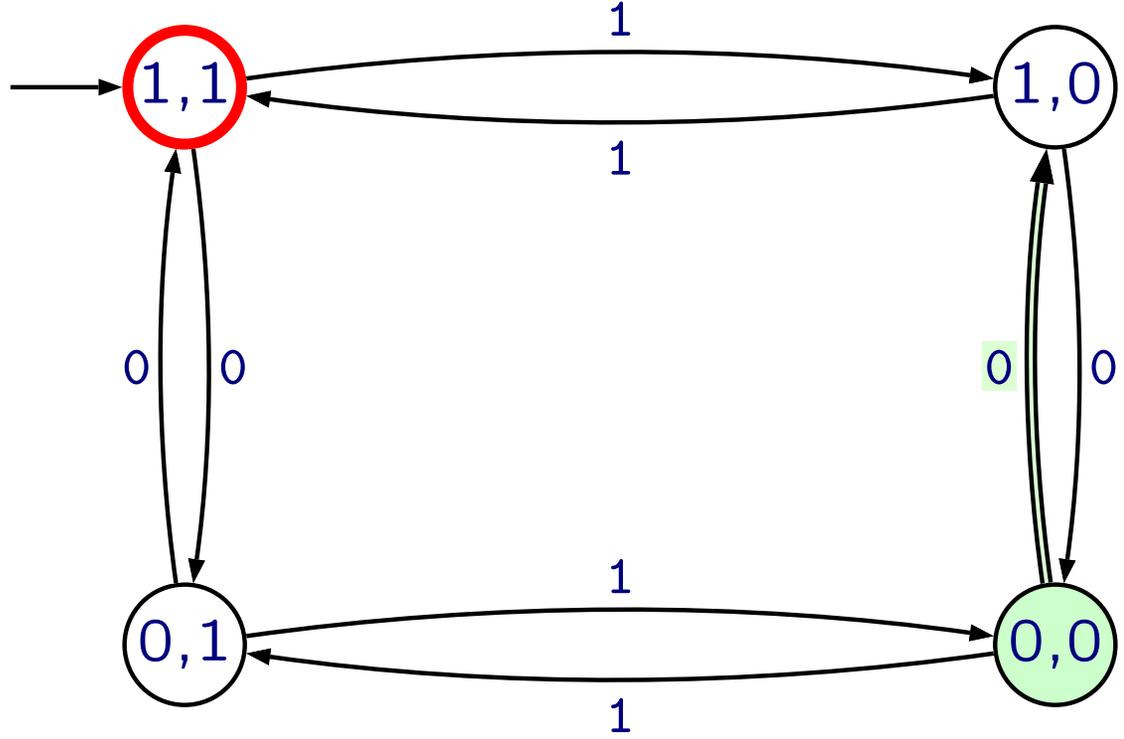
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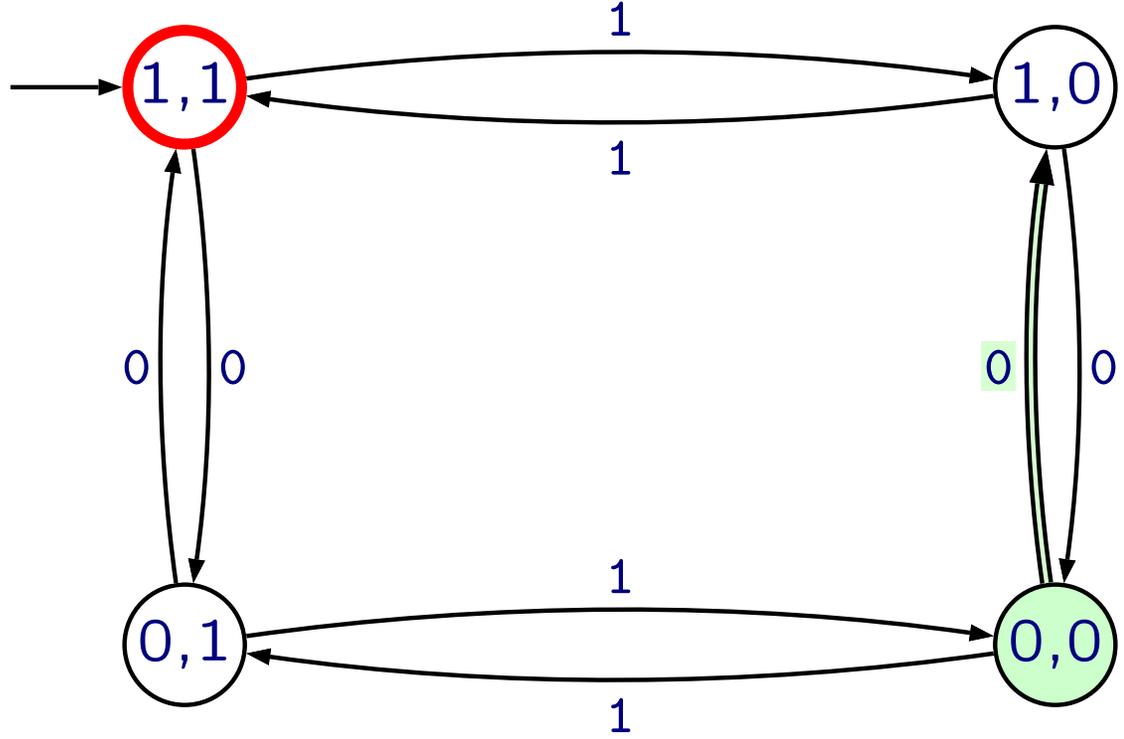
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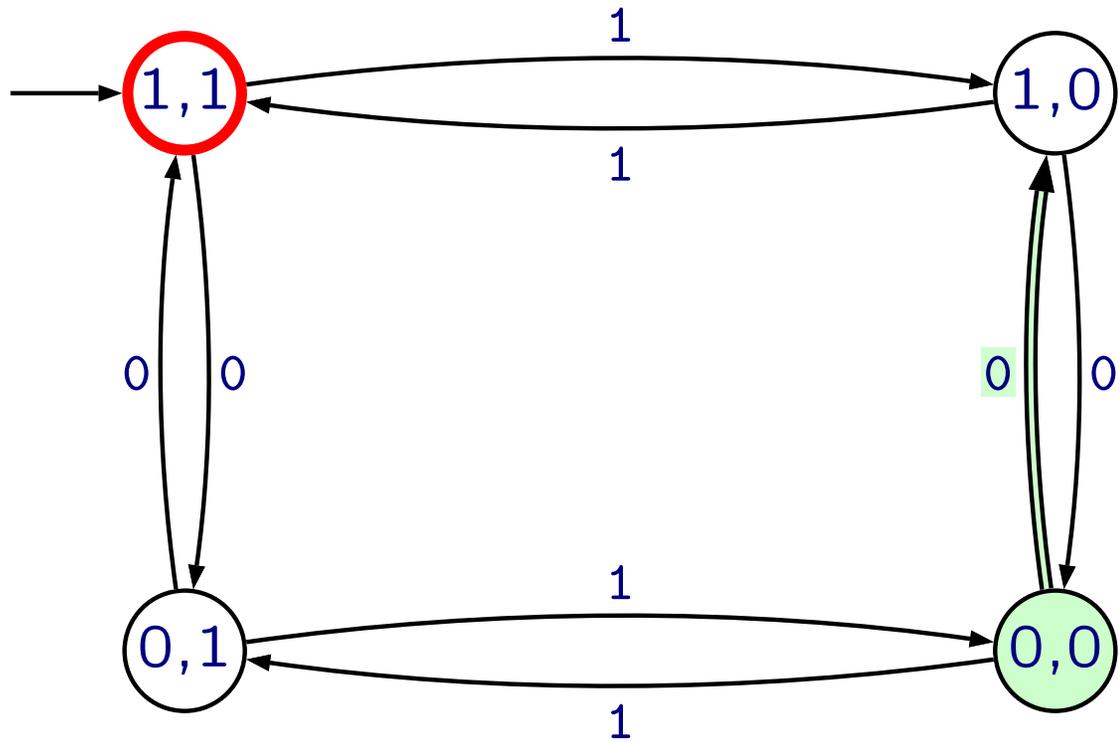
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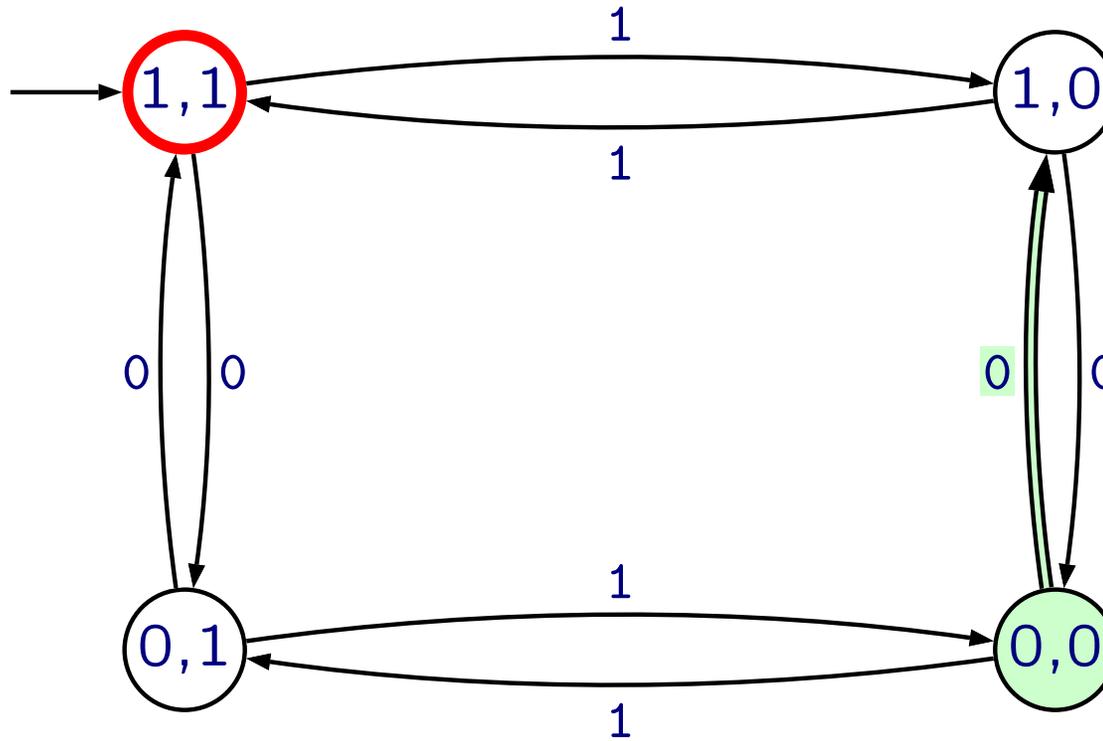
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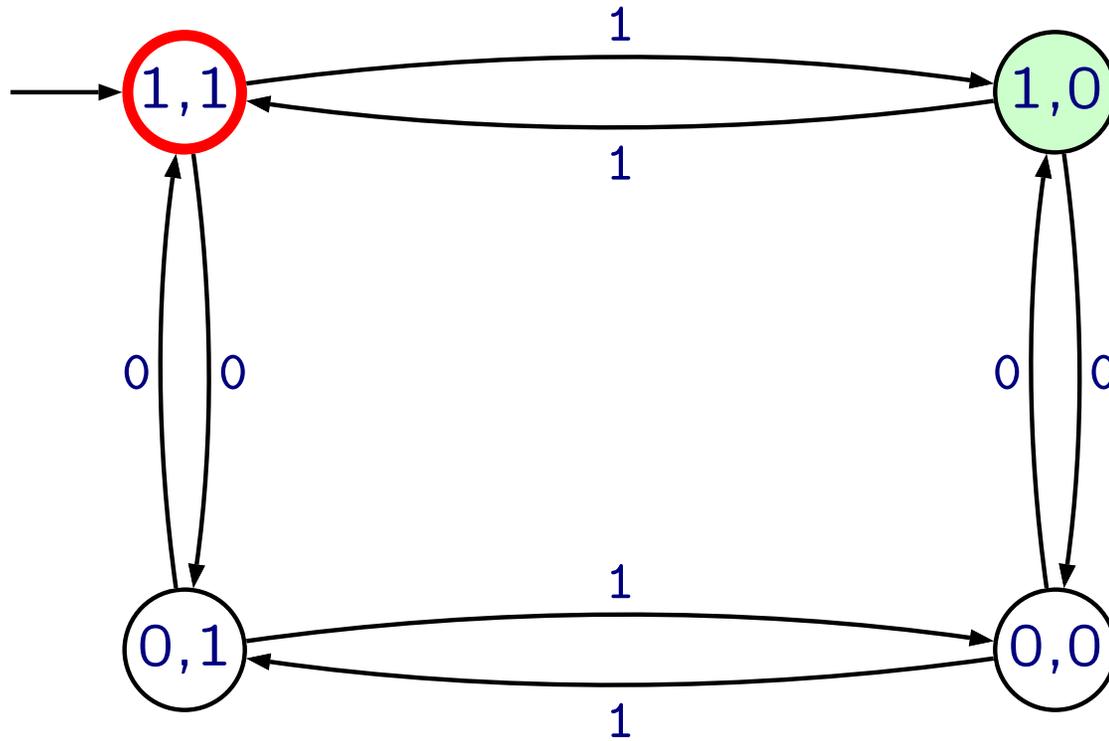
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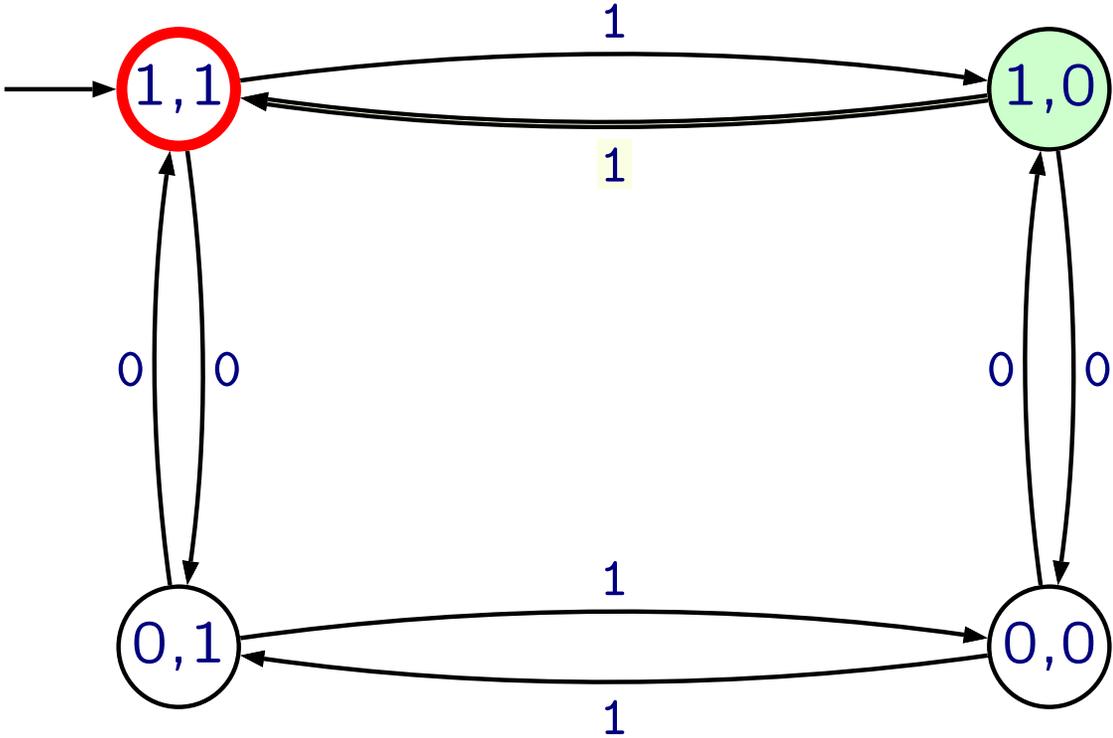
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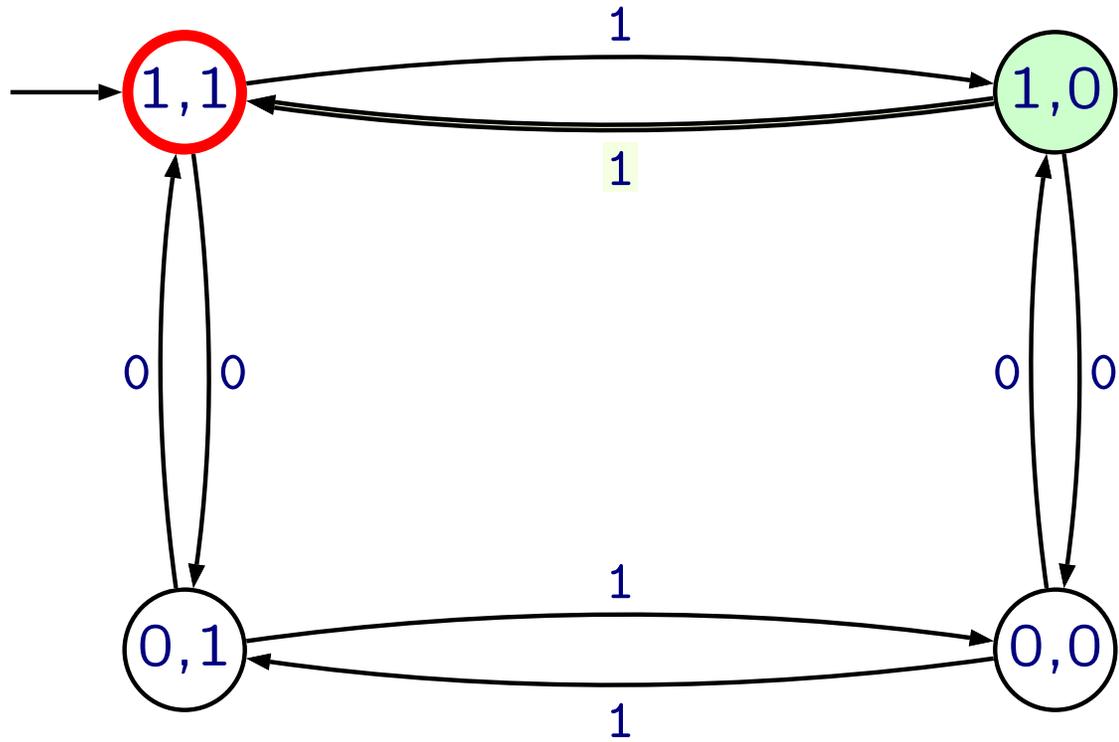
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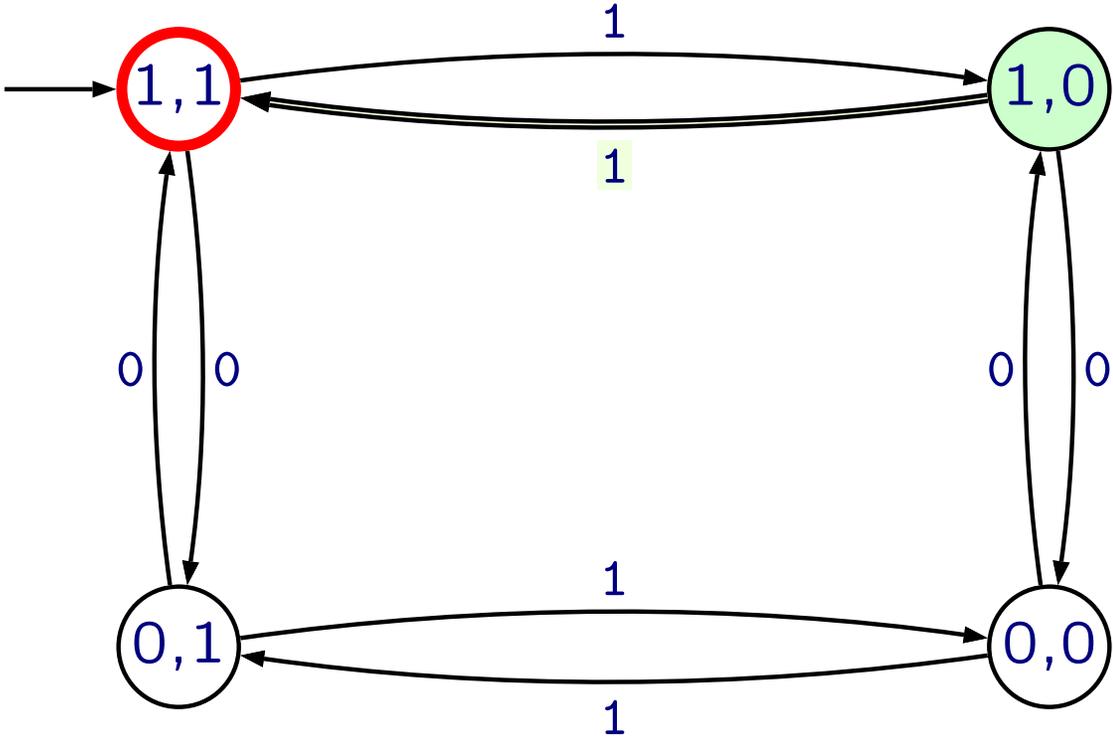
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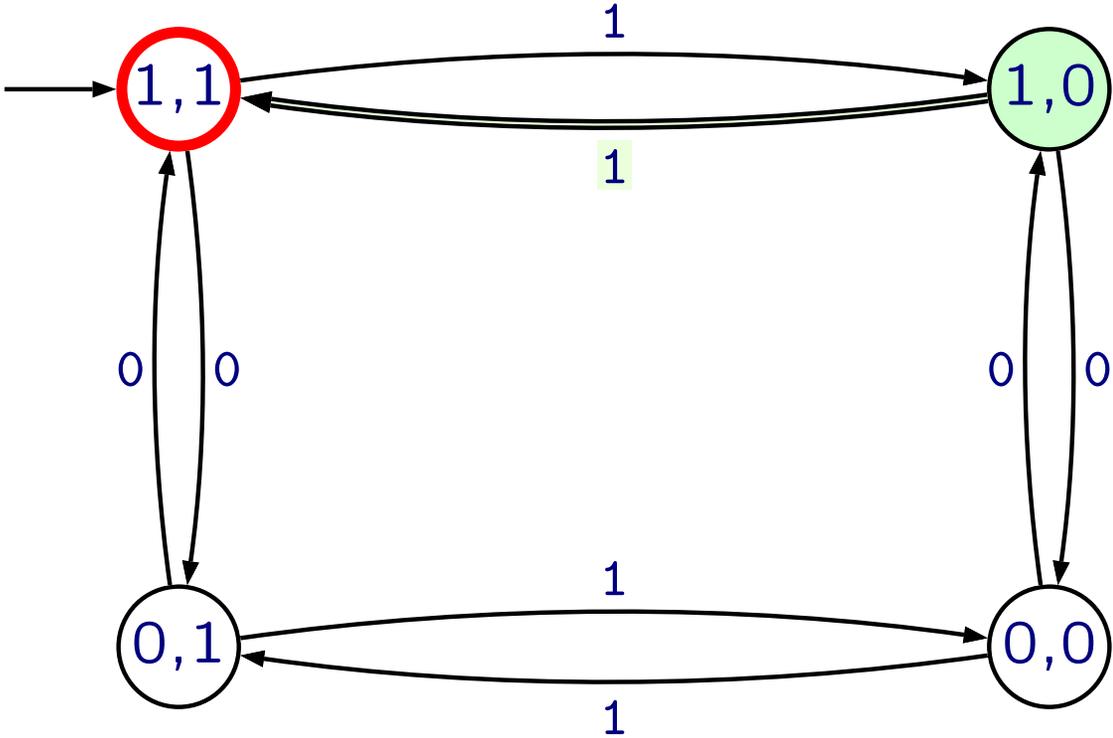
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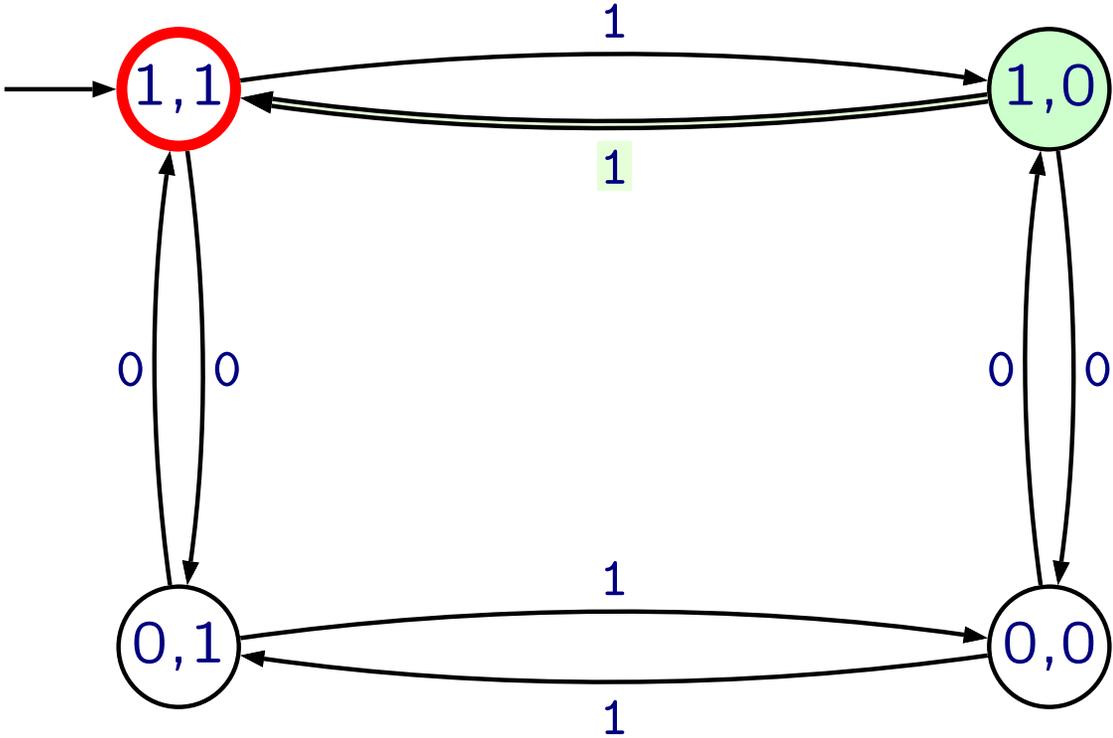
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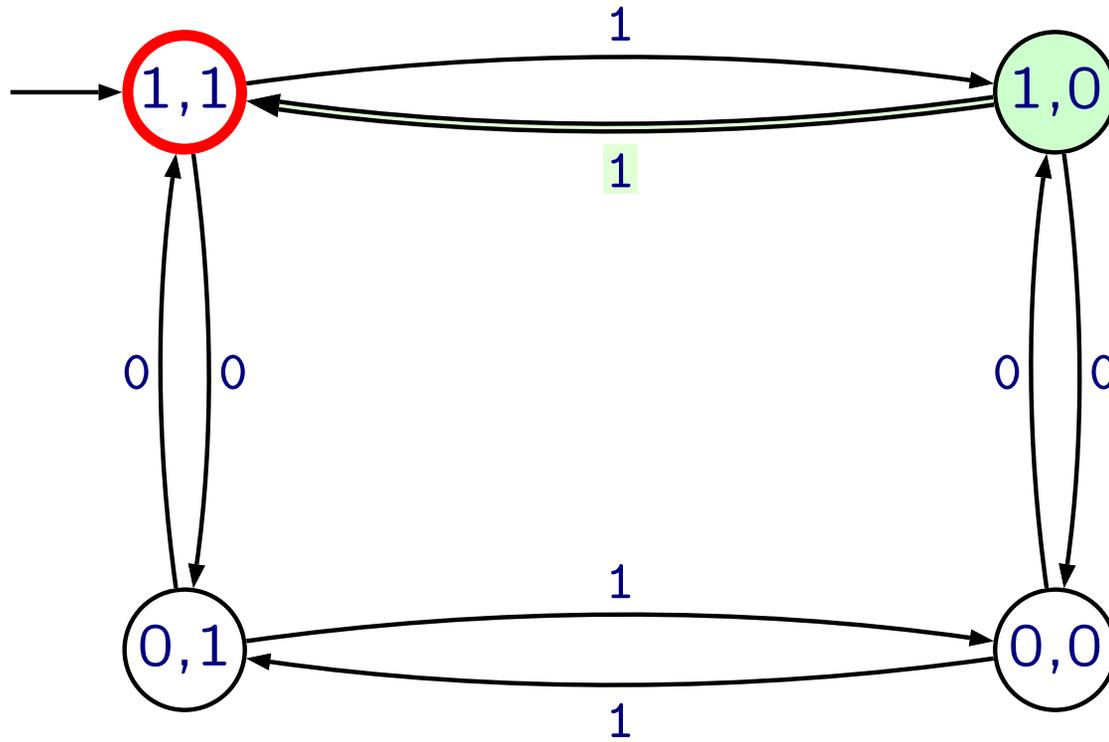
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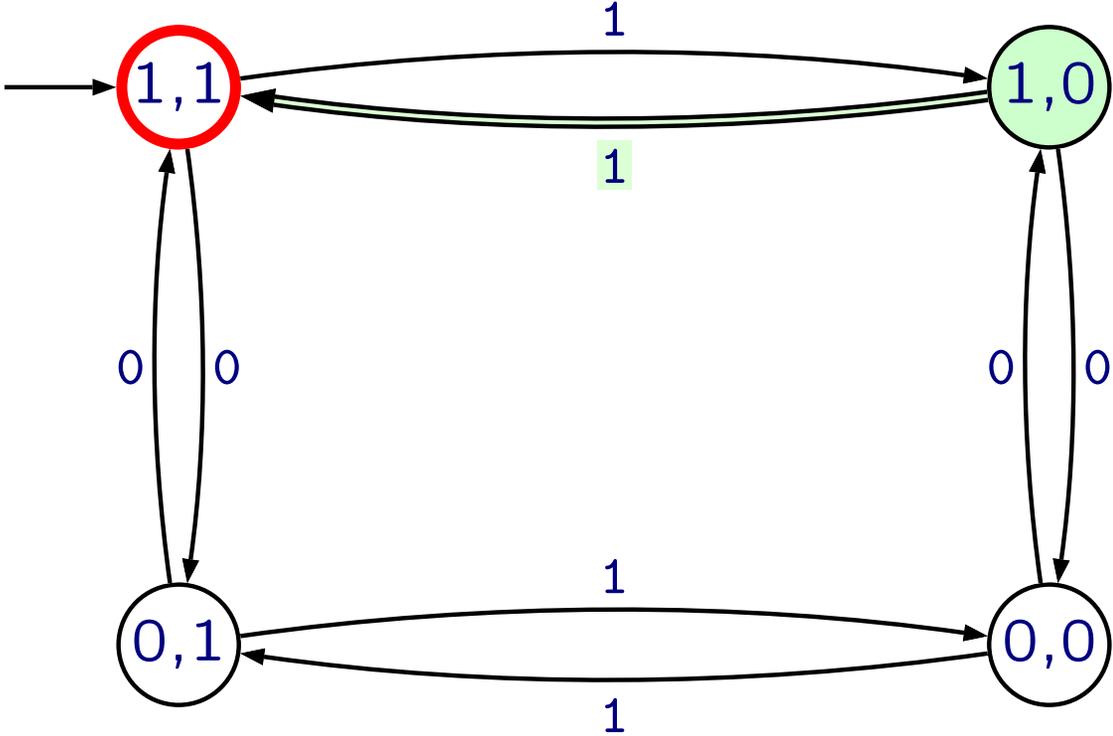
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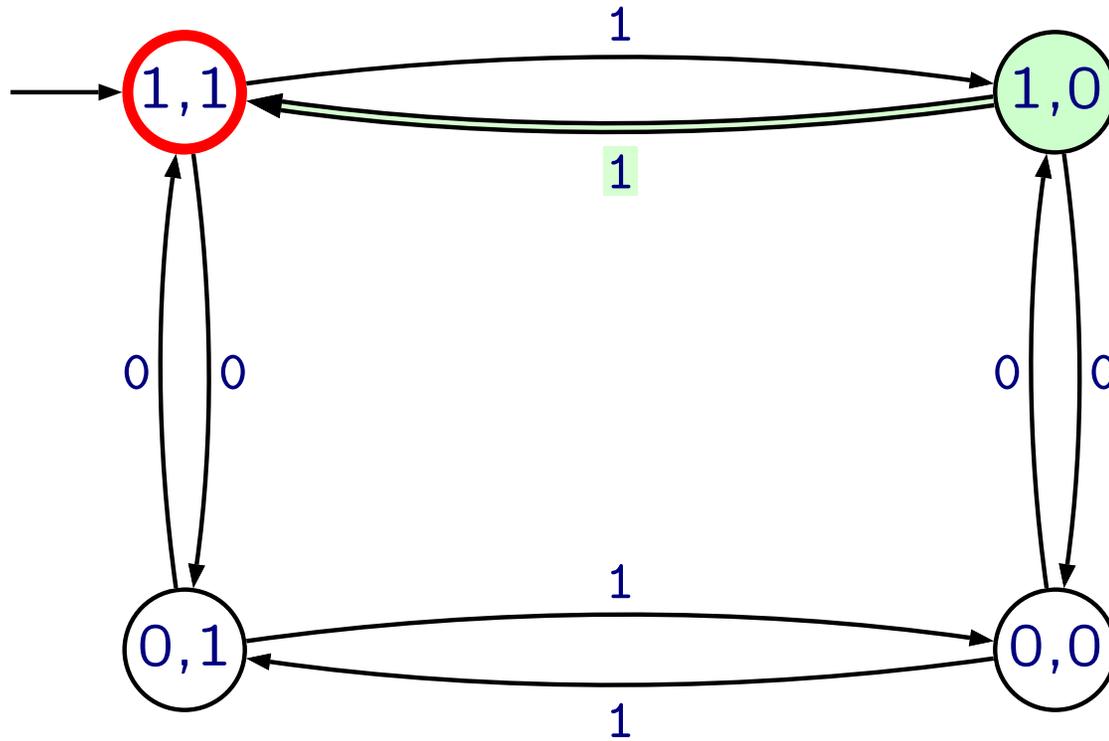
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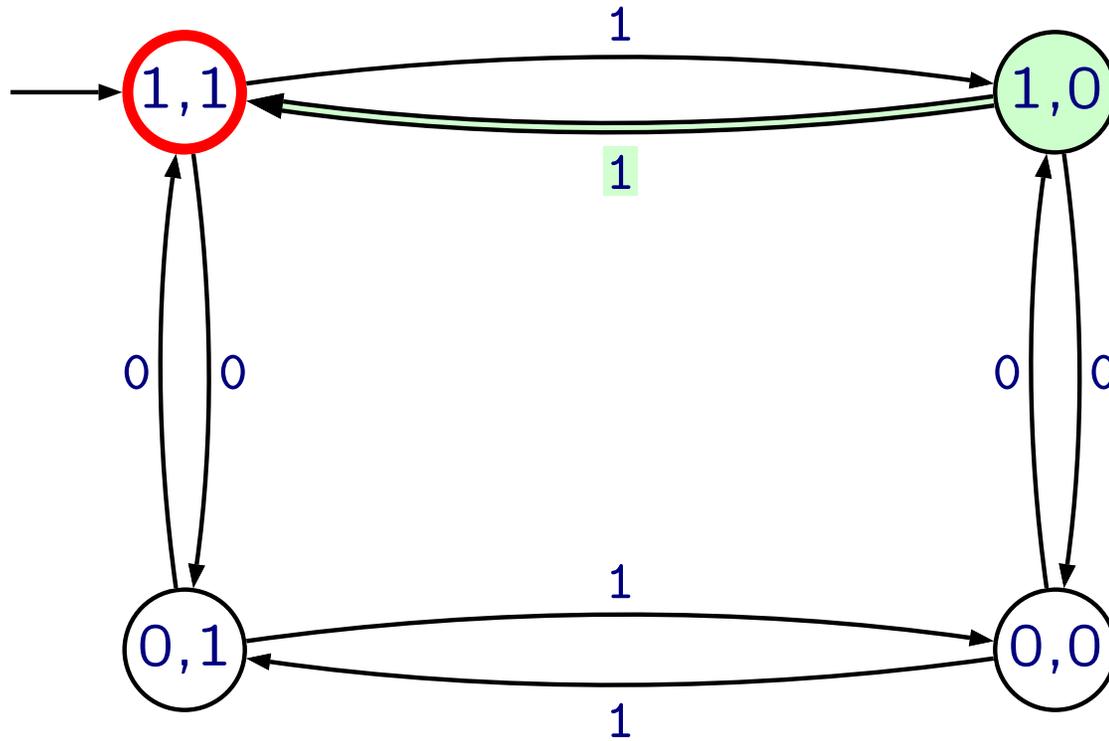
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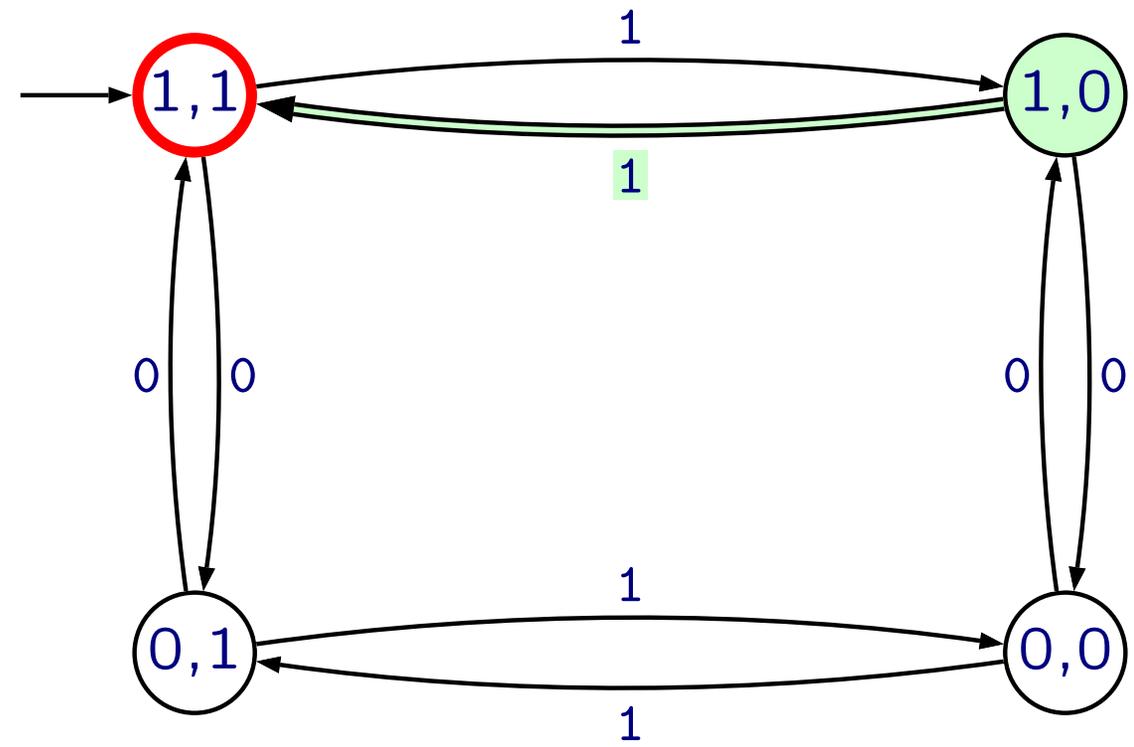
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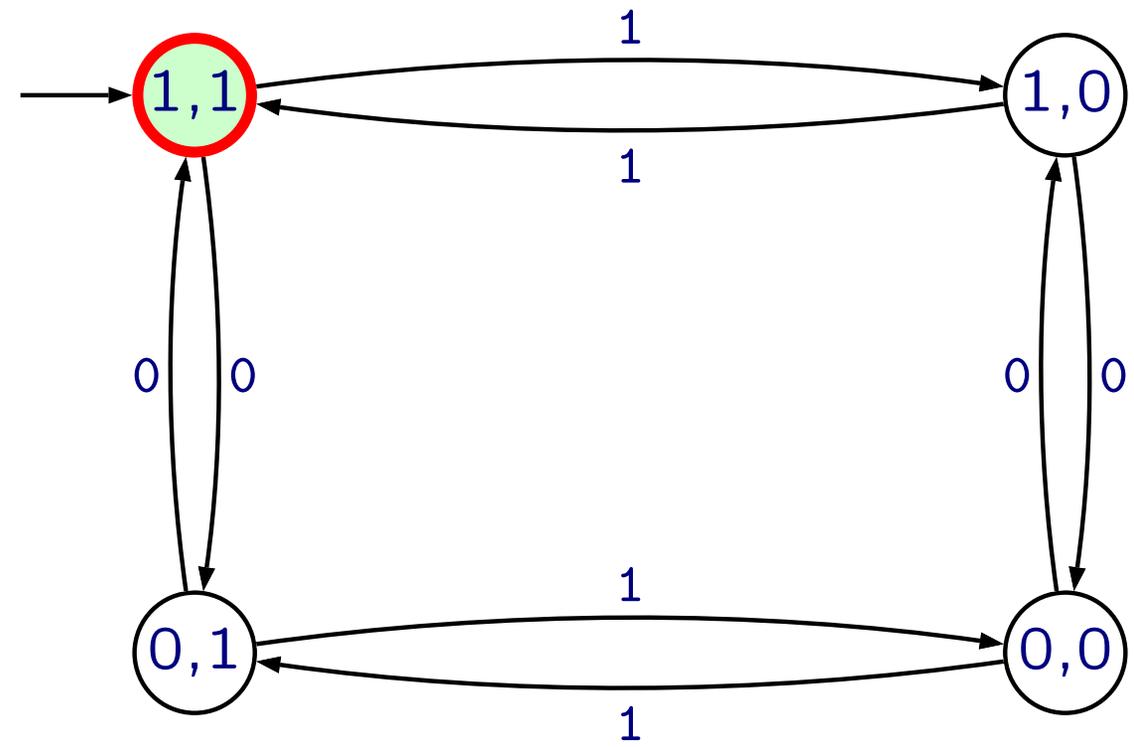
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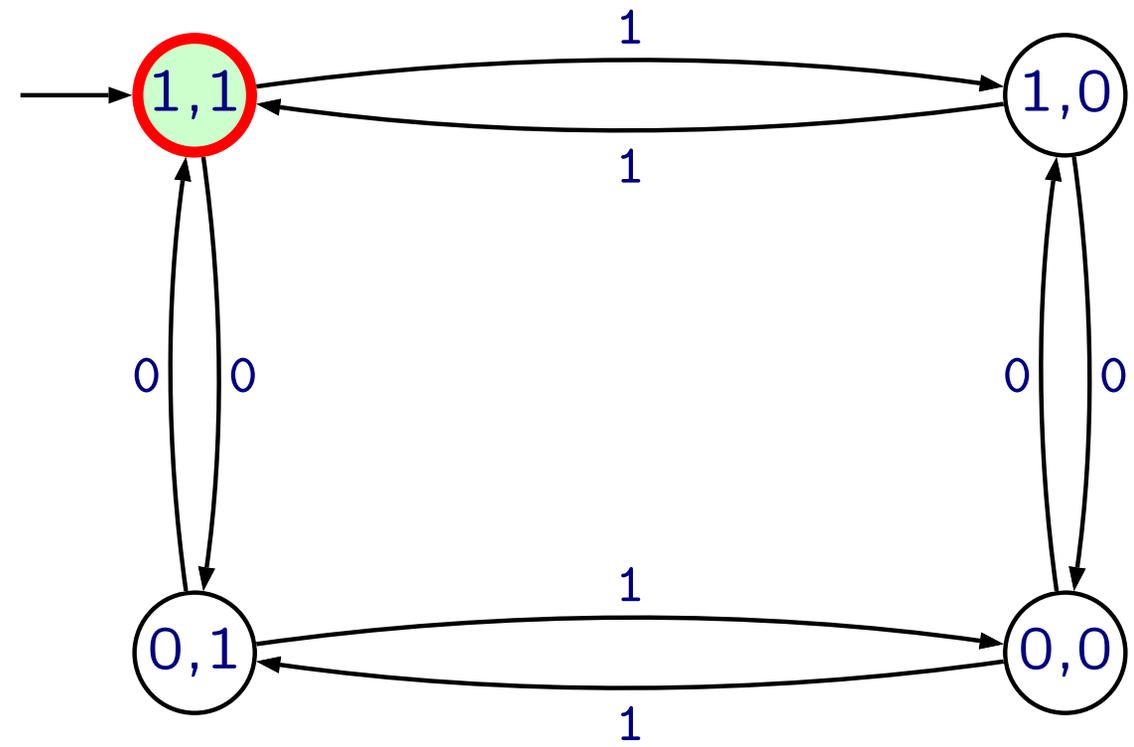


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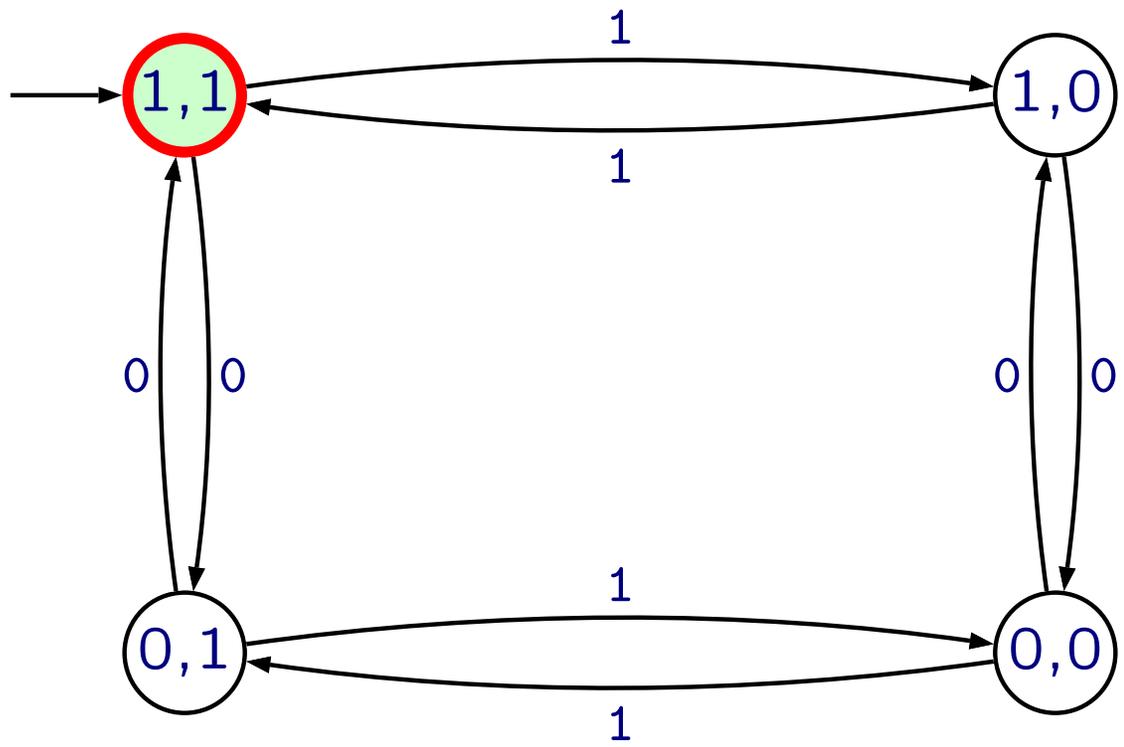


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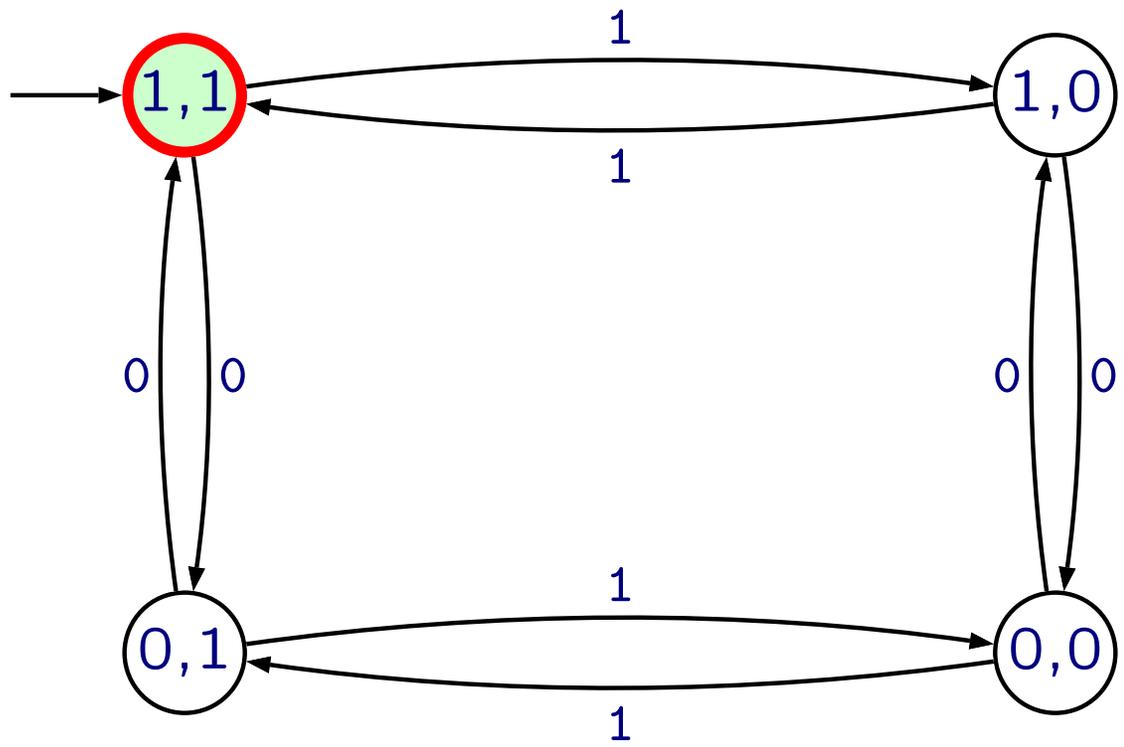
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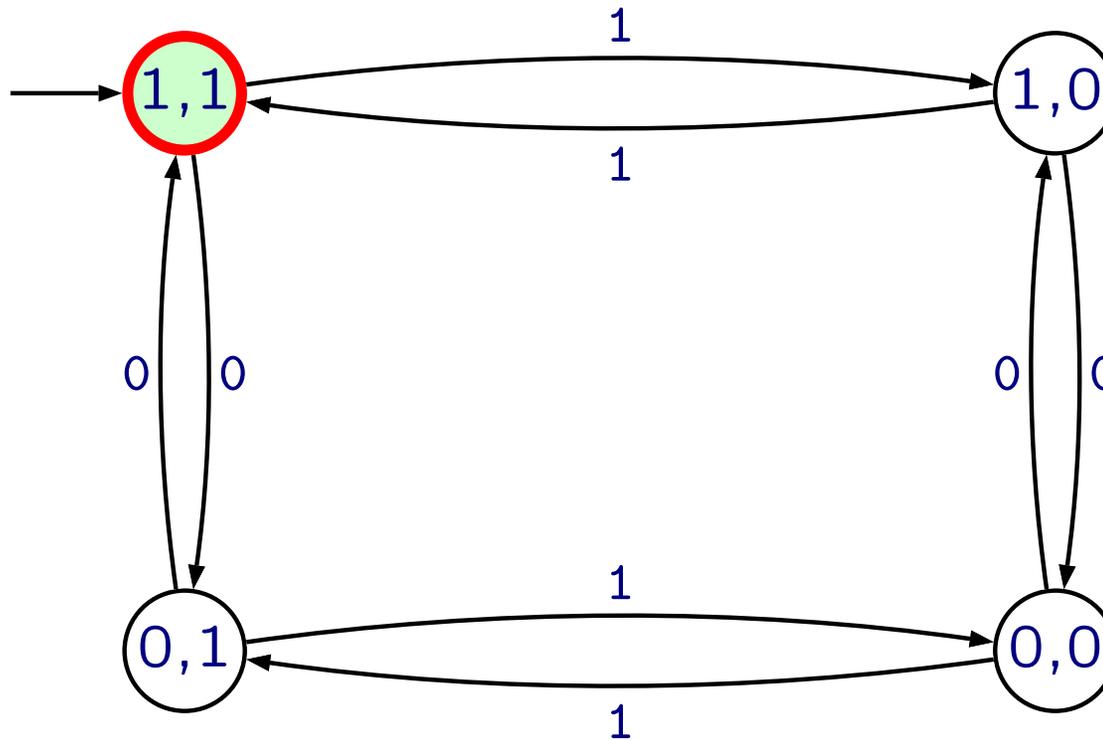
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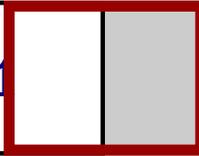
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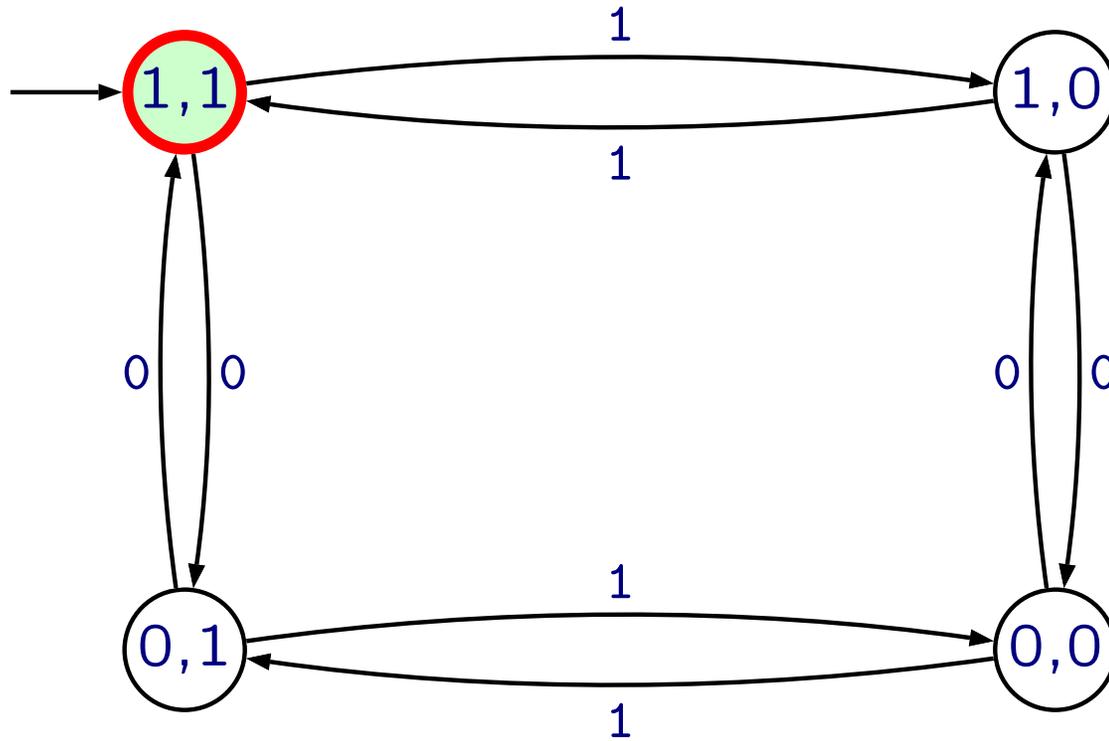
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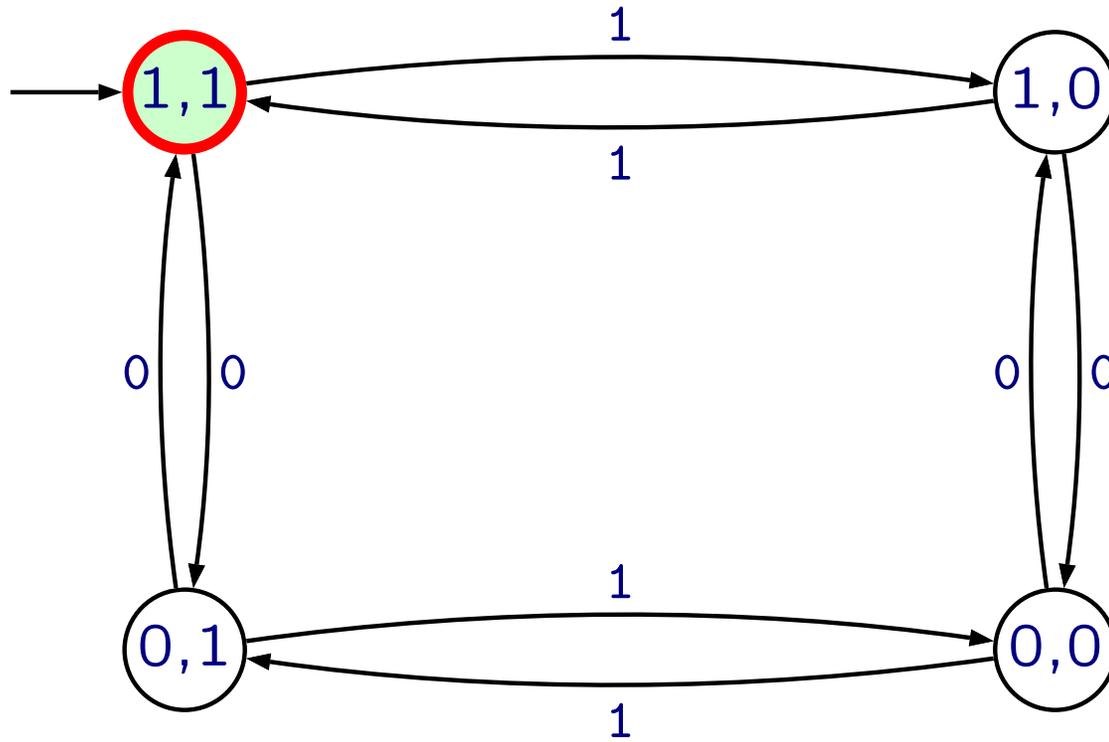
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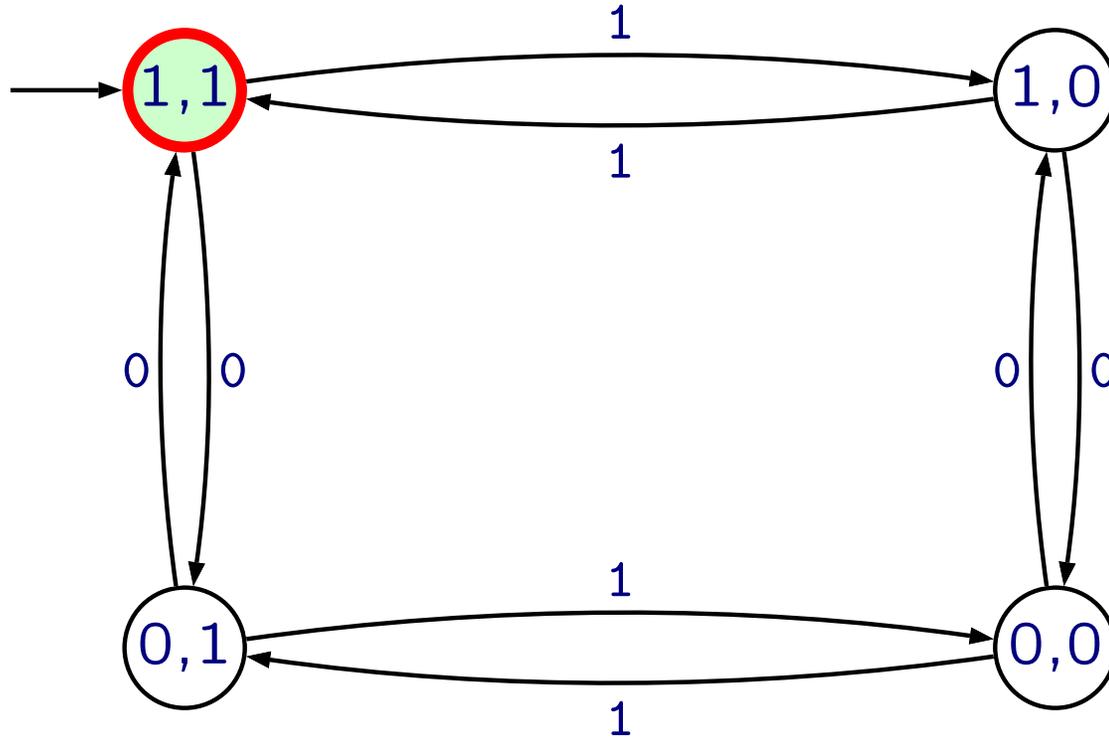
1

0

1



state transition graph



parity test



Akzeptieren

0

0

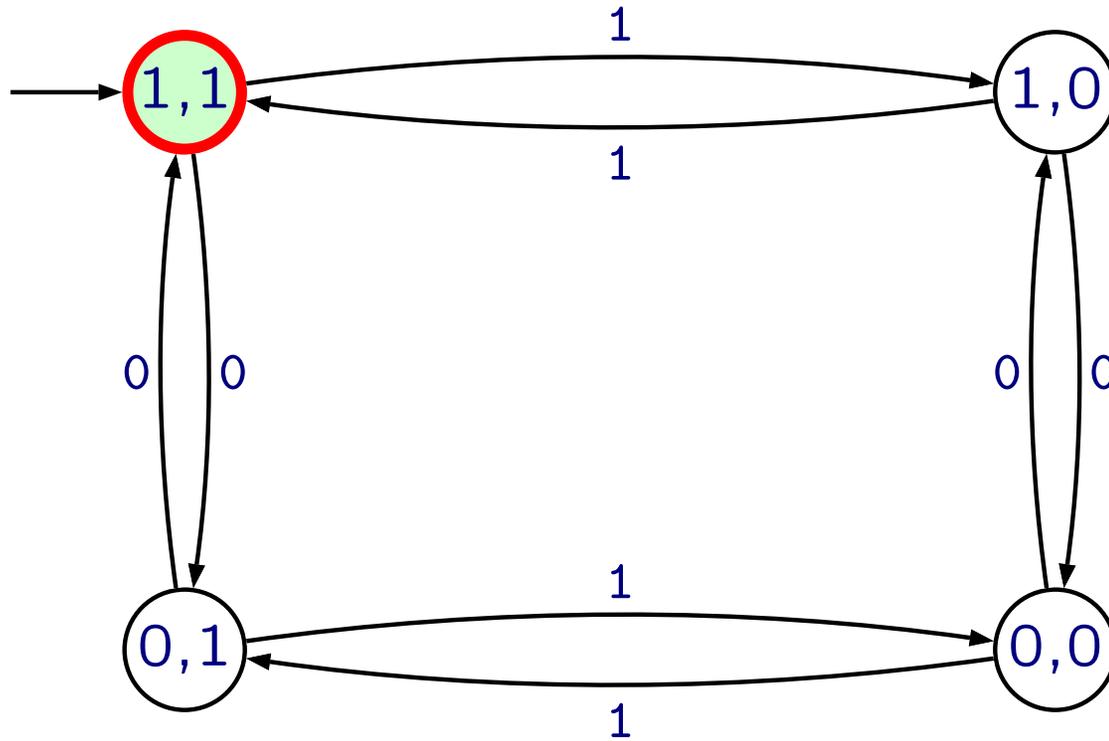
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0

1



state transition graph



parity test



Akzeptieren

0

0

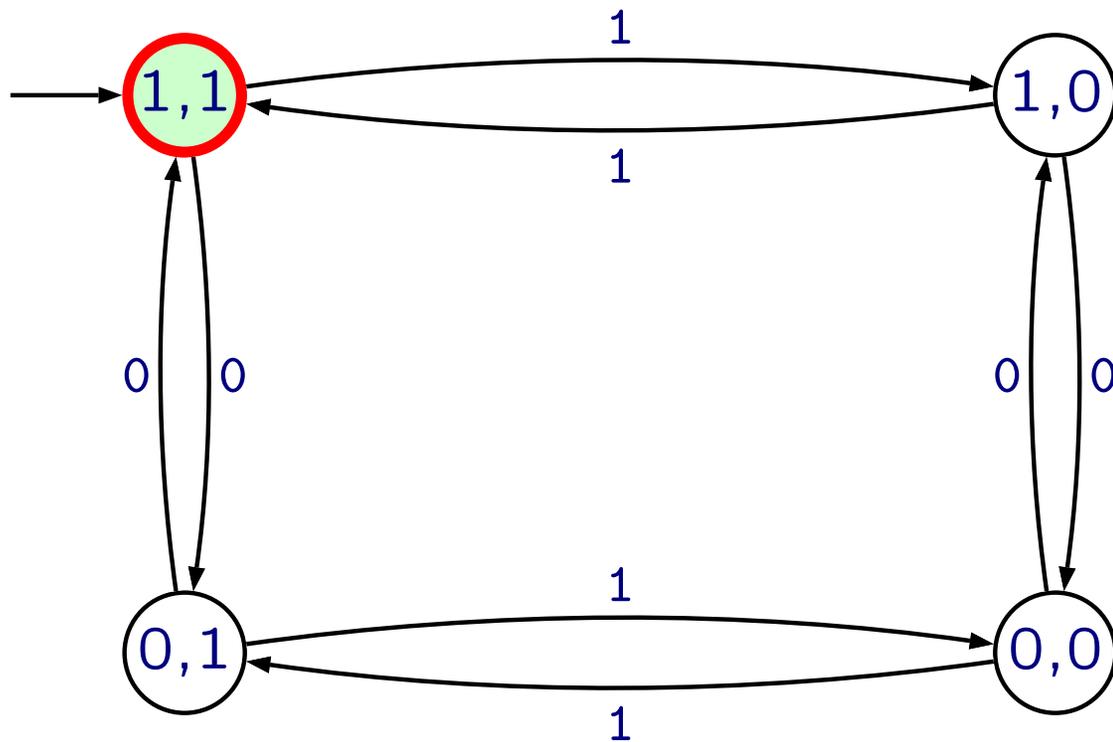
1

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state transition graph



parity test



Akzeptieren

0

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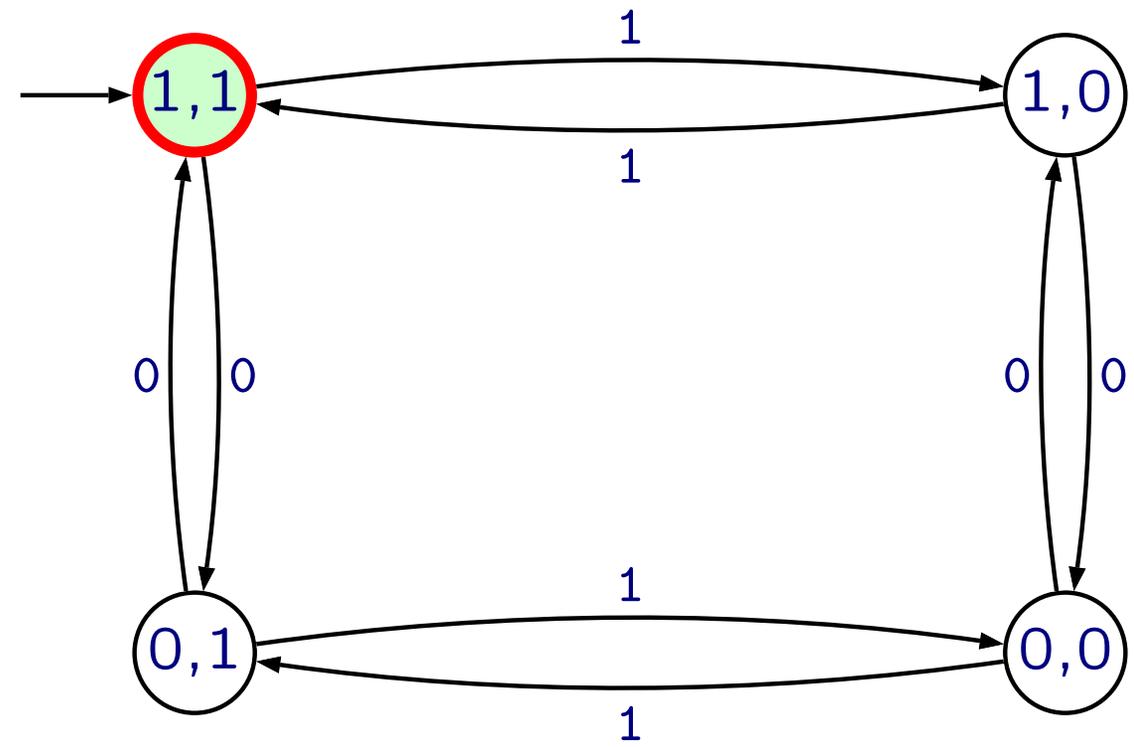
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1



parity test

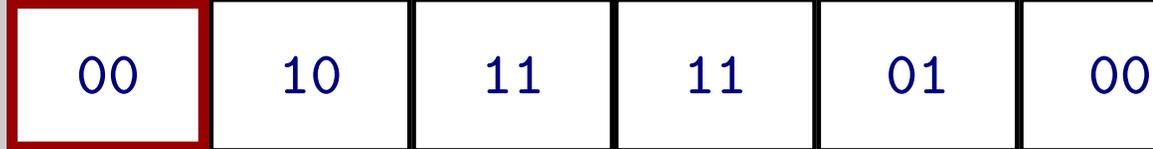
state transition graph



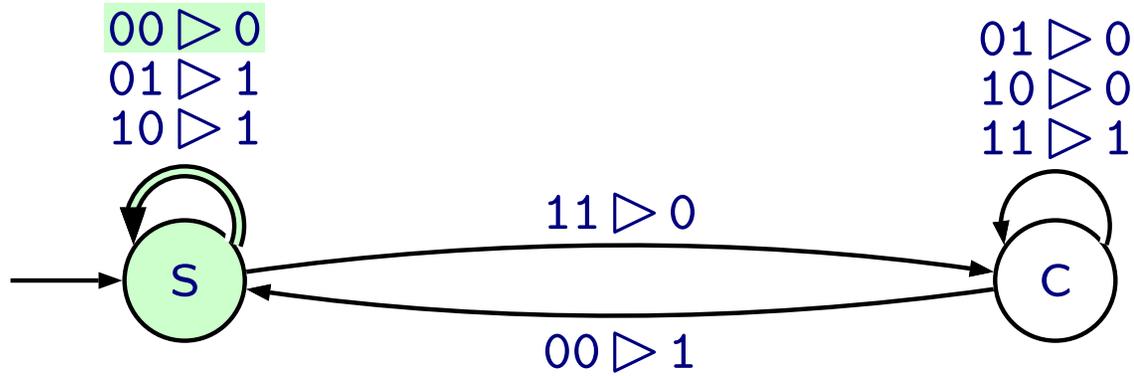
Akzeptieren

one bit serial adder

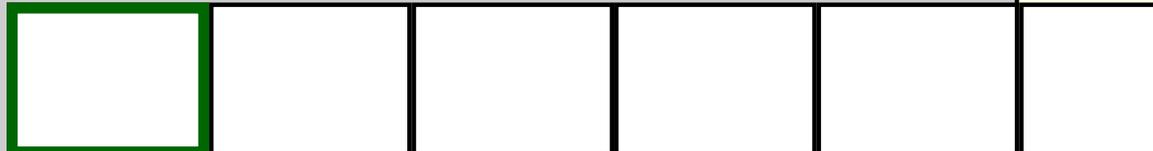
input tape



state transition graph



output tape



one bit serial adder

input tape

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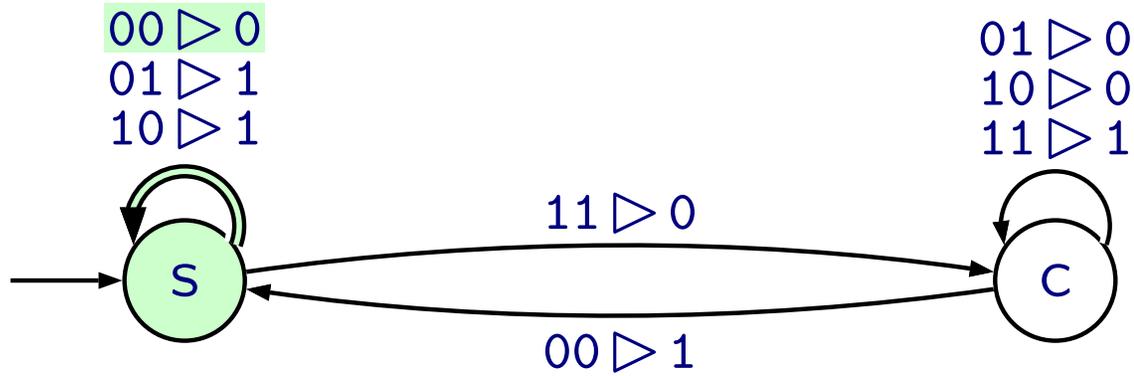
11

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01

00

state transition graph

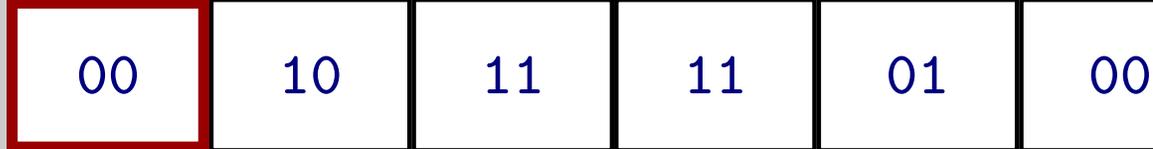


output tape

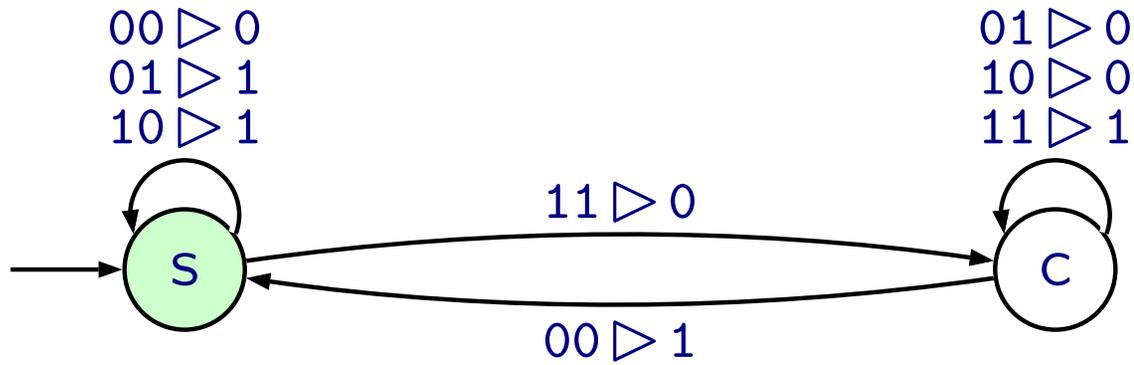
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one bit serial adder

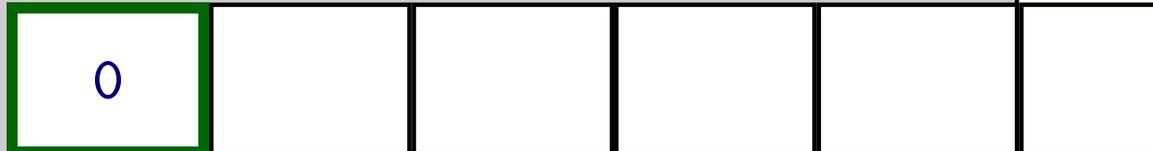
input tape



state transition graph

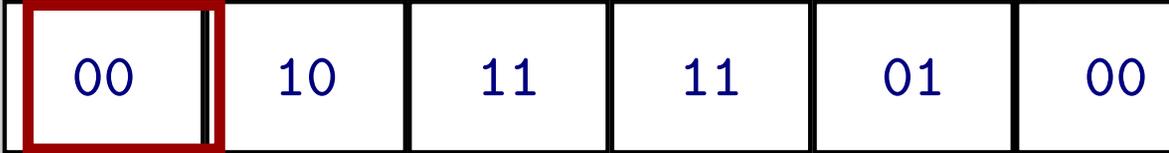


output tape

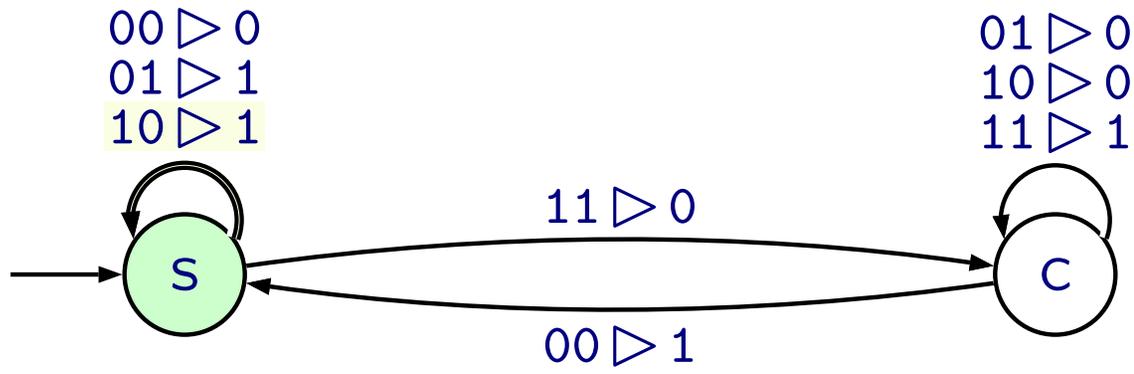


one bit serial adder

input tape



state transition graph

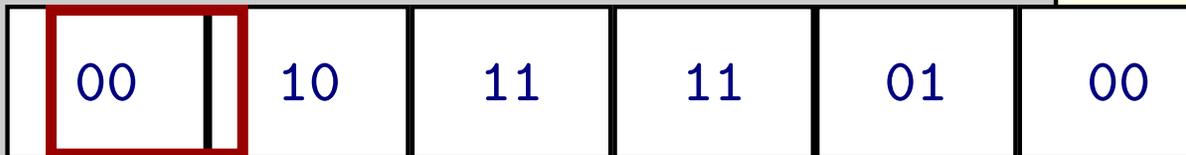


output tape

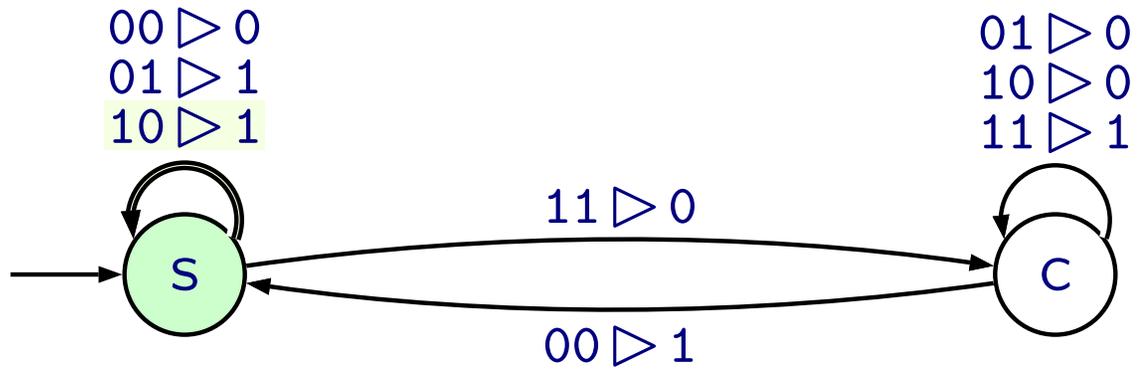


one bit serial adder

input tape



state transition graph



output tape

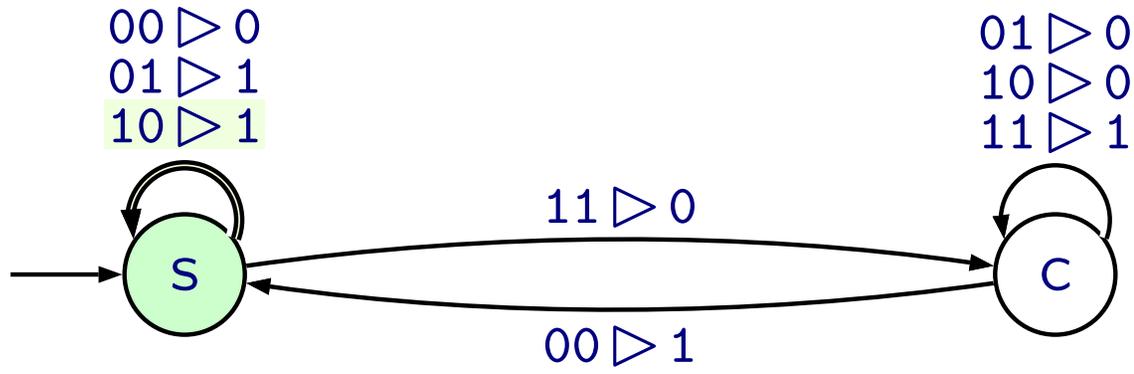


one bit serial adder

input tape



state transition graph

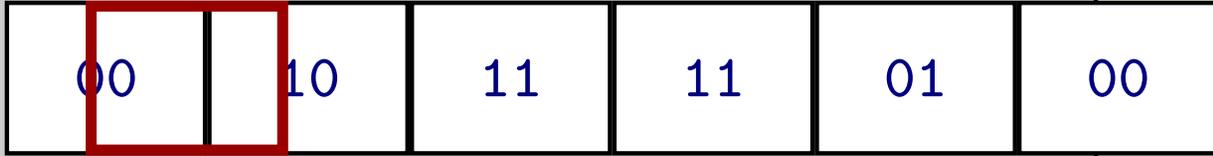


output tape

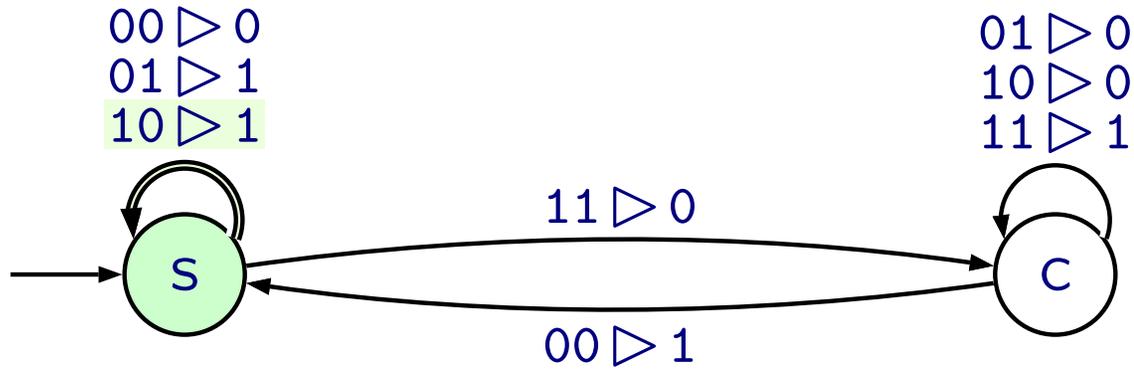


one bit serial adder

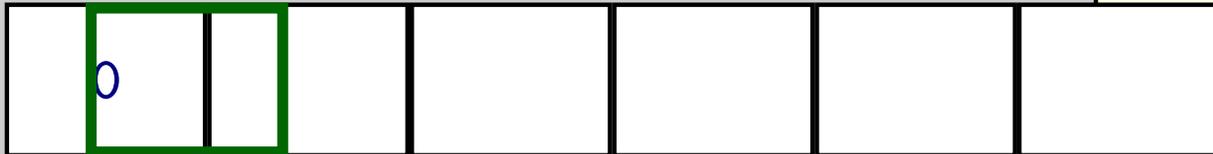
input tape



state transition graph

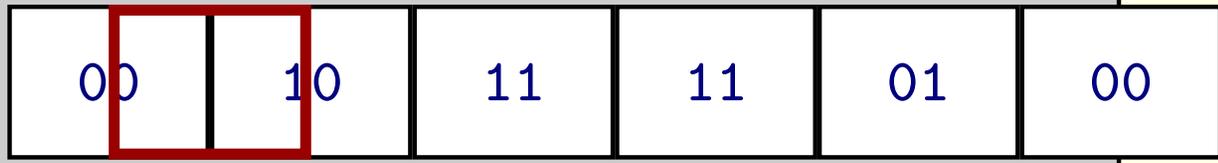


output tape

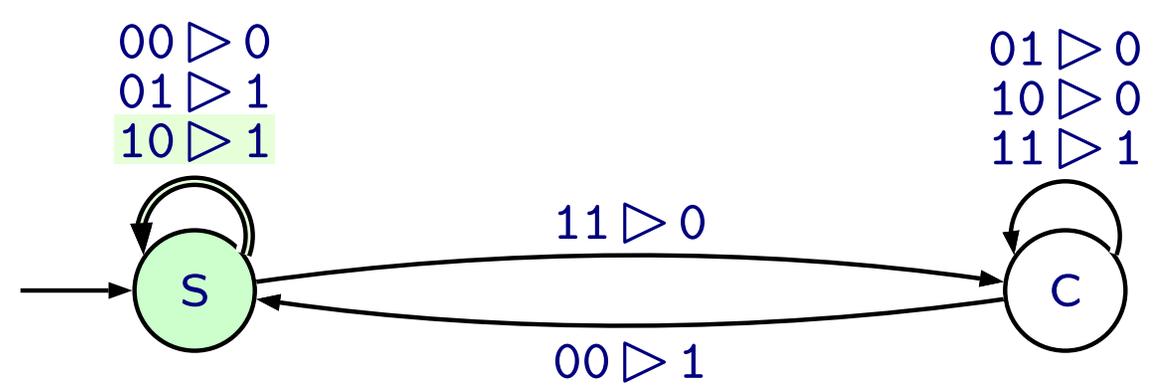


one bit serial adder

input tape



state transition graph



output tape

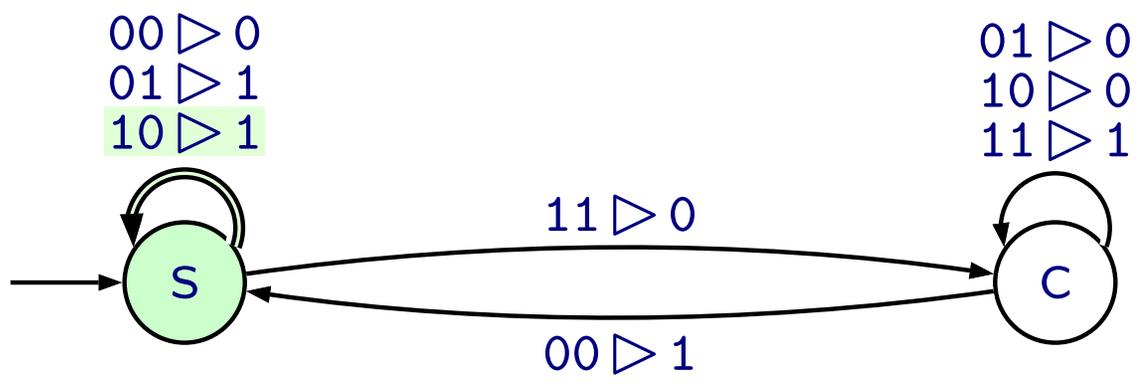


one bit serial adder

input tape



state transition graph



output tape

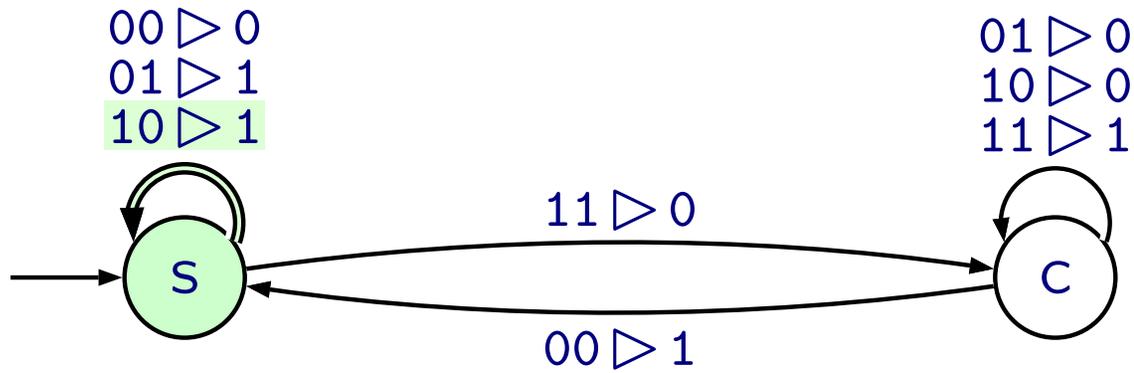


one bit serial adder

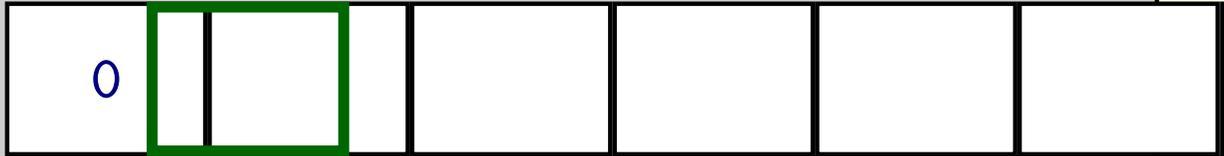
input tape



state transition graph



output tape

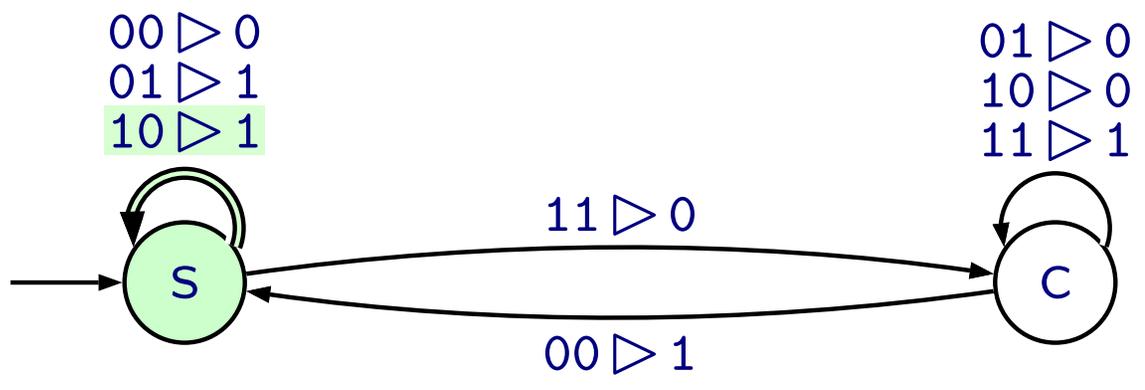


one bit serial adder

input tape



state transition graph



output tape

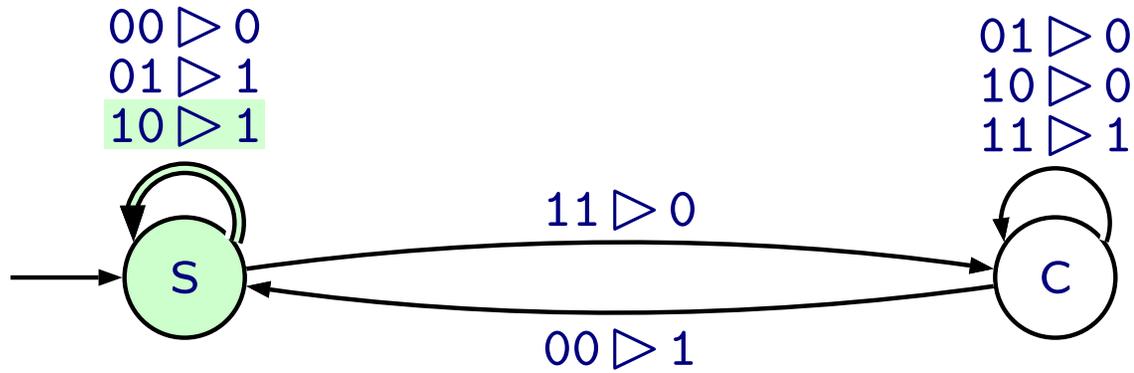


one bit serial adder

input tape



state transition graph



output tape

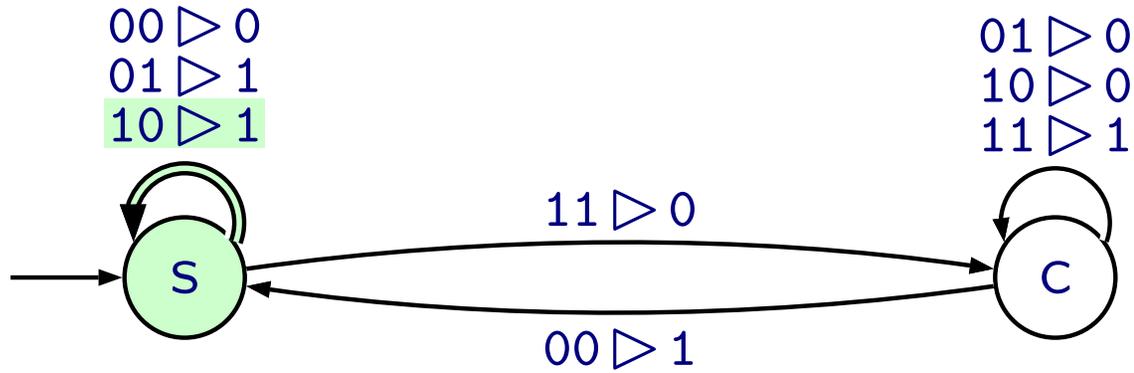


one bit serial adder

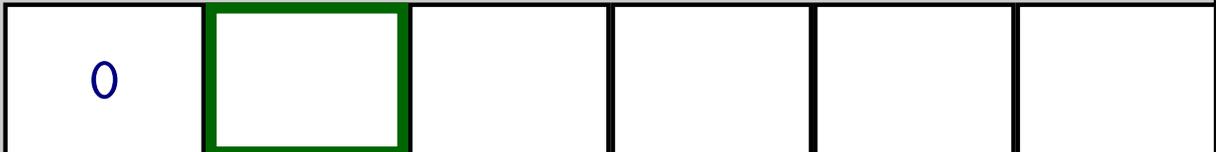
input tape



state transition graph



output tape

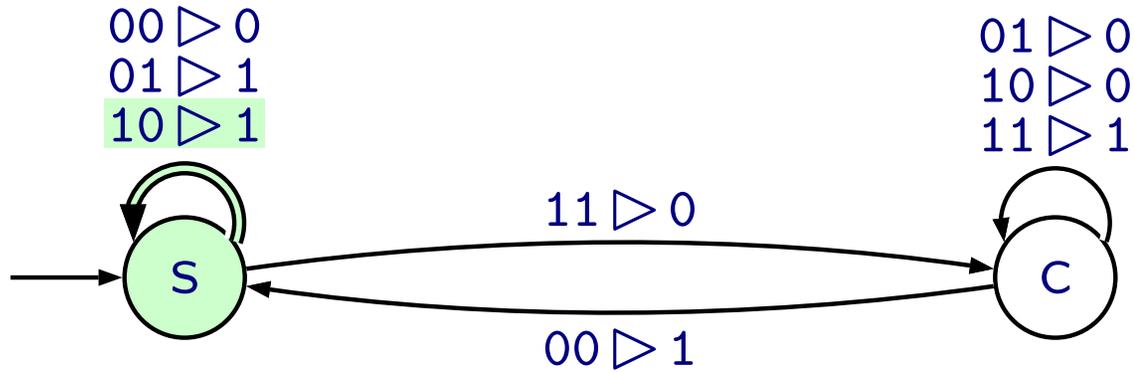


one bit serial adder

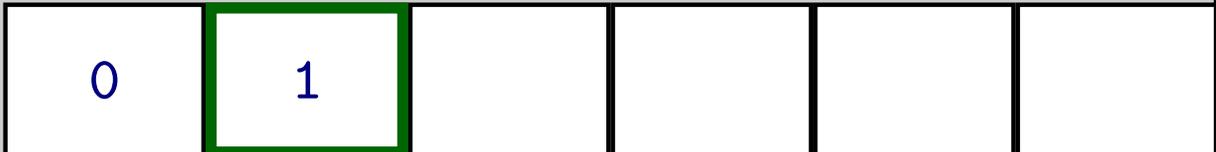
input tape



state transition graph



output tape

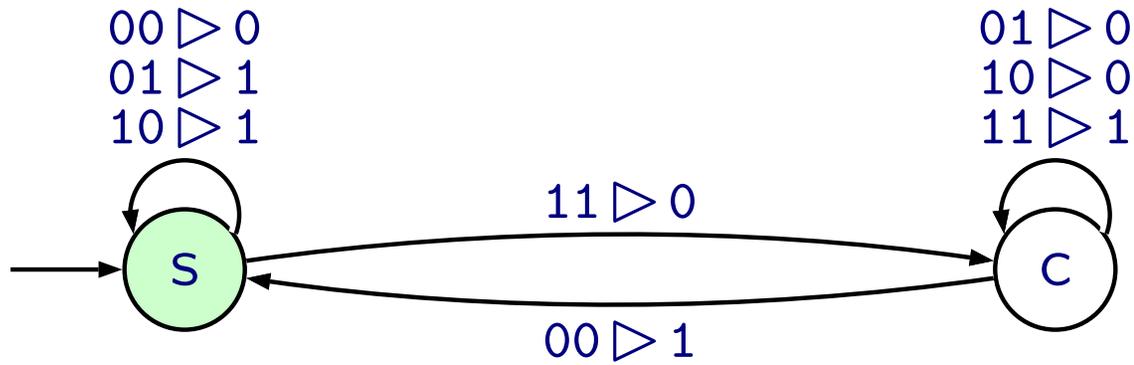


one bit serial adder

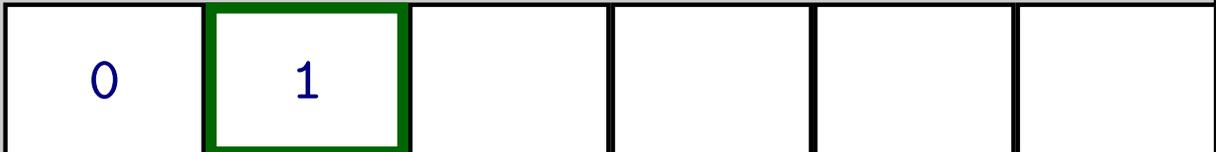
input tape



state transition graph



output tape

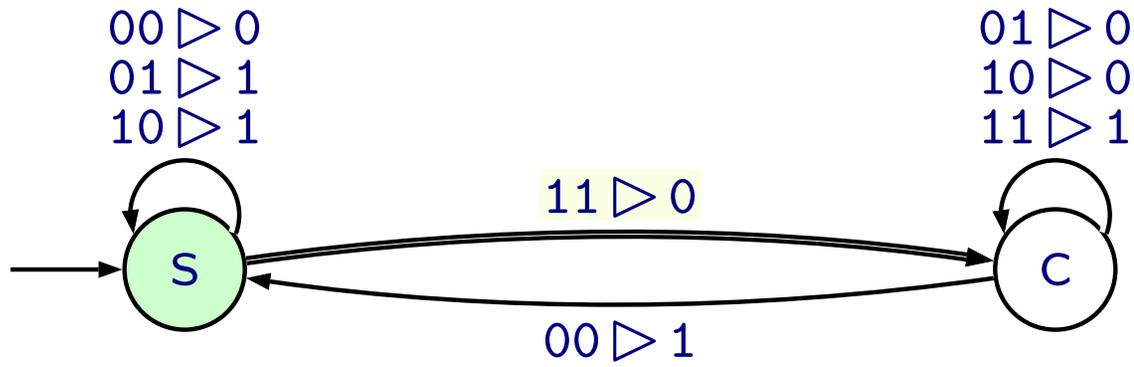


one bit serial adder

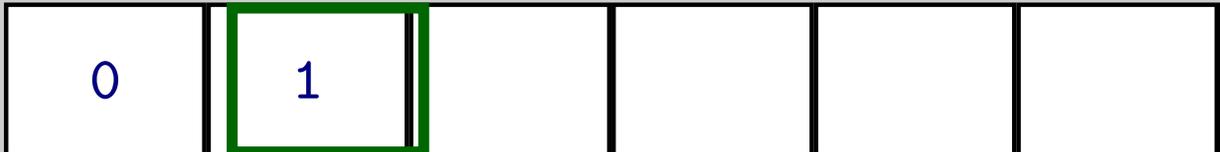
input tape



state transition graph



output tape

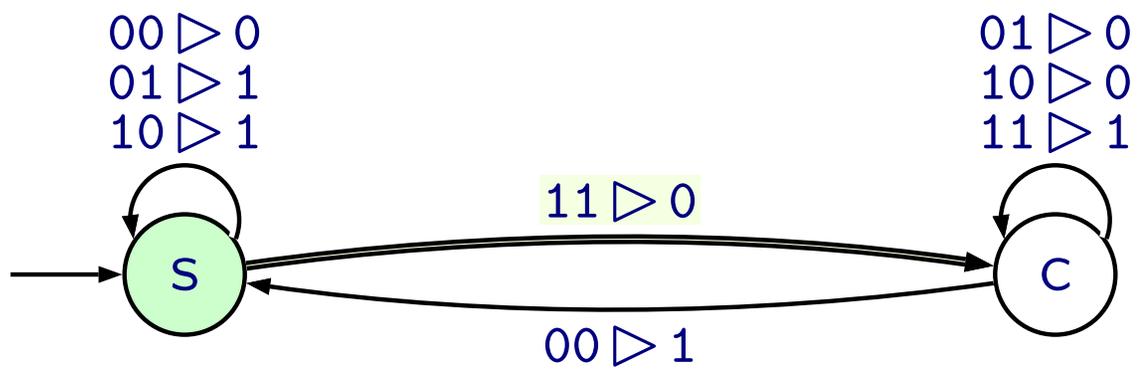


one bit serial adder

input tape



state transition graph



output tape

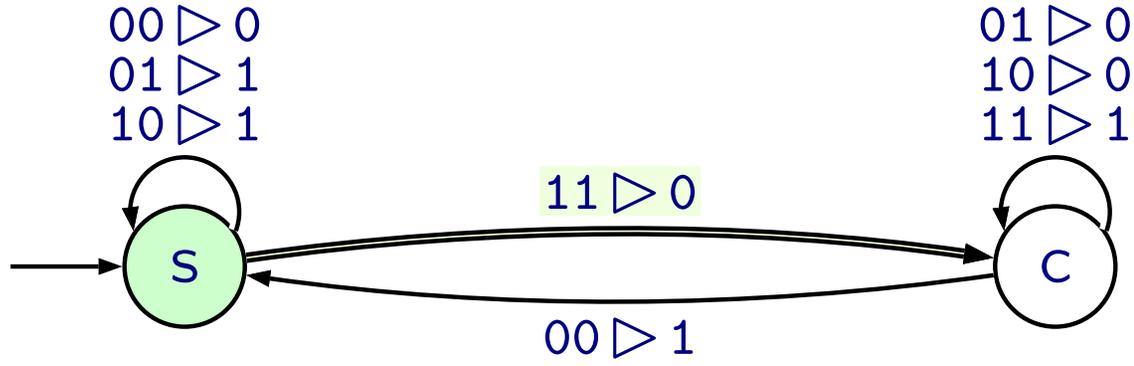


one bit serial adder

input tape



state transition graph



output tape

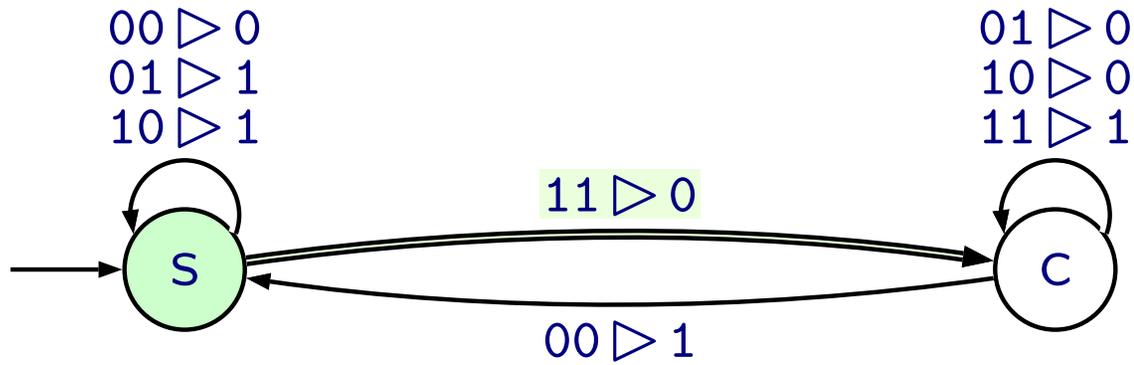


one bit serial adder

input tape



state transition graph



output tape

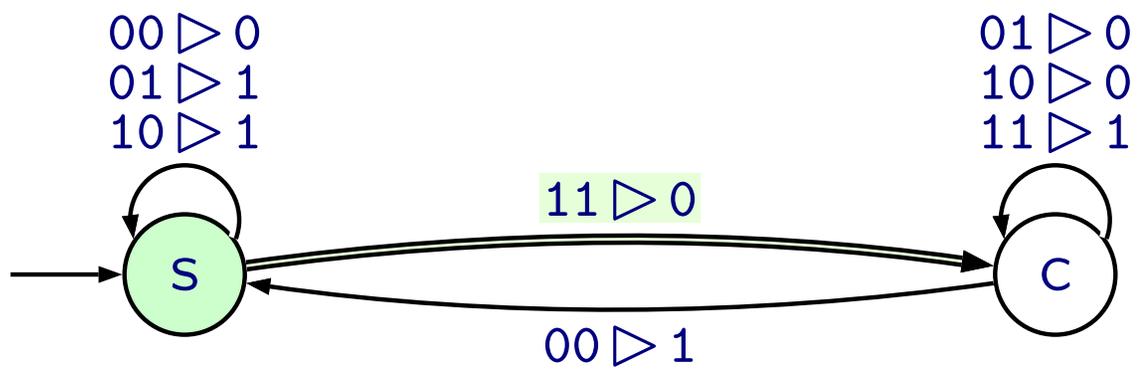


one bit serial adder

input tape



state transition graph



output tape

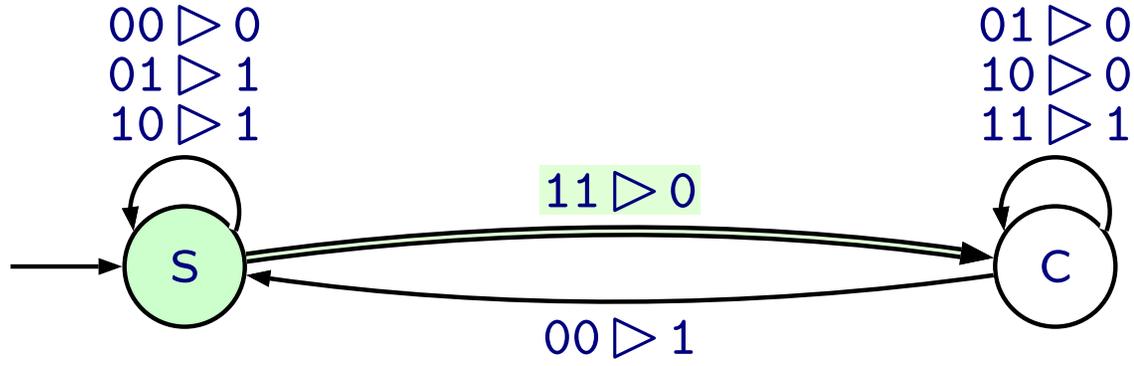


one bit serial adder

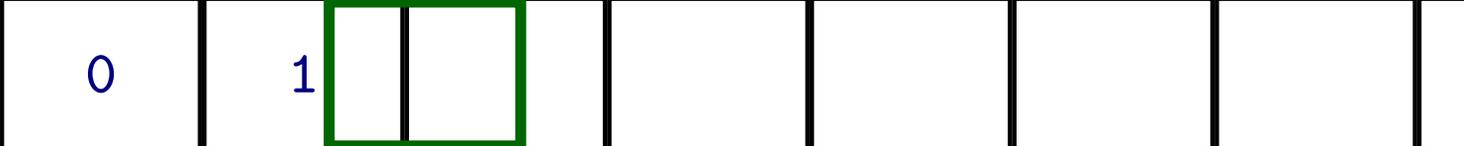
input tape



state transition graph

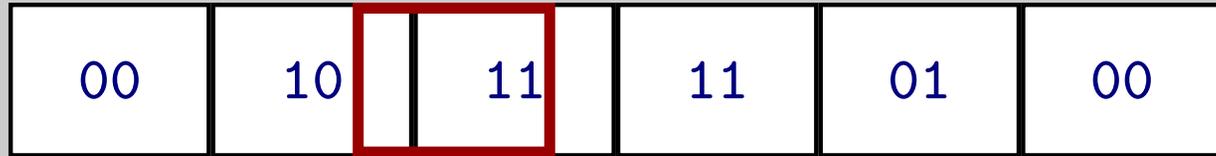


output tape

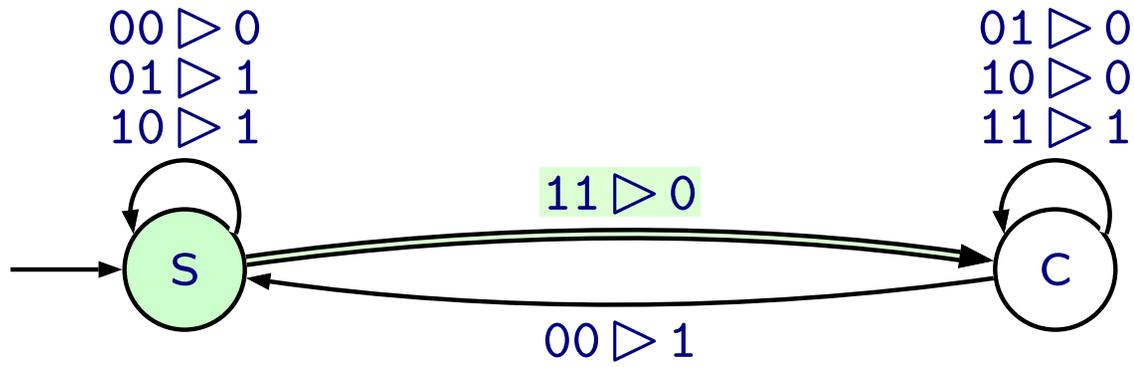


one bit serial adder

input tape



state transition graph

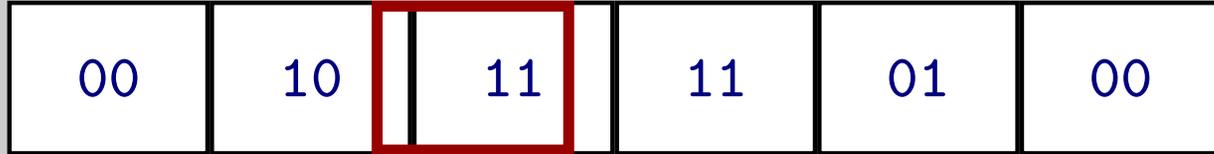


output tape

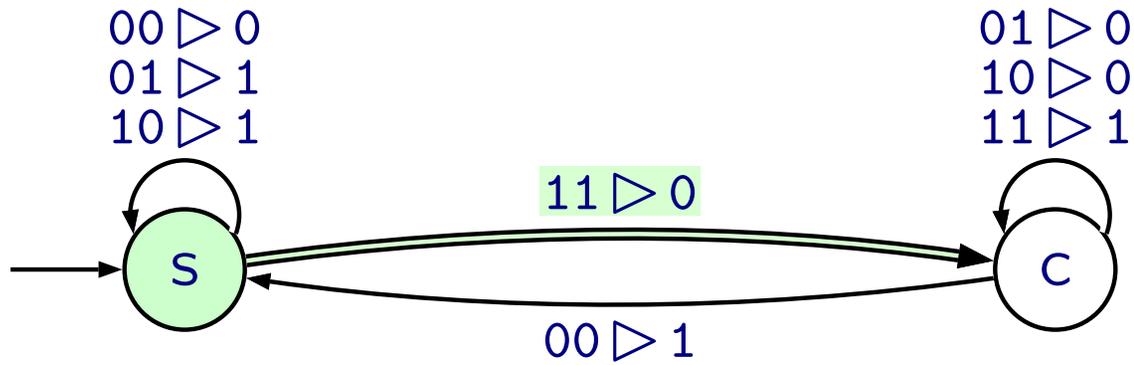


one bit serial adder

input tape



state transition graph

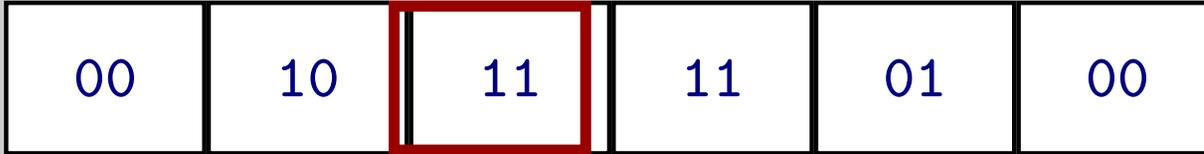


output tape

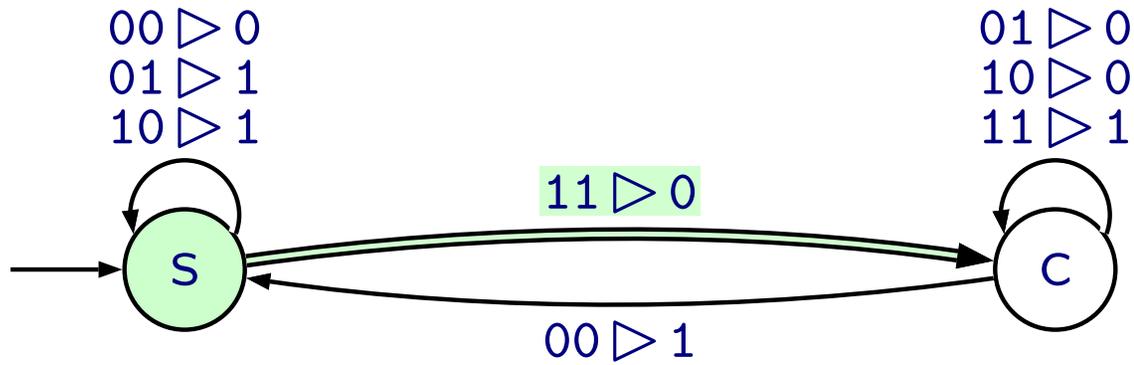


one bit serial adder

input tape



state transition graph



output tape



one bit serial adder

input tape

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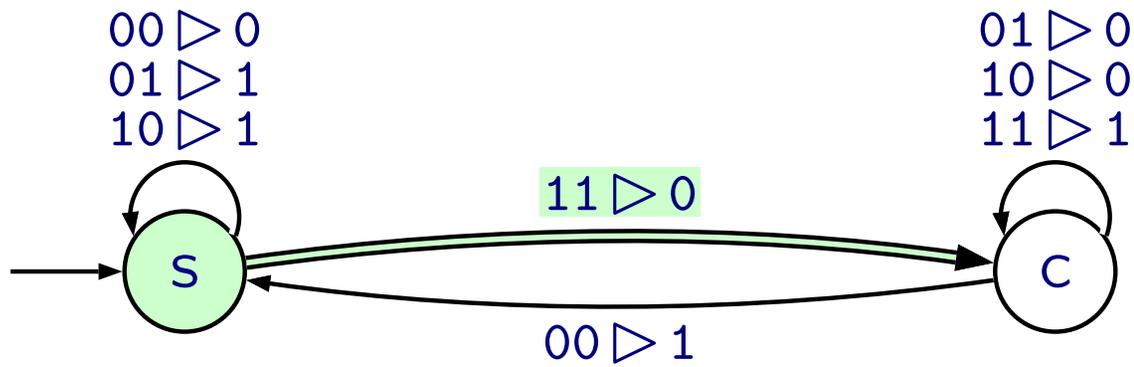
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state transition graph



output tape

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one bit serial adder

input tape

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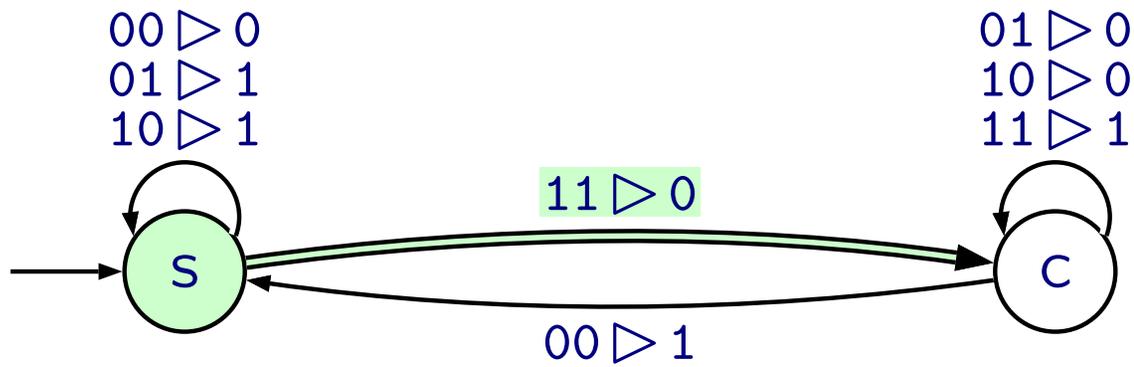
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state transition graph



output tape

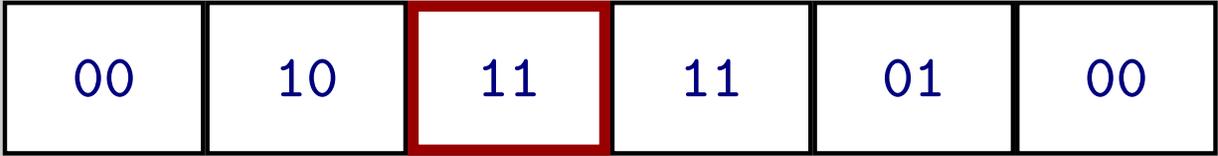
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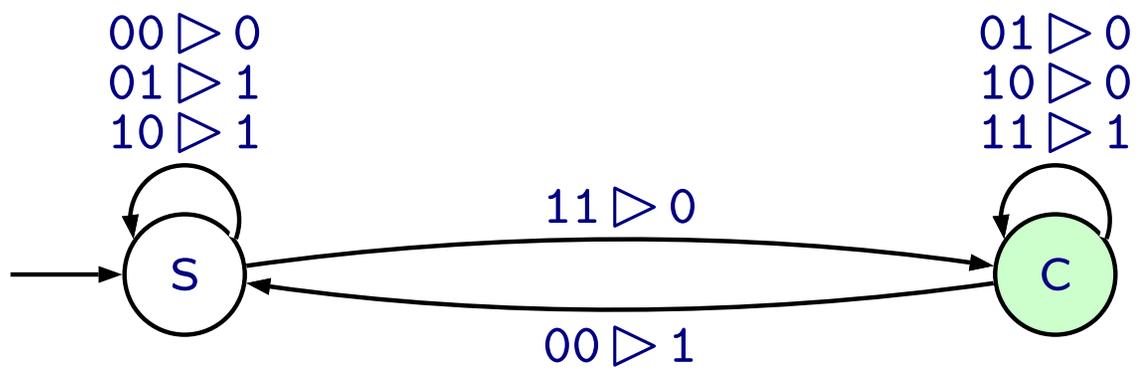
0

one bit serial adder

input tape



state transition graph



output tape

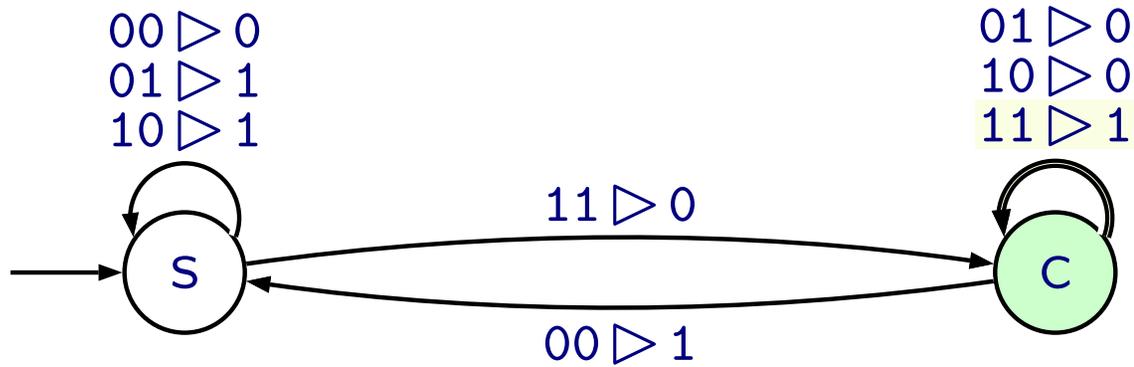


one bit serial adder

input tape



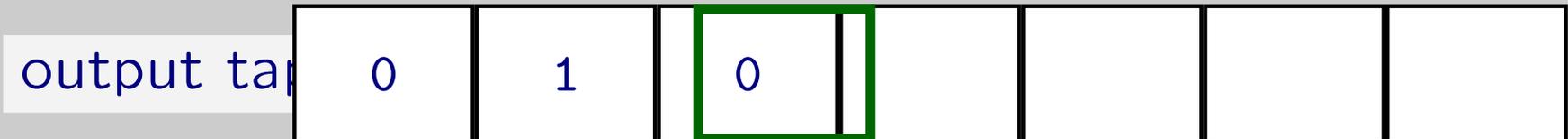
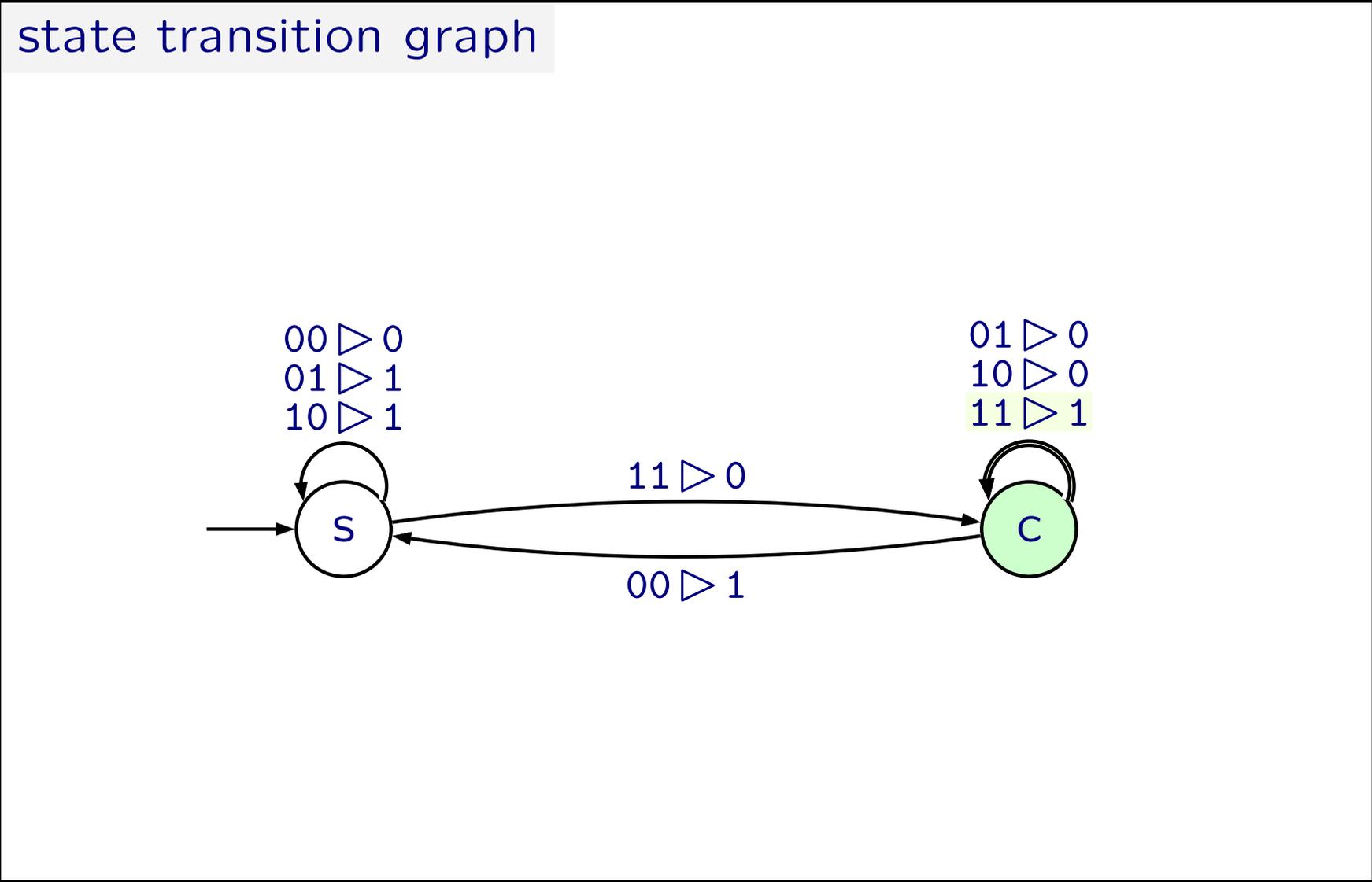
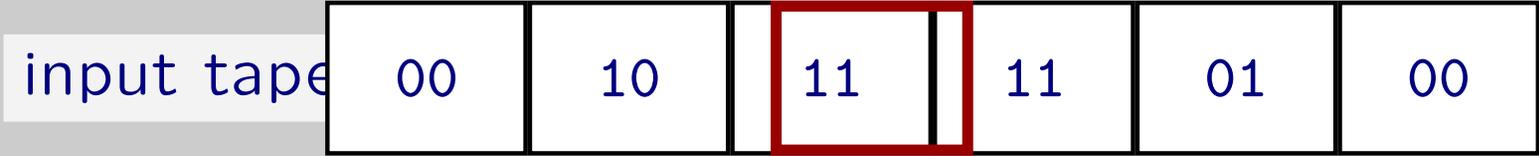
state transition graph



output tap

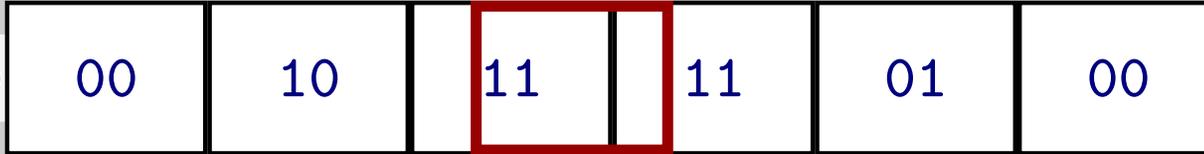


one bit serial adder

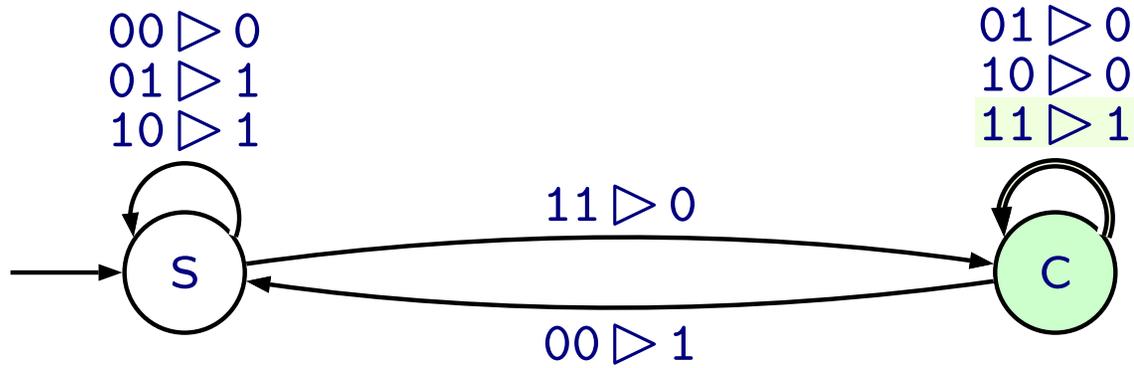


one bit serial adder

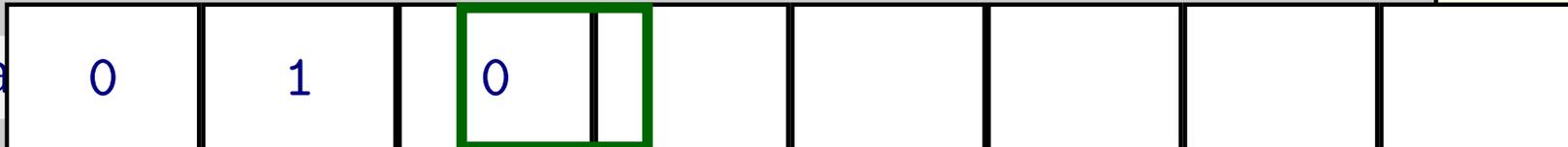
input tap



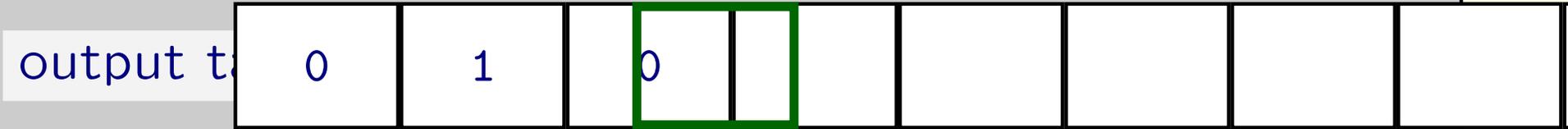
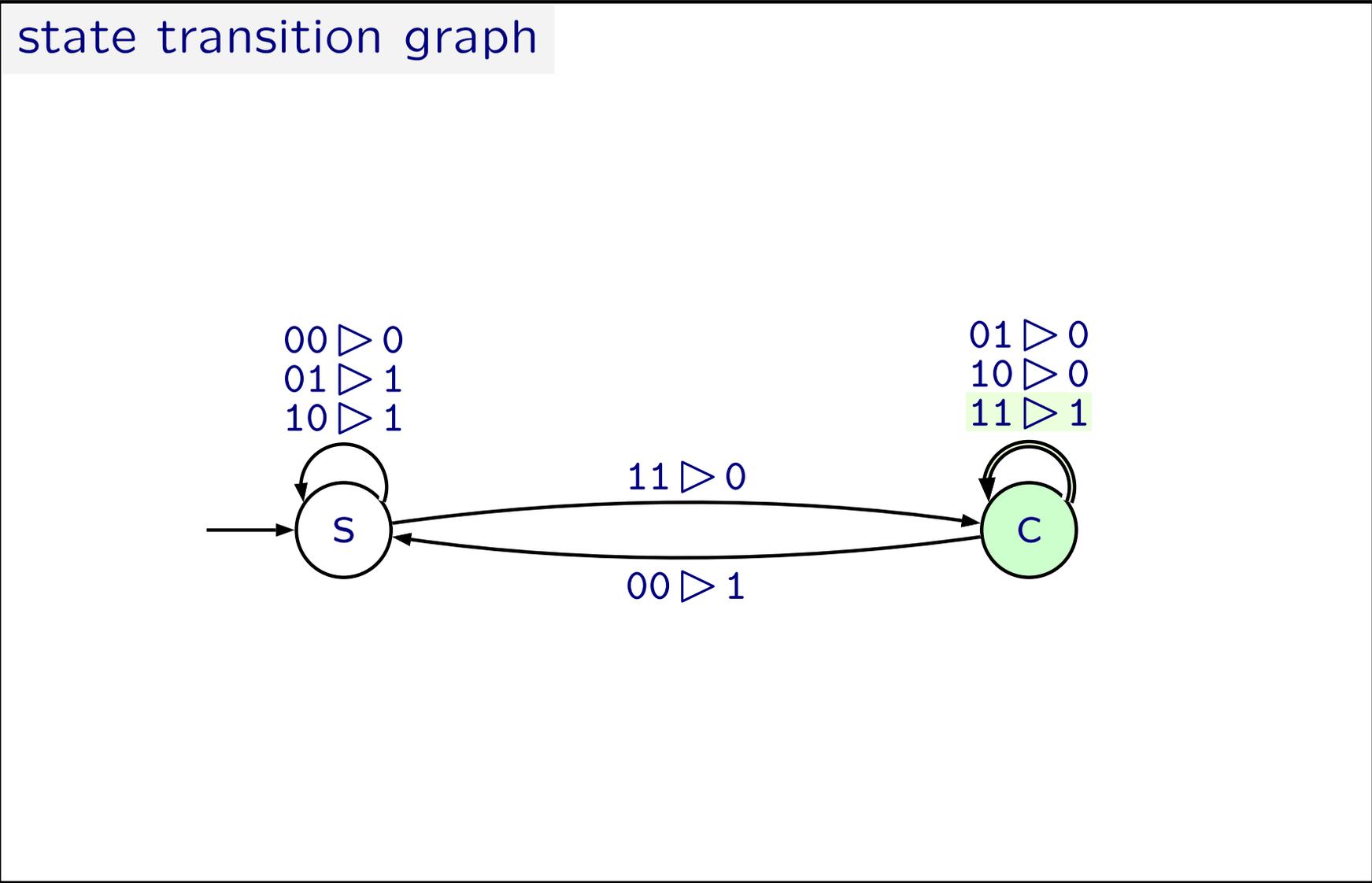
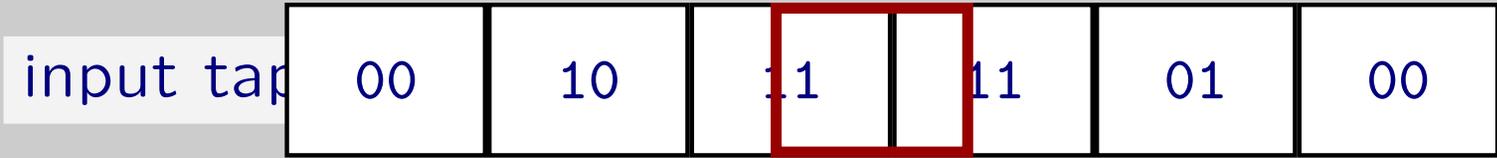
state transition graph



output tap

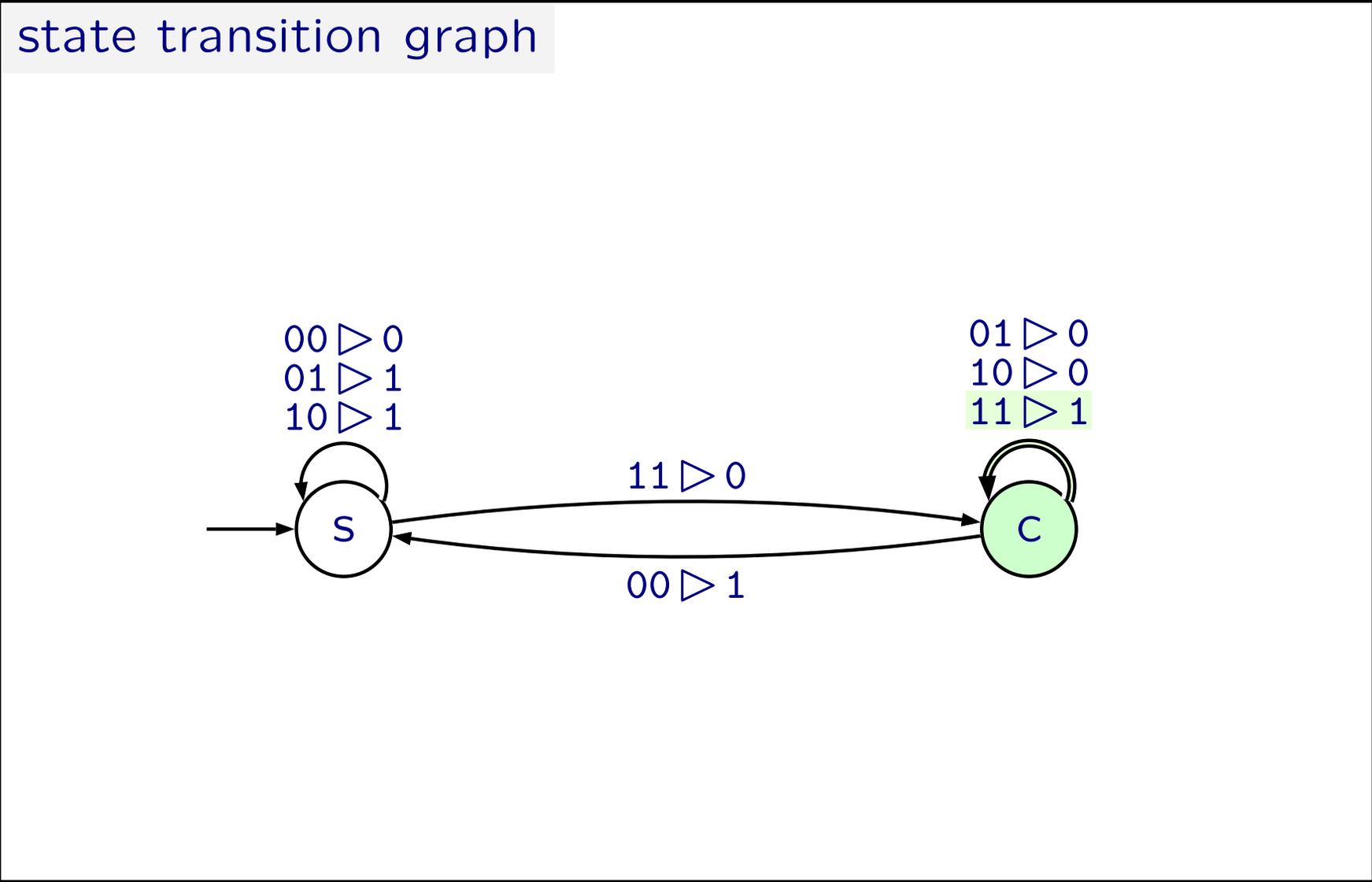


one bit serial adder



one bit serial adder

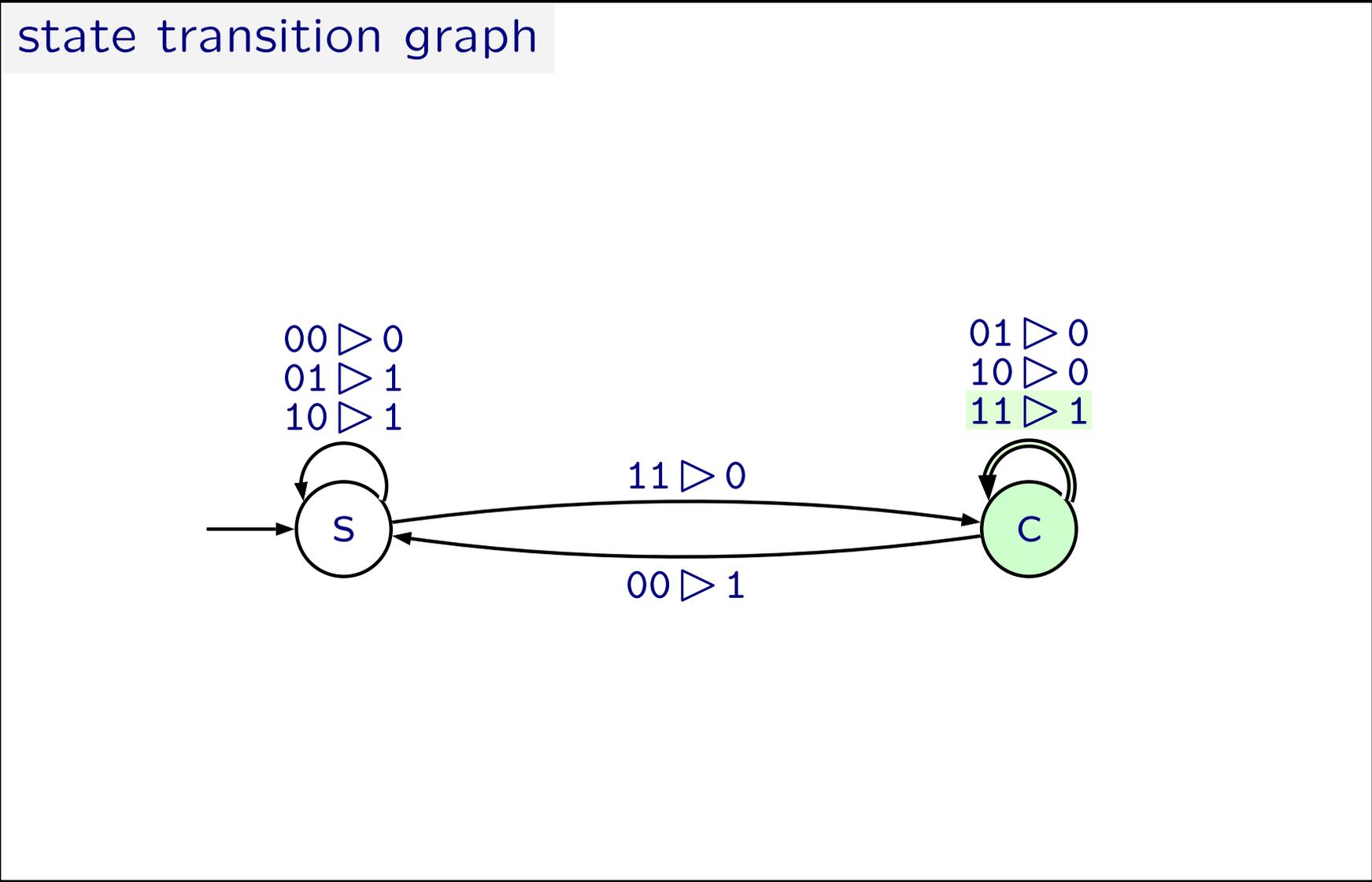
input ta 00 10 11 11 01 00



output t 0 1 0 1

one bit serial adder

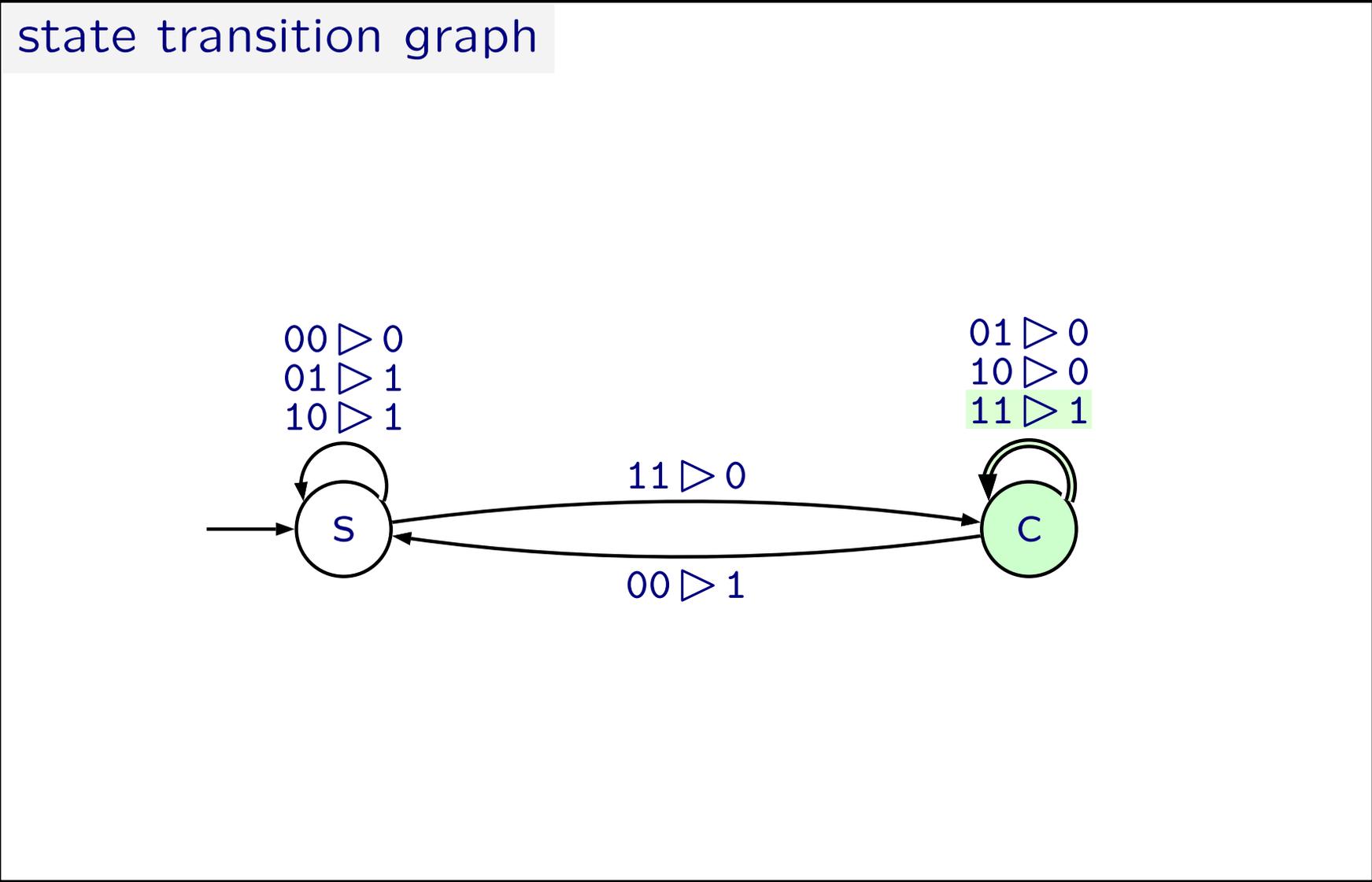
input ta 00 10 11 11 01 00



output 0 1 0 0 0 0 0 0 0 0 0 0

one bit serial adder

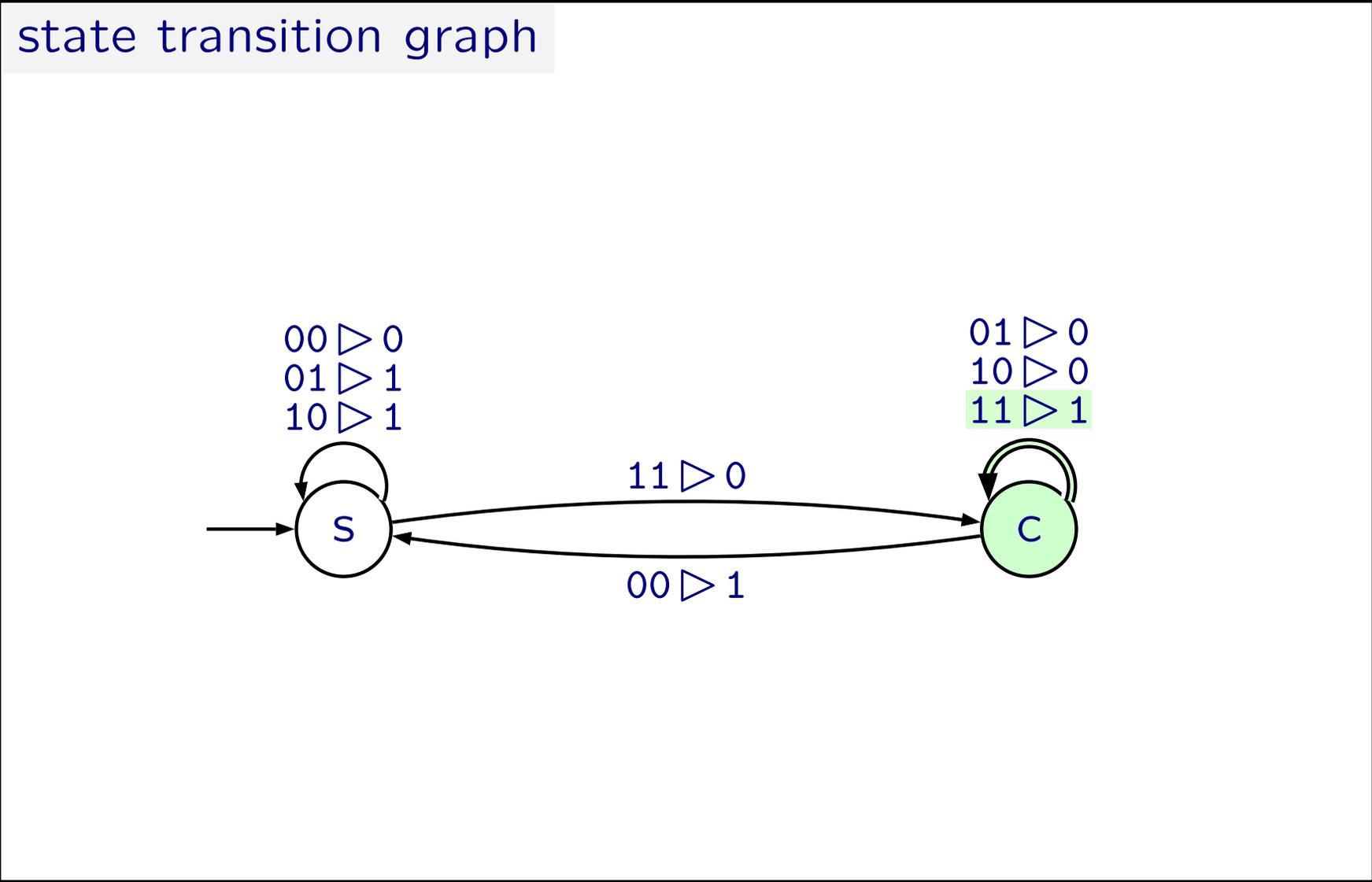
input t 00 10 11 11 01 00



output 0 1 0 1 1 1 1 1 1 1 1 1

one bit serial adder

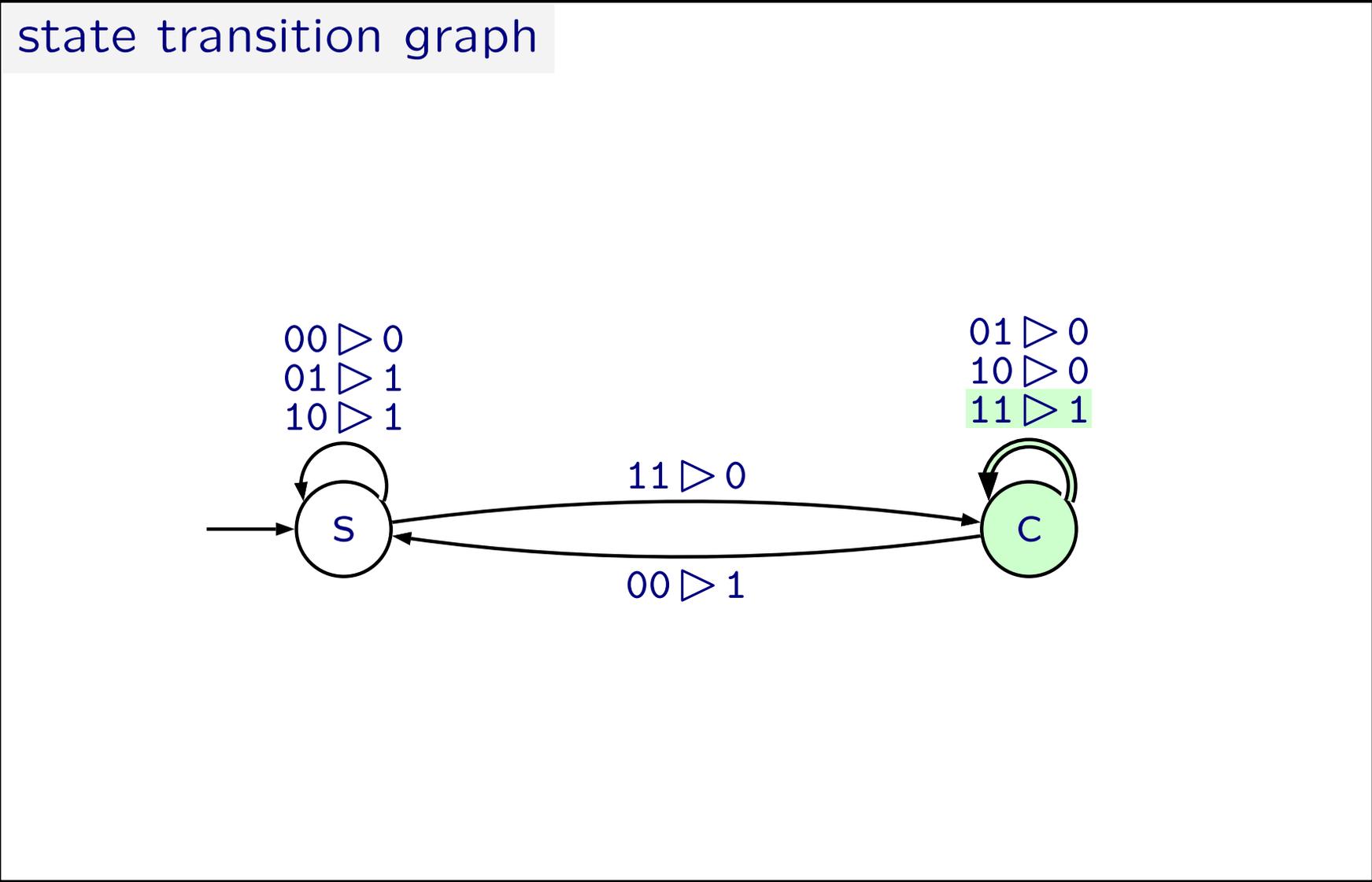
input 00 10 11 11 01 00



output 0 1 0 1

one bit serial adder

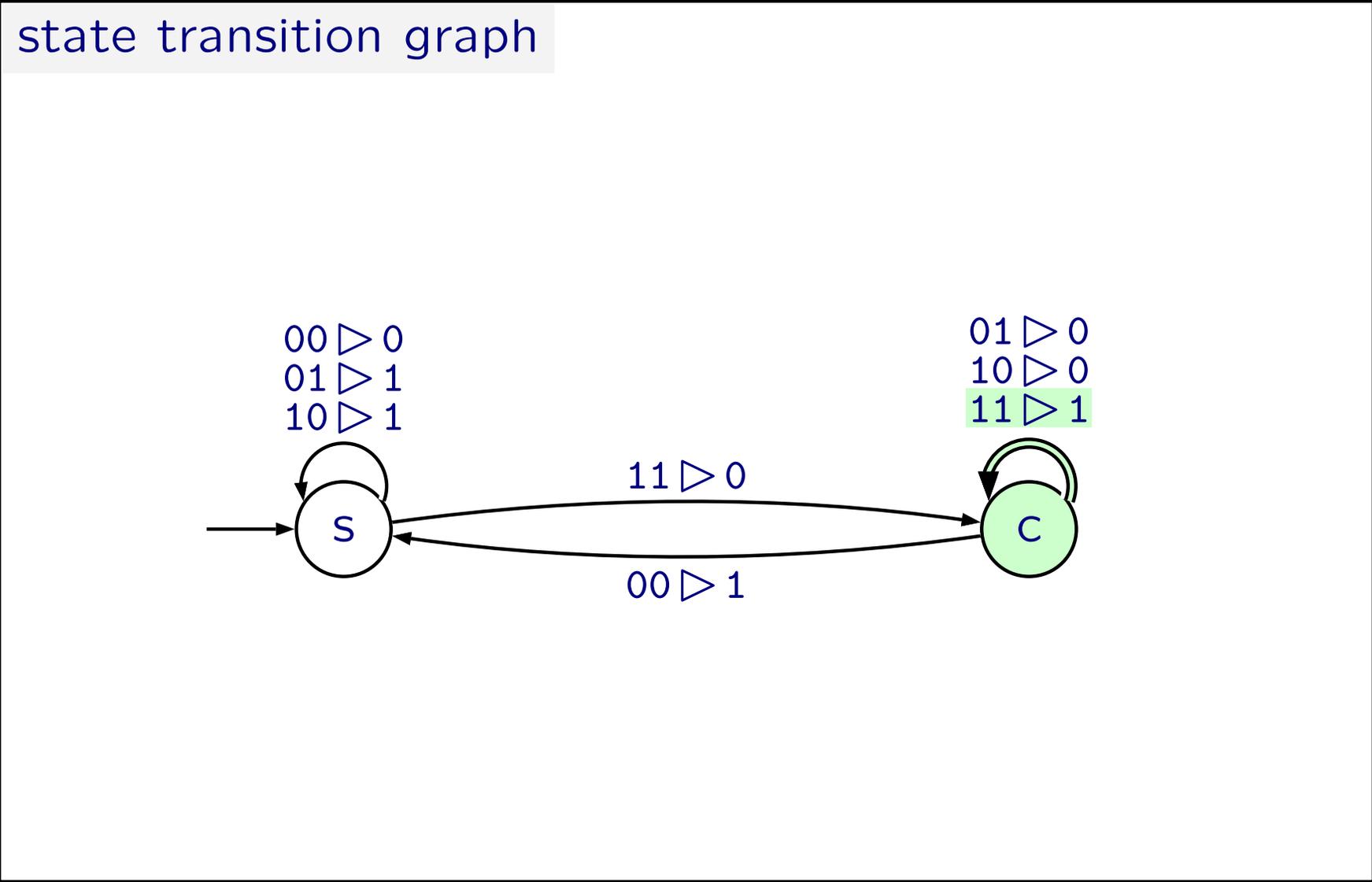
input 00 10 11 11 01 00



output 0 1 0

one bit serial adder

input 00 10 11 11 01 00



outp 0 1 0

one bit serial adder

input

00

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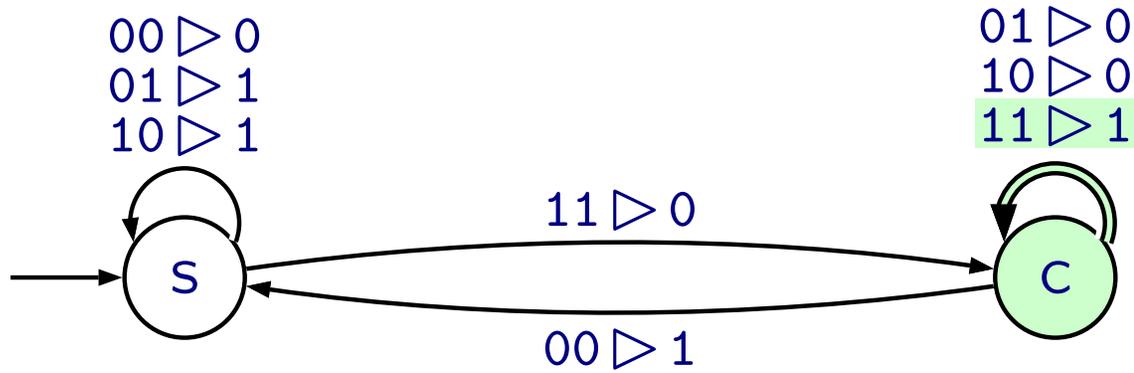
11

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01

00

state transition graph



output

0

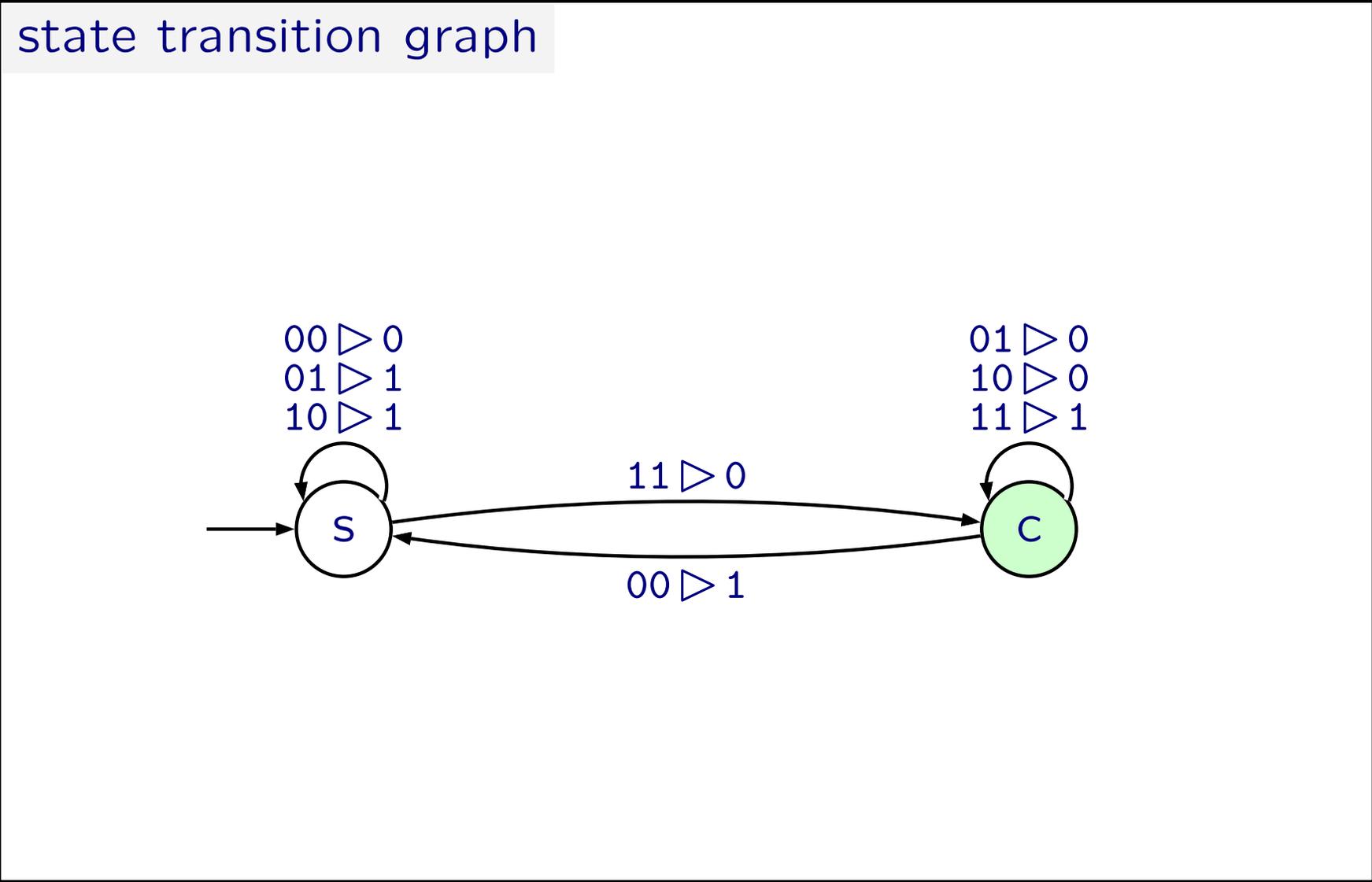
1

0

1

one bit serial adder

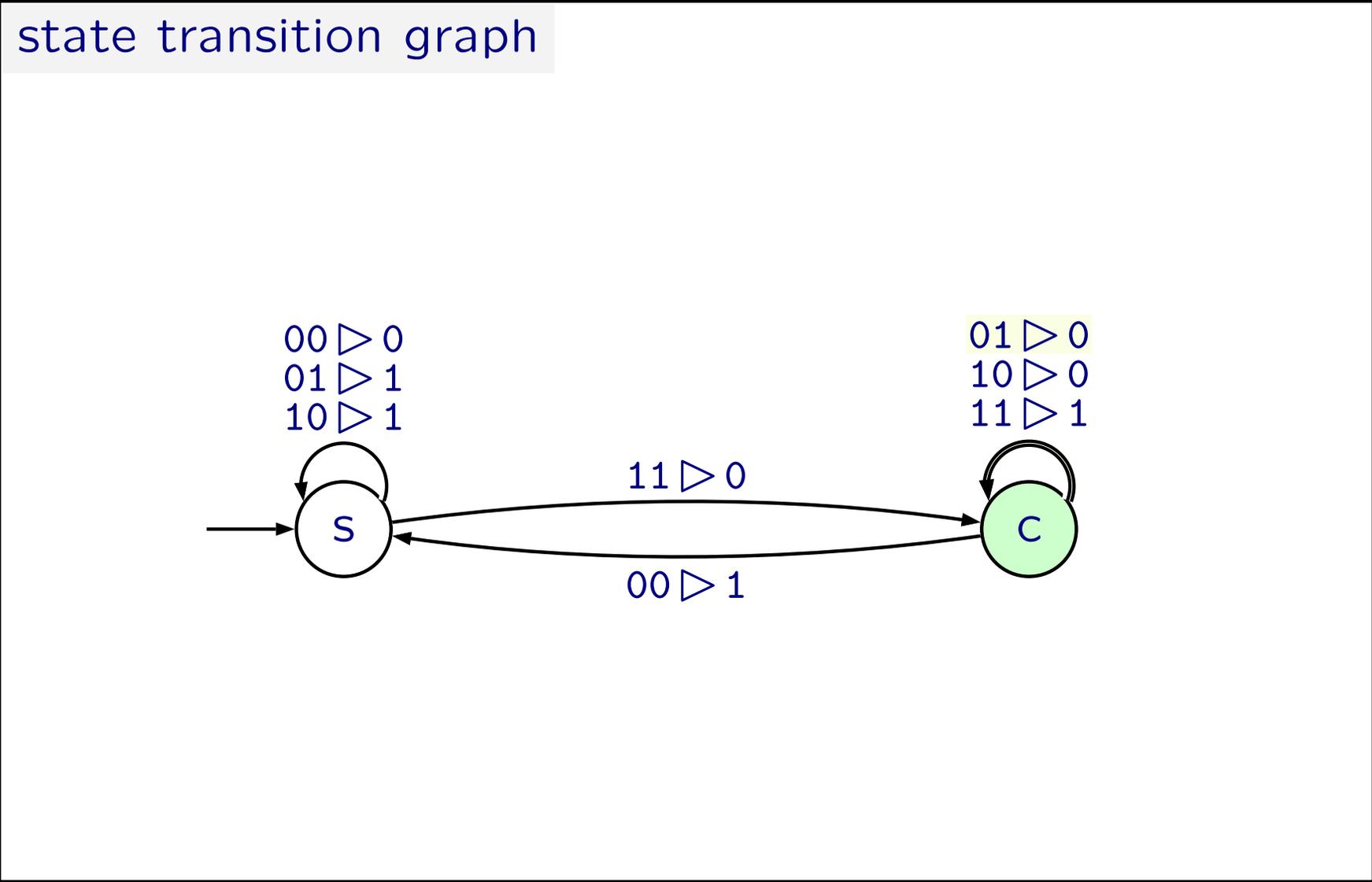
input 00 10 11 11 01 00



outp 0 1 0 1

one bit serial adder

input 00 10 11 11 01 00



output 0 1 0 1

one bit serial adder

input

00

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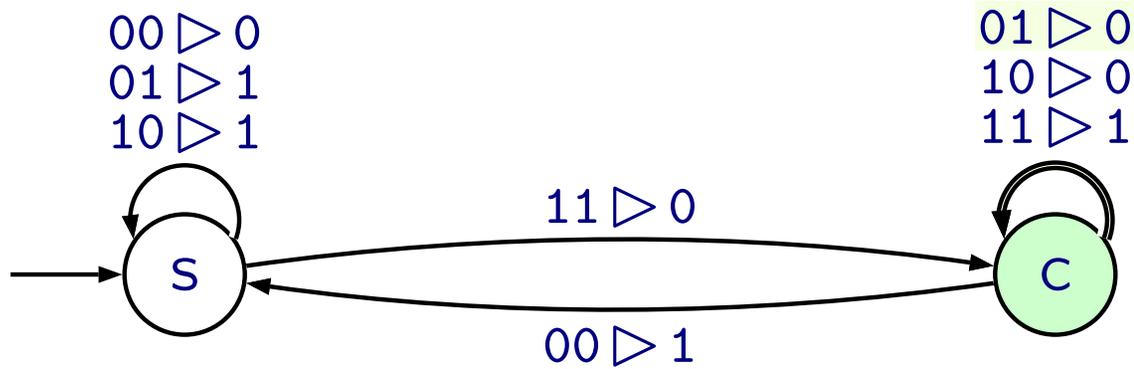
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state transition graph



output

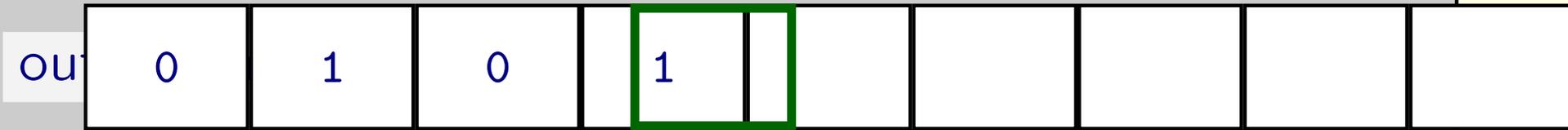
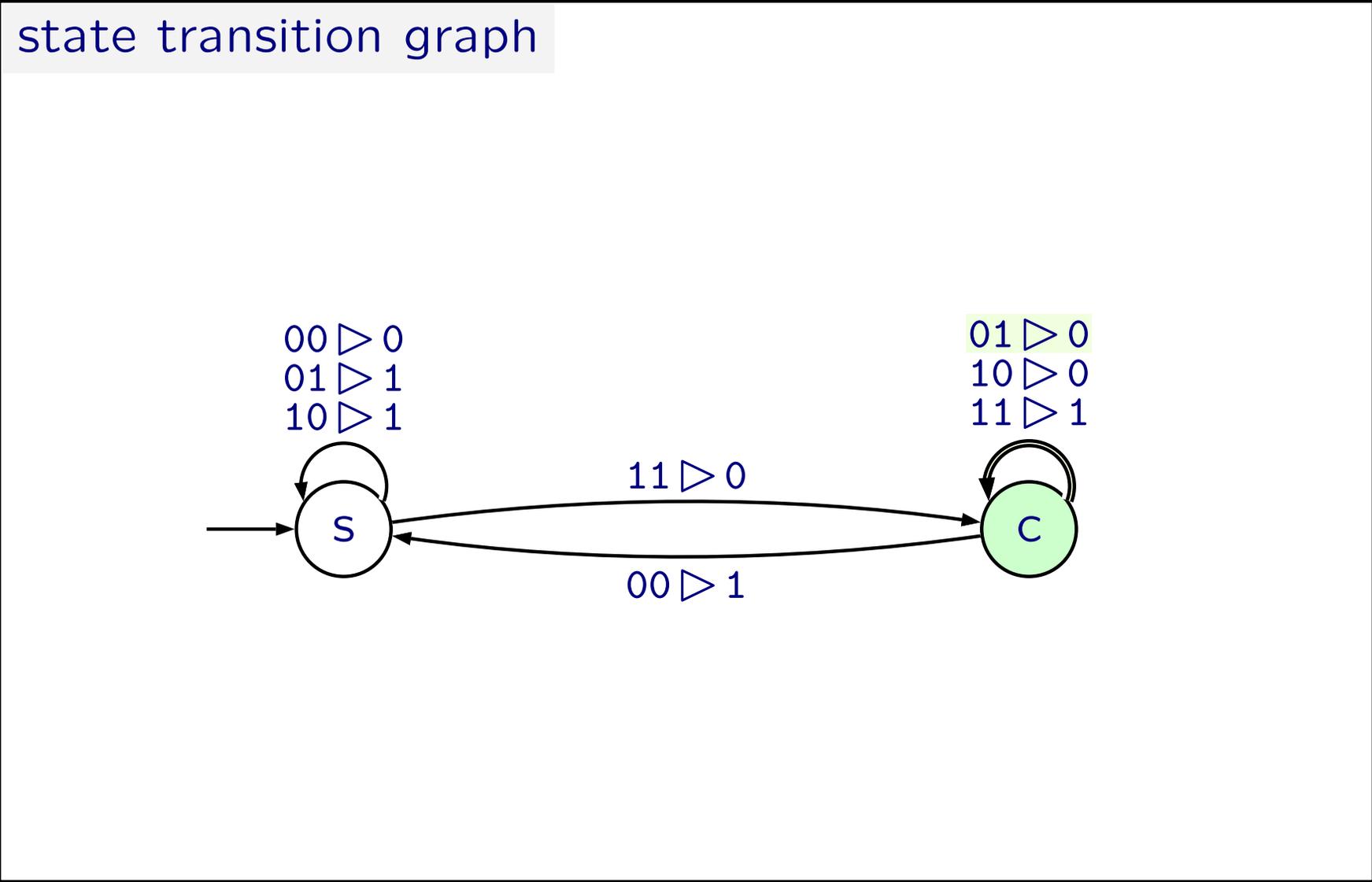
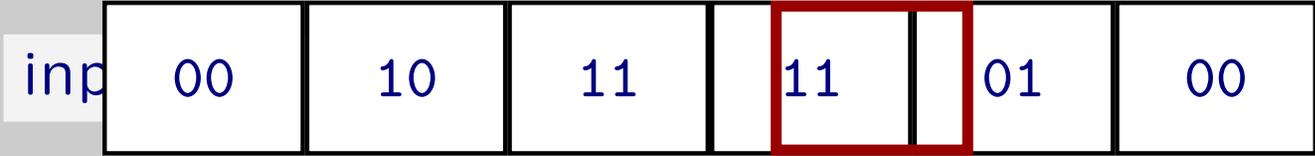
0

1

0

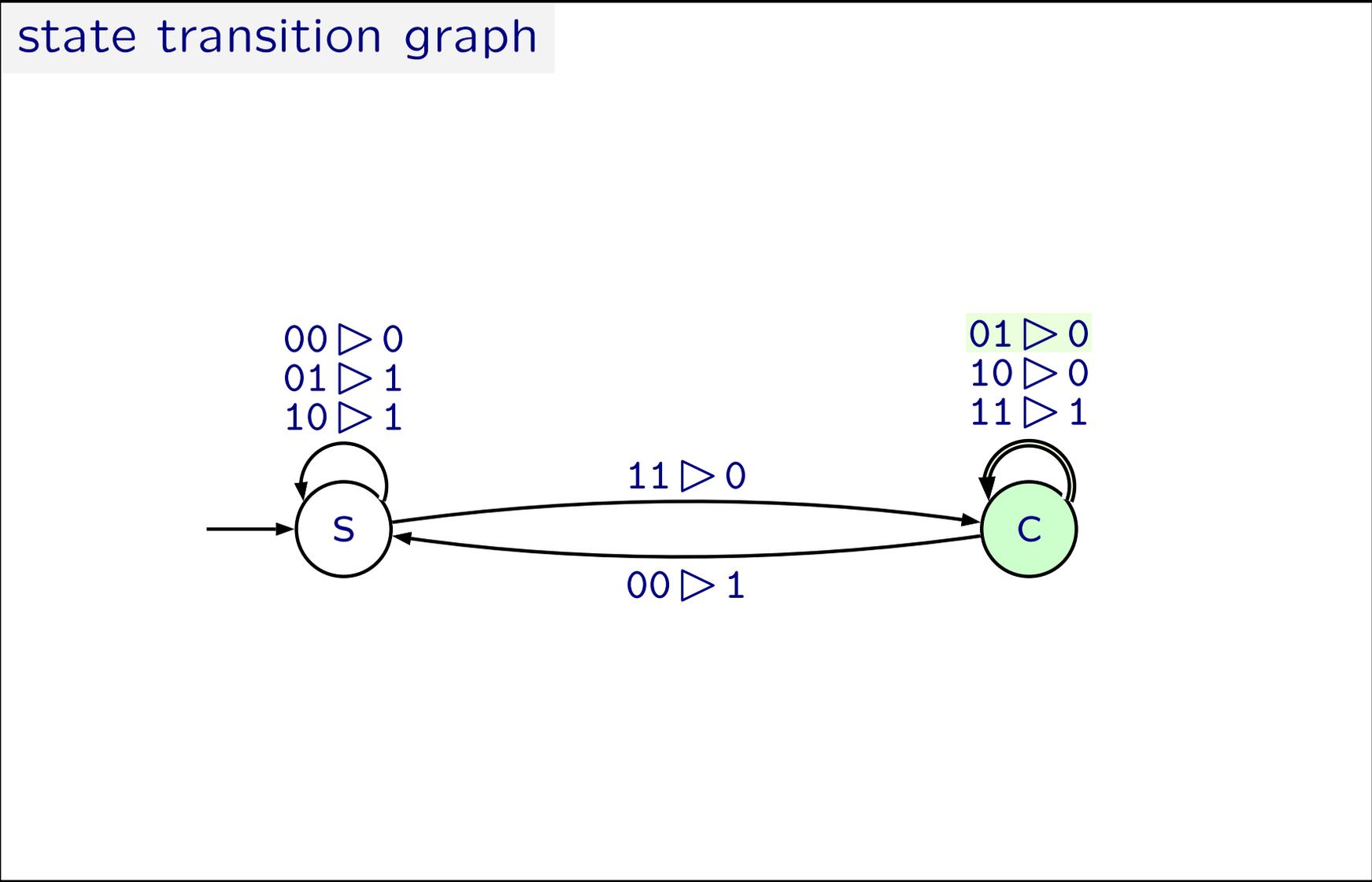
1

one bit serial adder



one bit serial adder

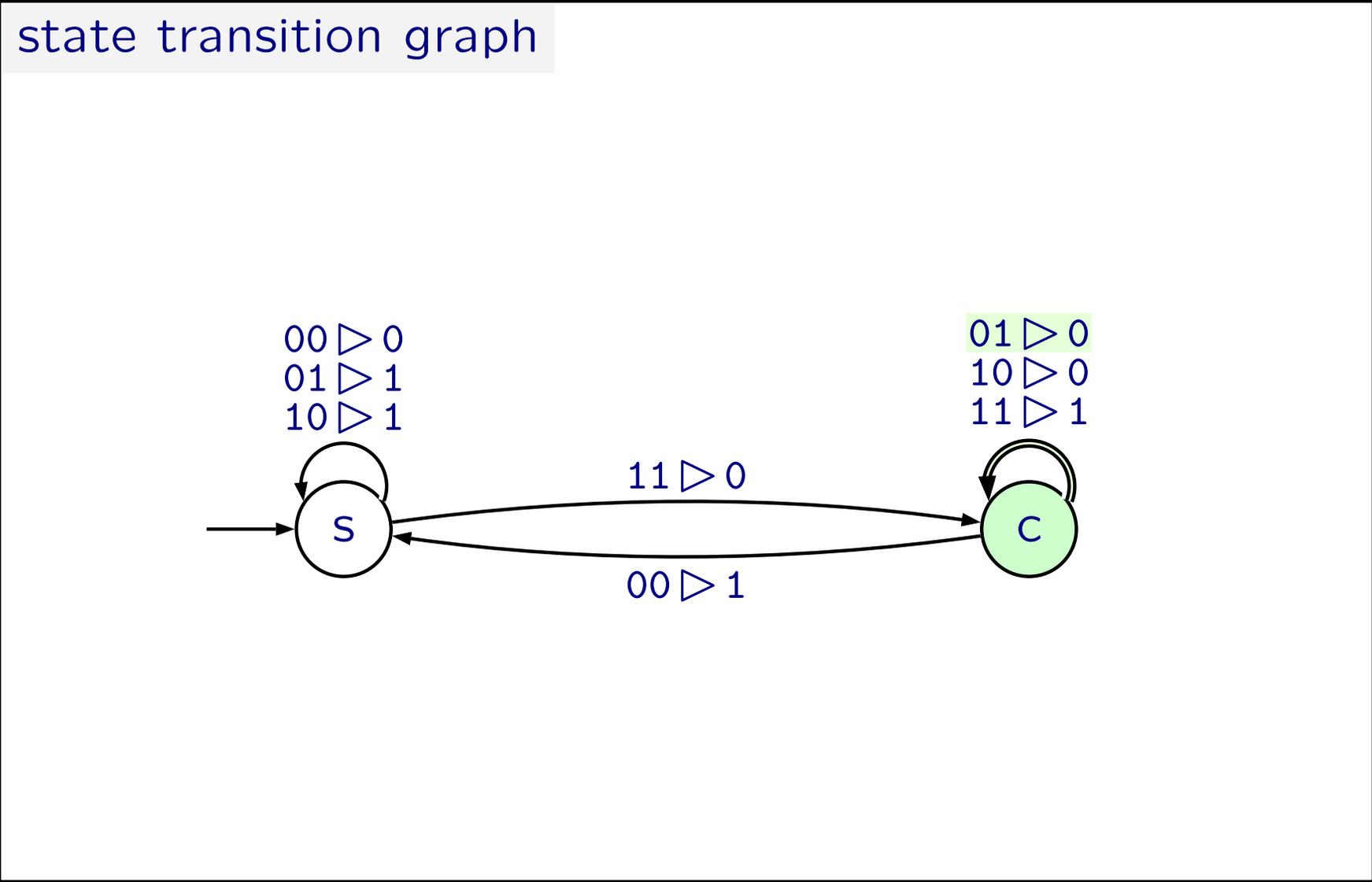
in 00 10 11 11 01 00



ou 0 1 0 1

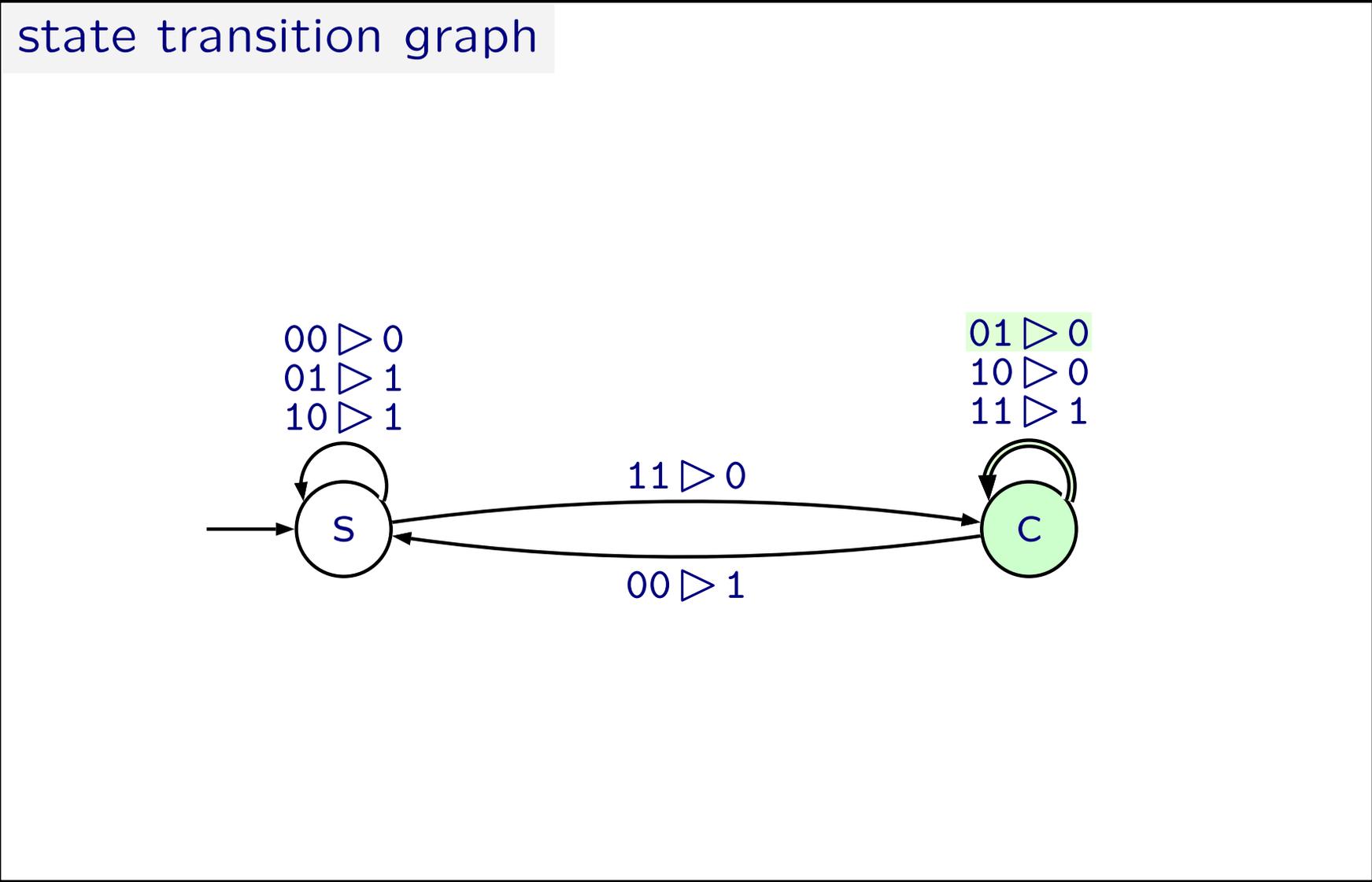
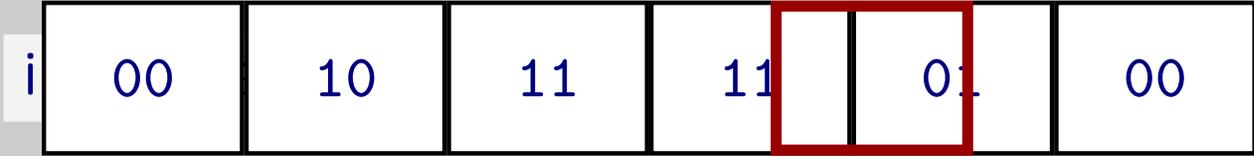
one bit serial adder

in 00 10 11 11 01 00



o 0 1 0 1

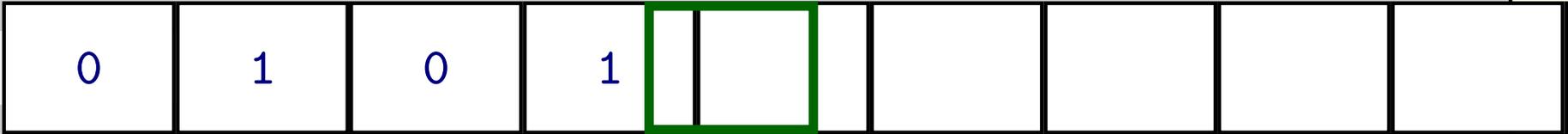
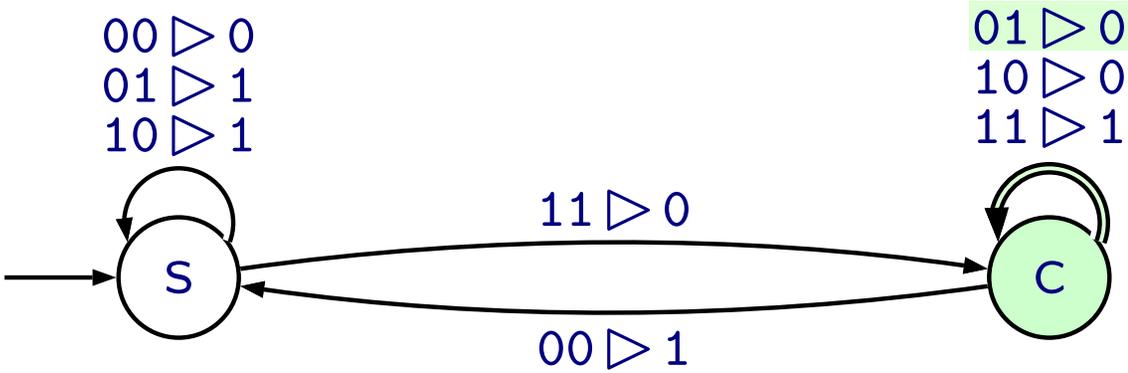
one bit serial adder



one bit serial adder



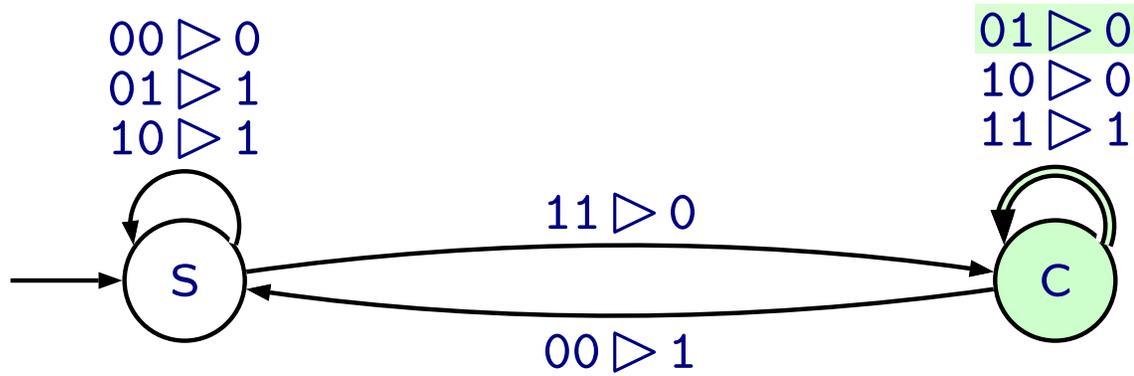
state transition graph



one bit serial adder

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|----|----|----|----|----|----|
| 00 | 10 | 11 | 11 | 01 | 00 |
|----|----|----|----|----|----|

state transition graph

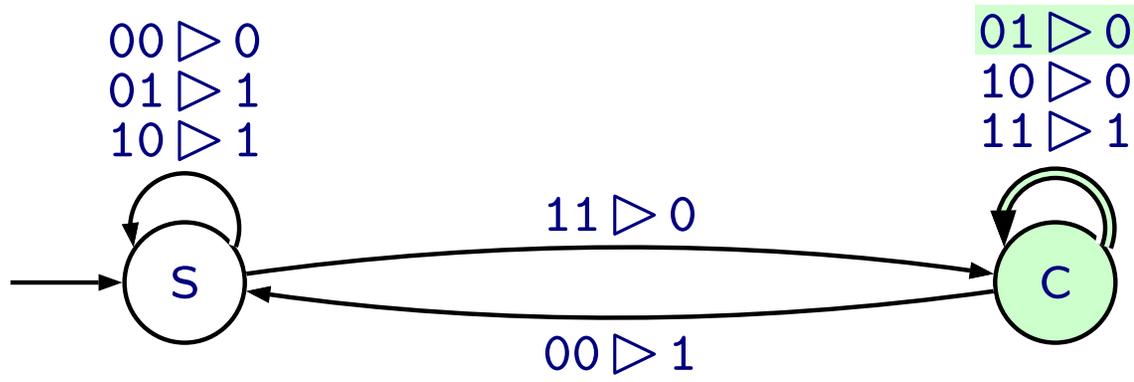


| | | | | | | | | | |
|---|---|---|---|--|--|--|--|--|--|
| 0 | 1 | 0 | 1 | | | | | | |
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one bit serial adder



state transition graph



one bit serial adder

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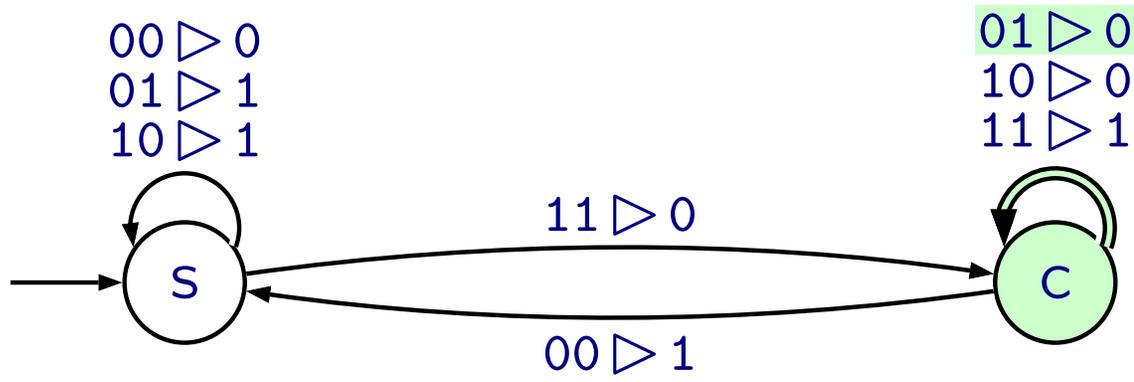
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state transition graph



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one bit serial adder

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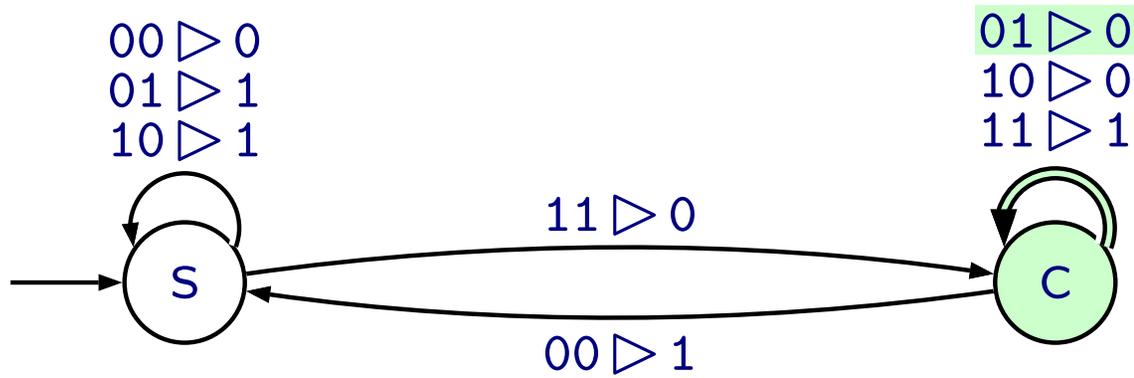
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state transition graph



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one bit serial adder

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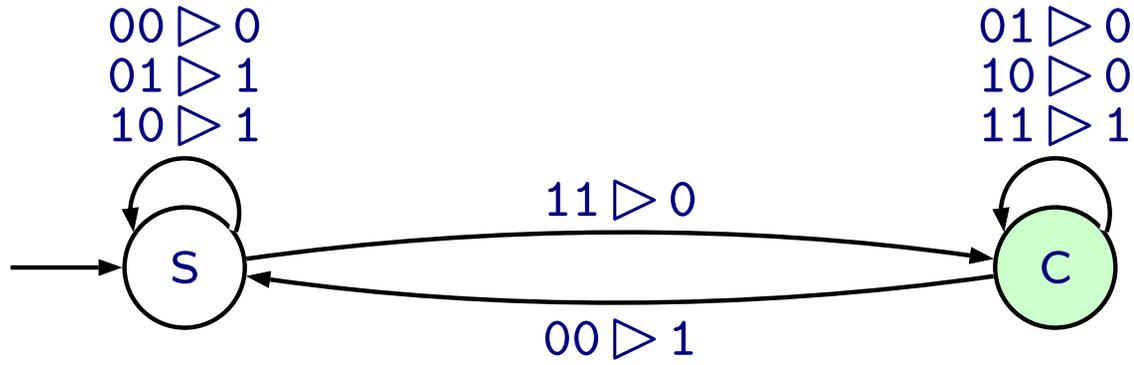
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state transition graph



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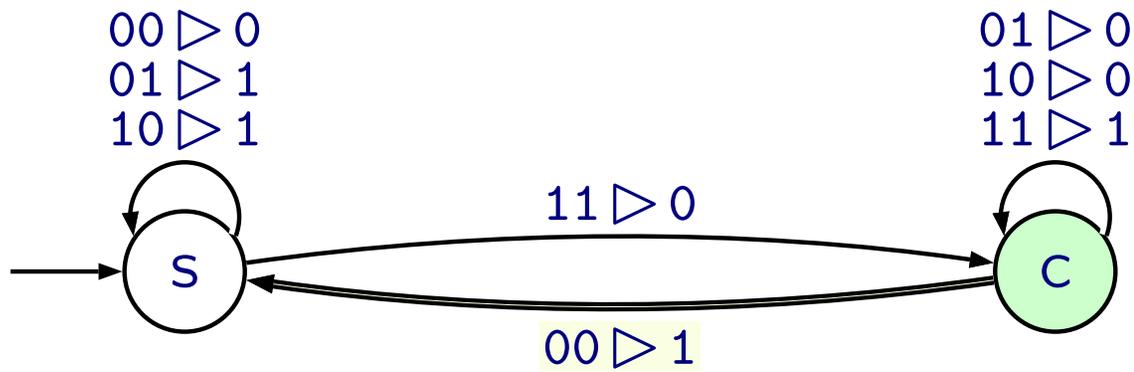
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state transition graph



one bit serial adder

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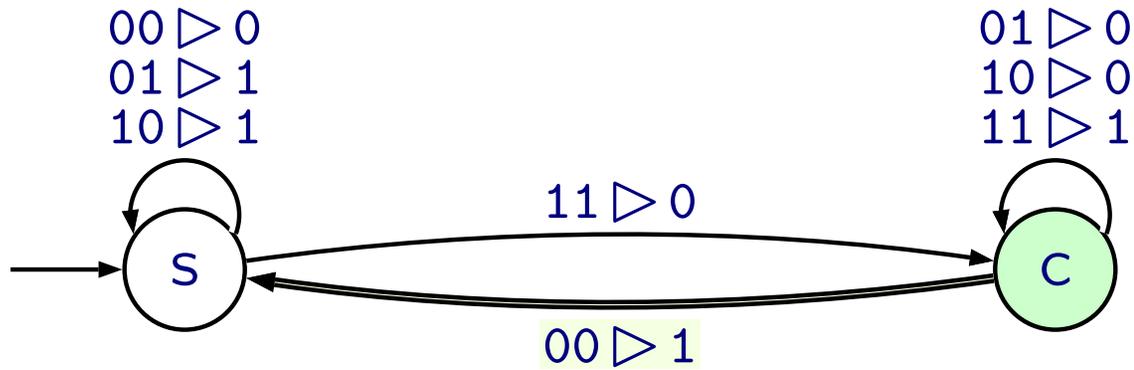
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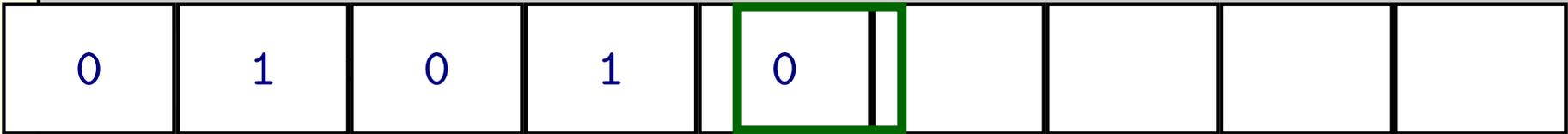
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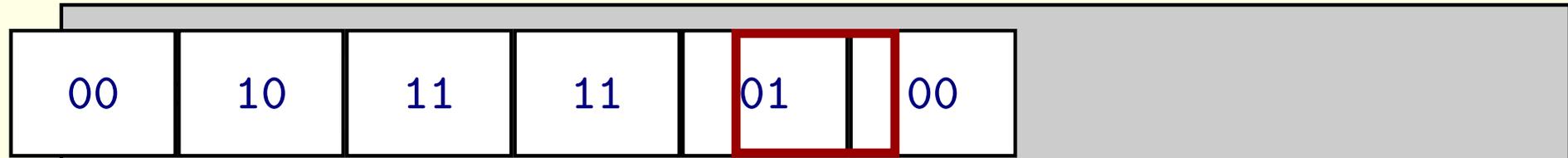


state transition graph

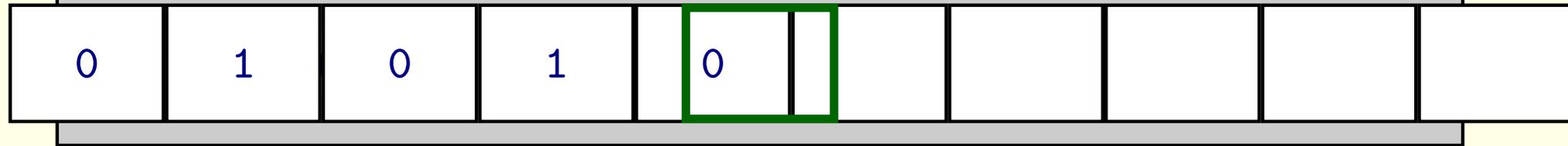
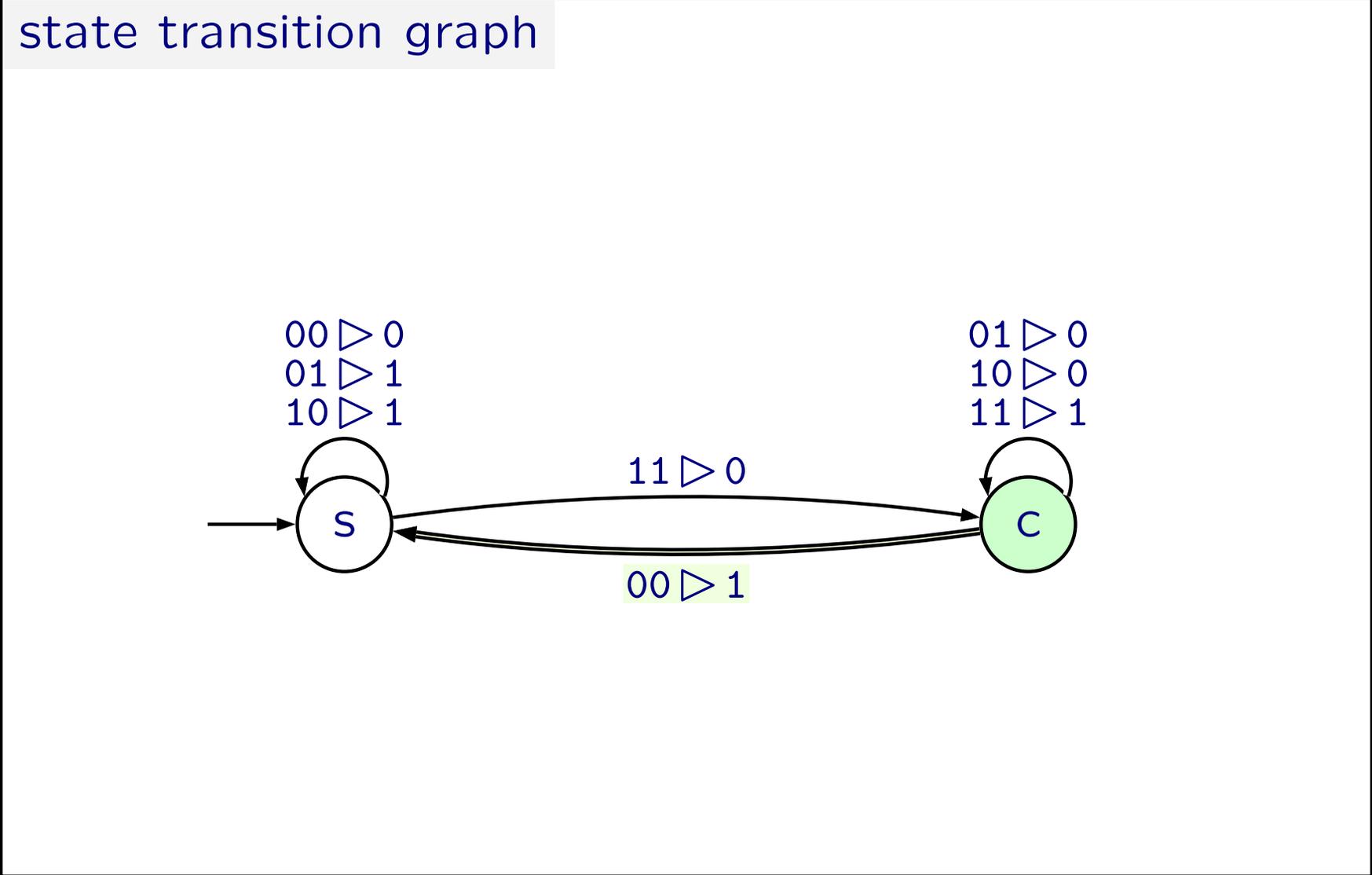


one bit serial adder



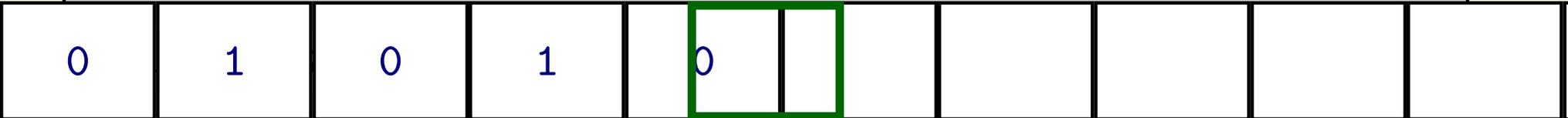
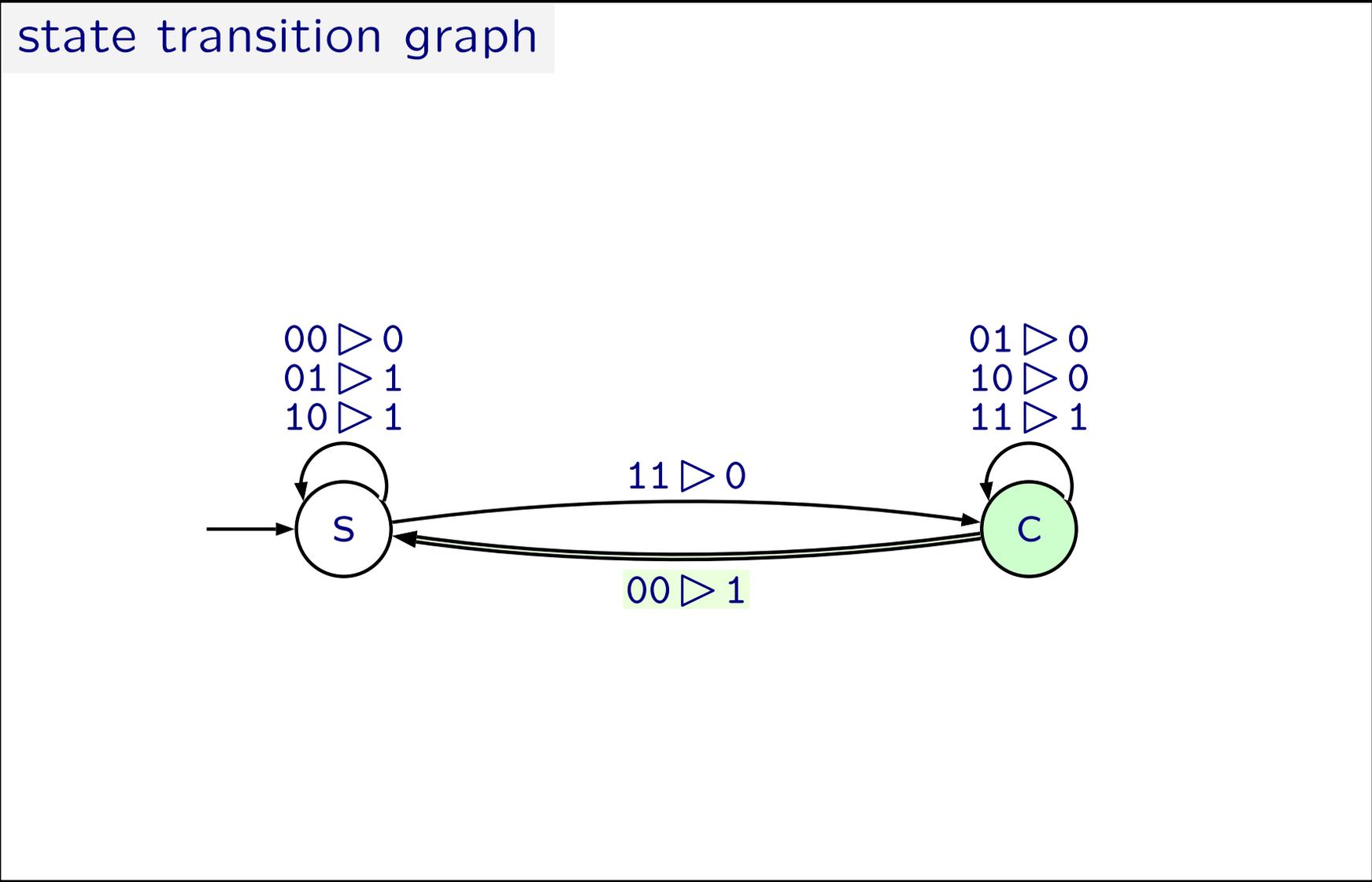


one bit serial adder



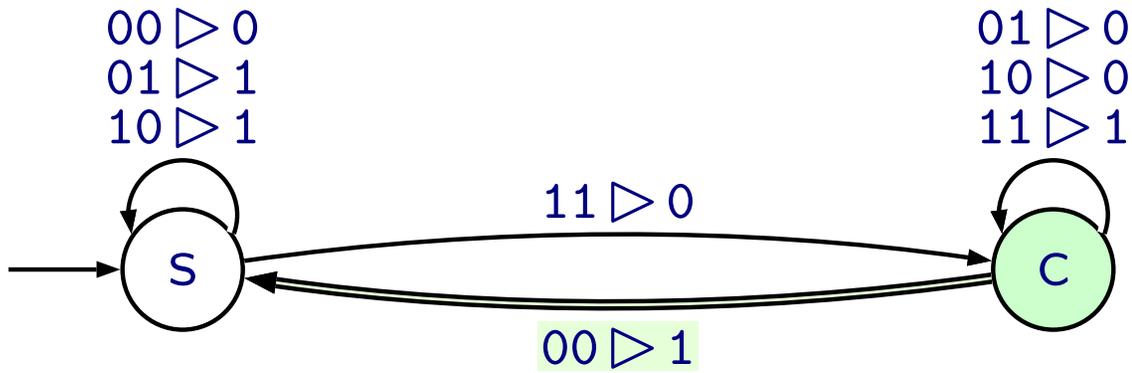


one bit serial adder



00 10 11 11 01 00

state transition graph



one bit serial adder

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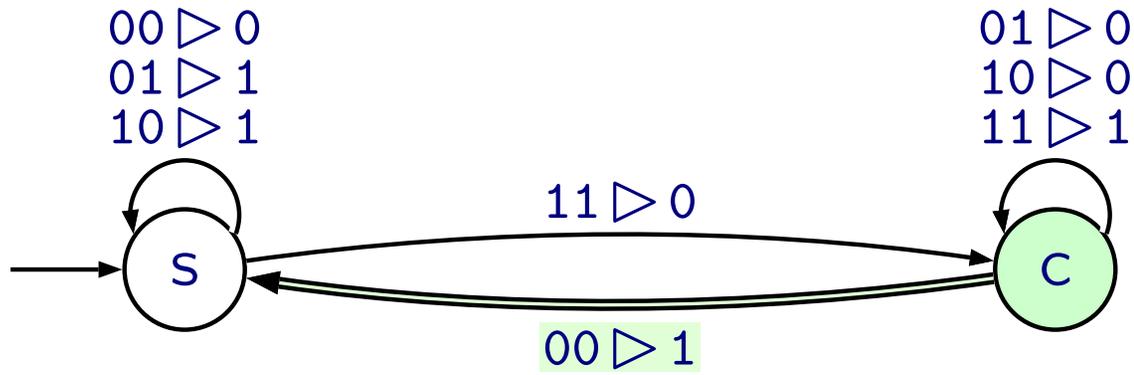
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state transition graph



one bit serial adder

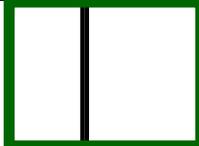
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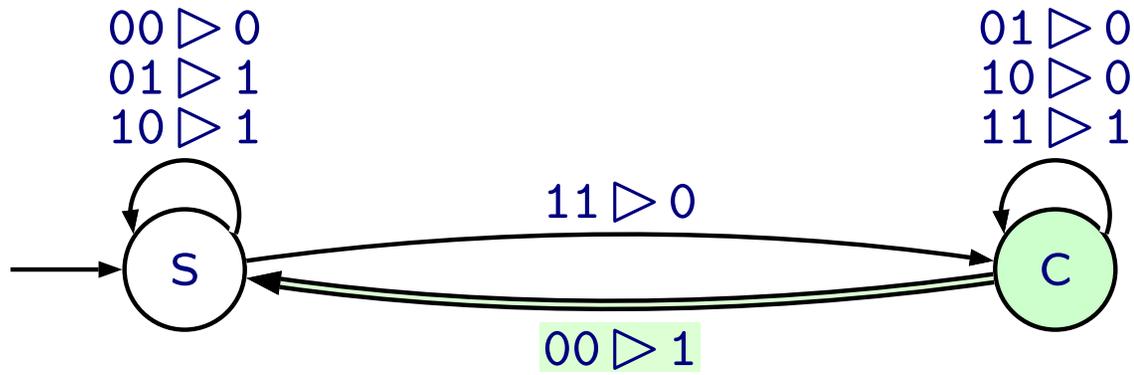
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state transition graph



one bit serial adder

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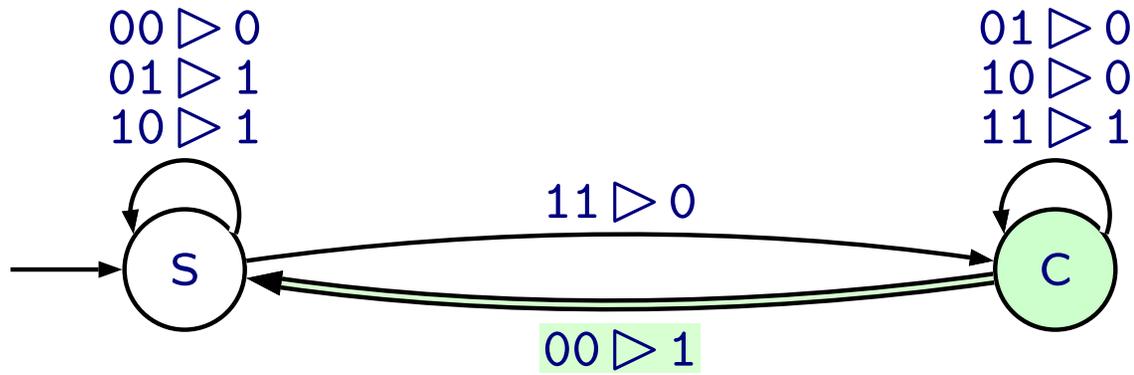
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one bit serial adder

state transition graph



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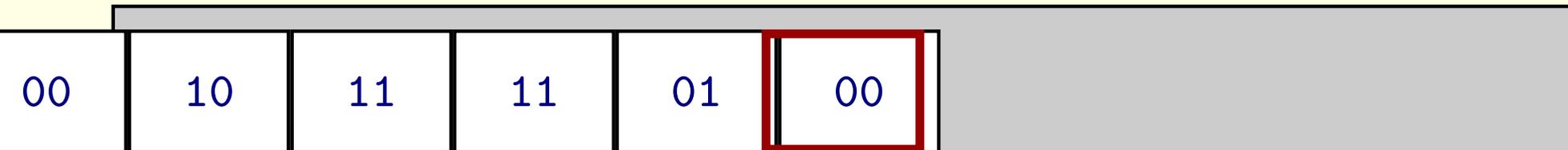
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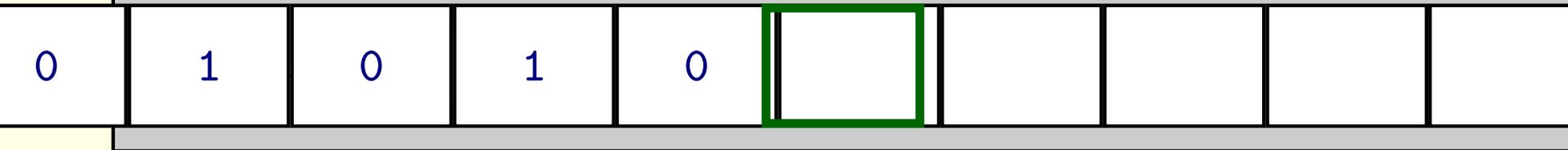
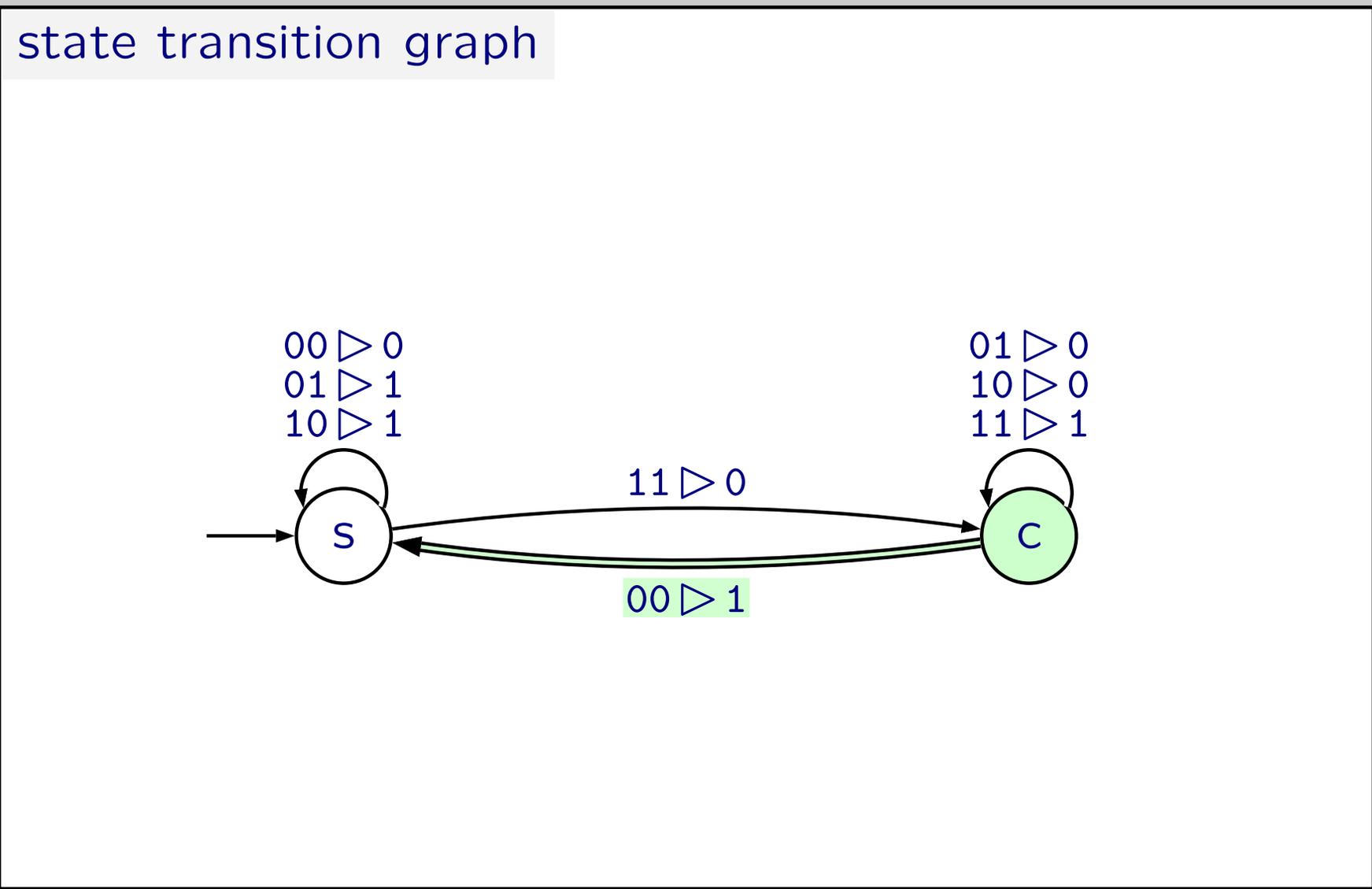
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one bit serial adder



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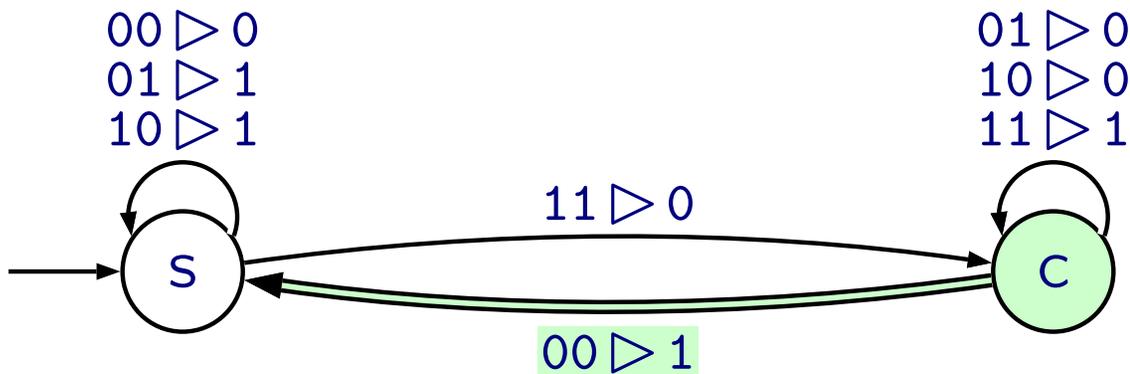
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one bit serial adder

state transition graph



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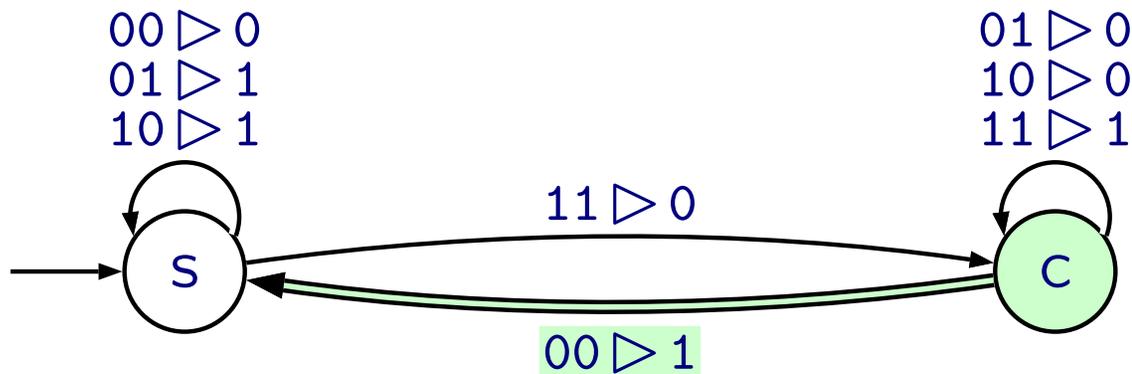
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one bit serial adder

state transition graph



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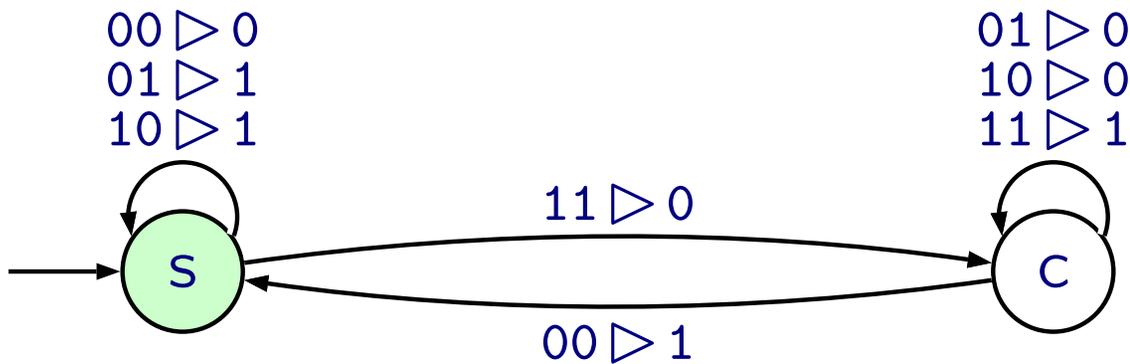
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one bit serial adder

state transition graph



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